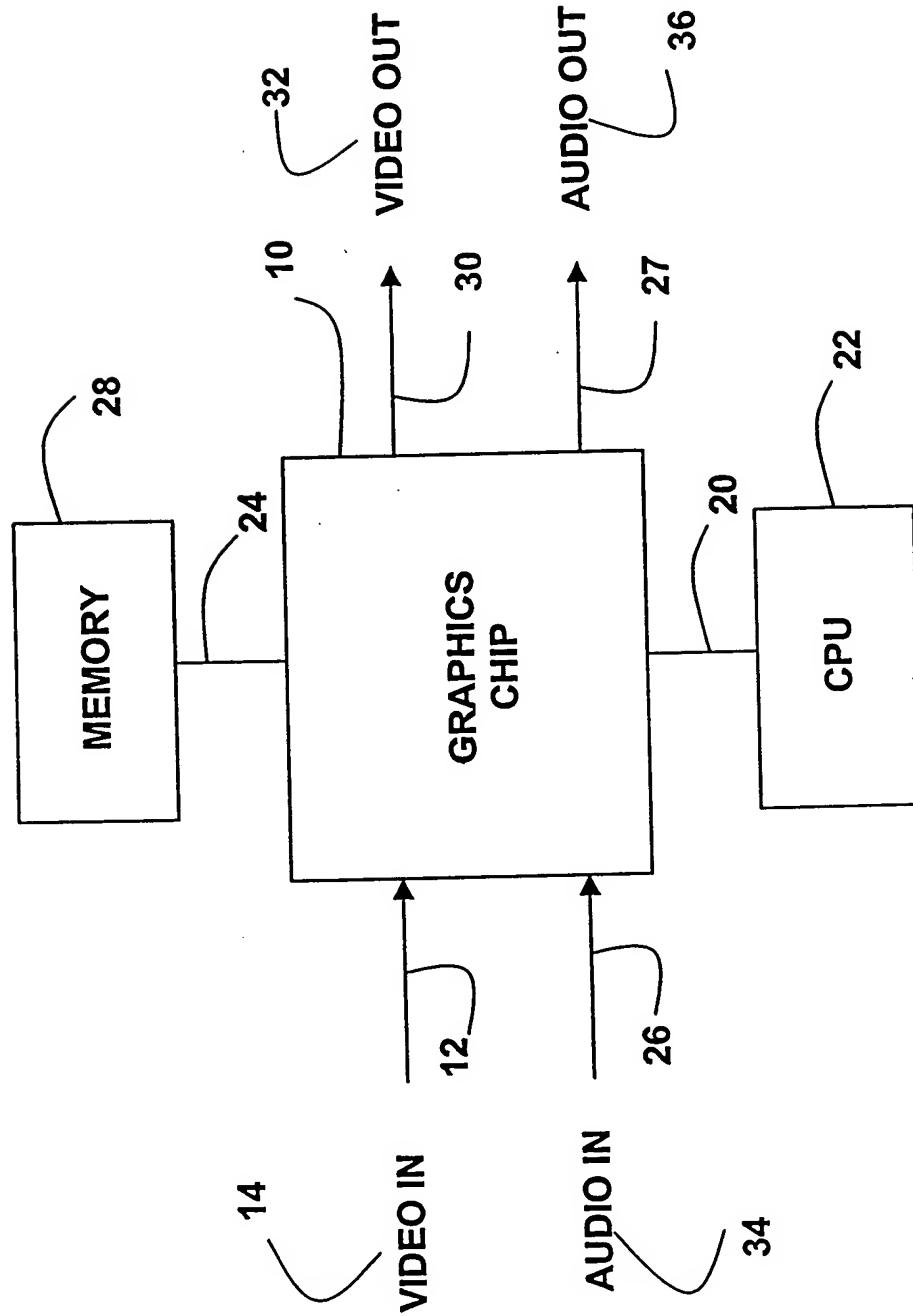


FIG. 1



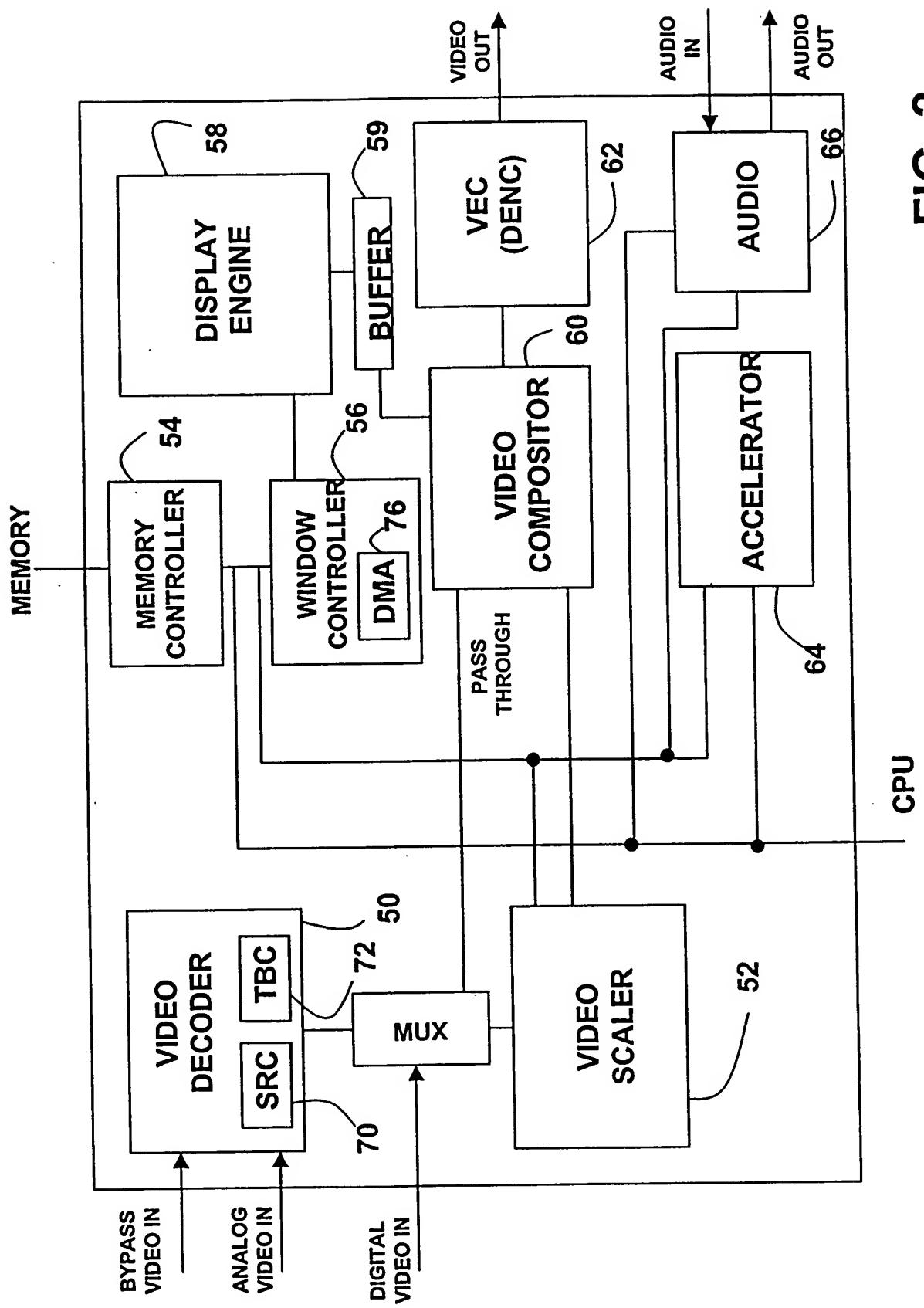


FIG. 2

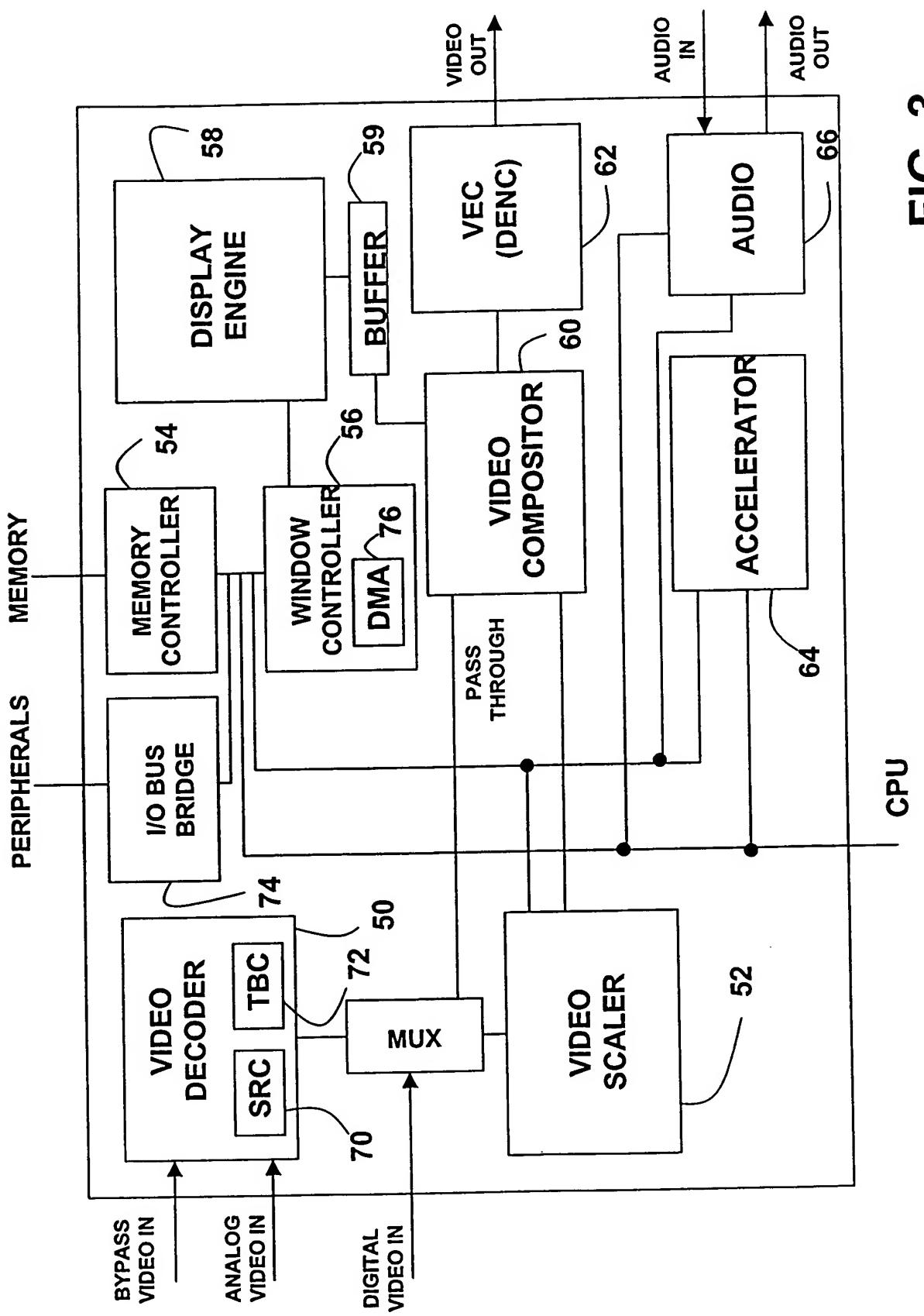


FIG. 3

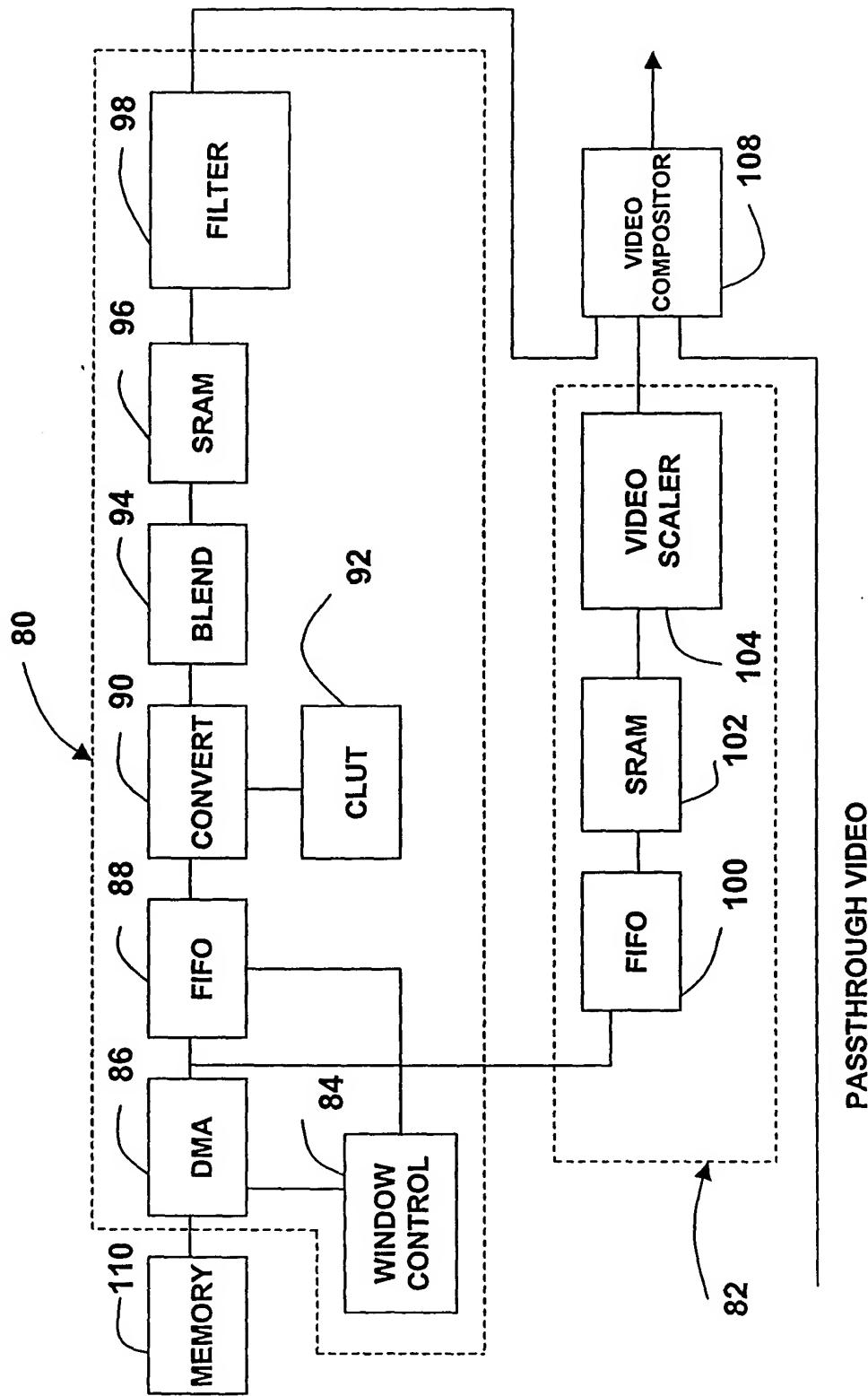


FIG. 4

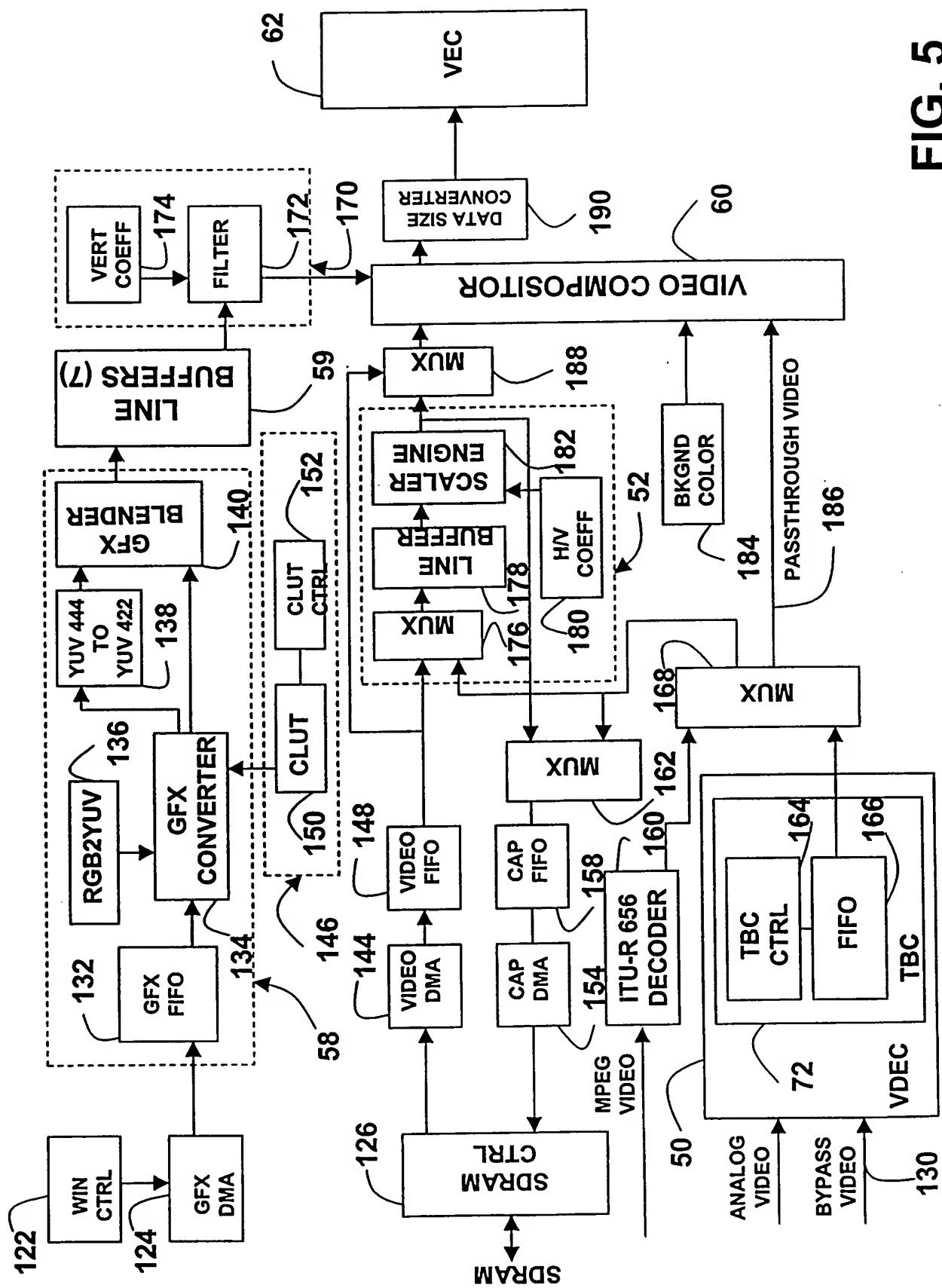
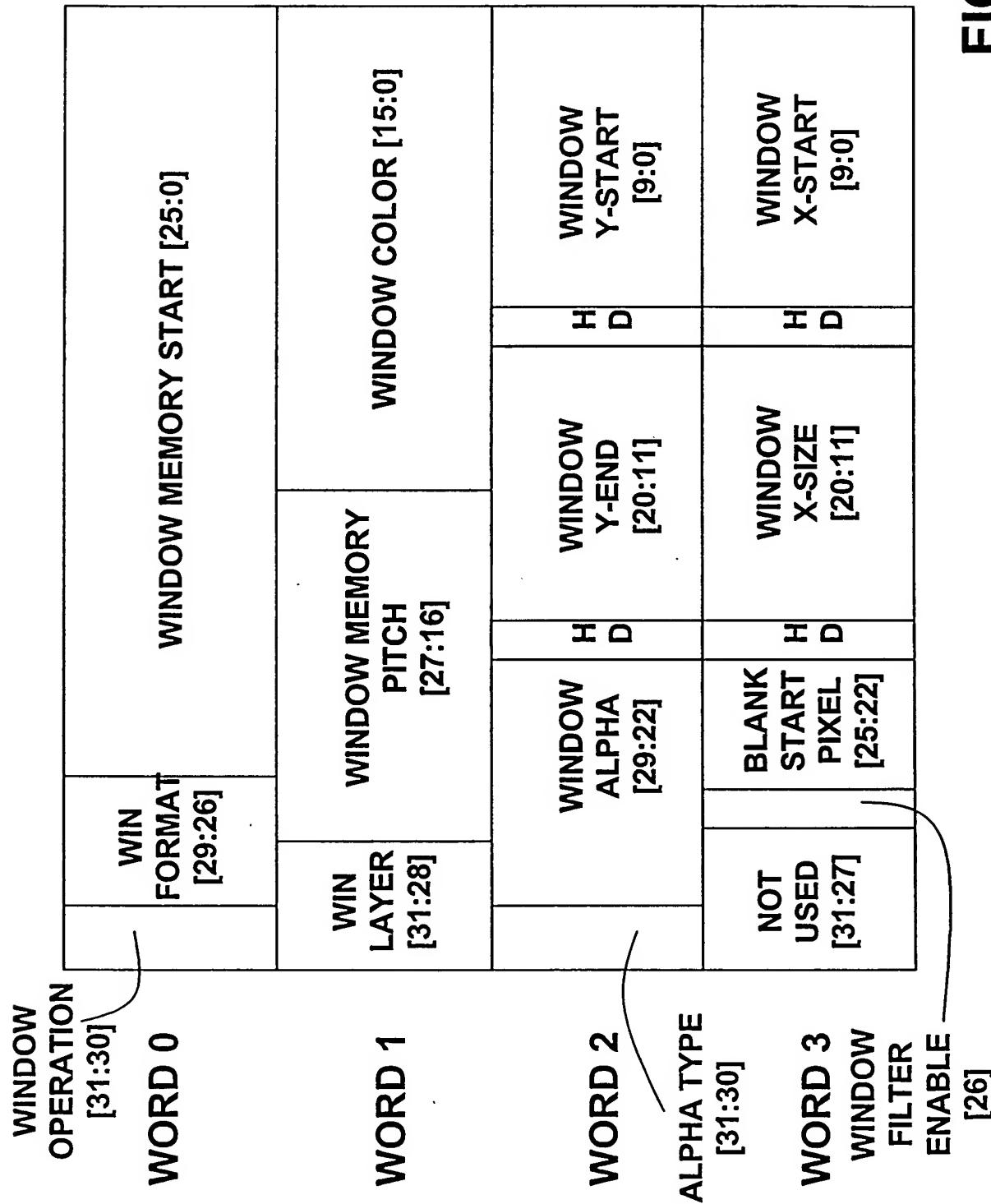
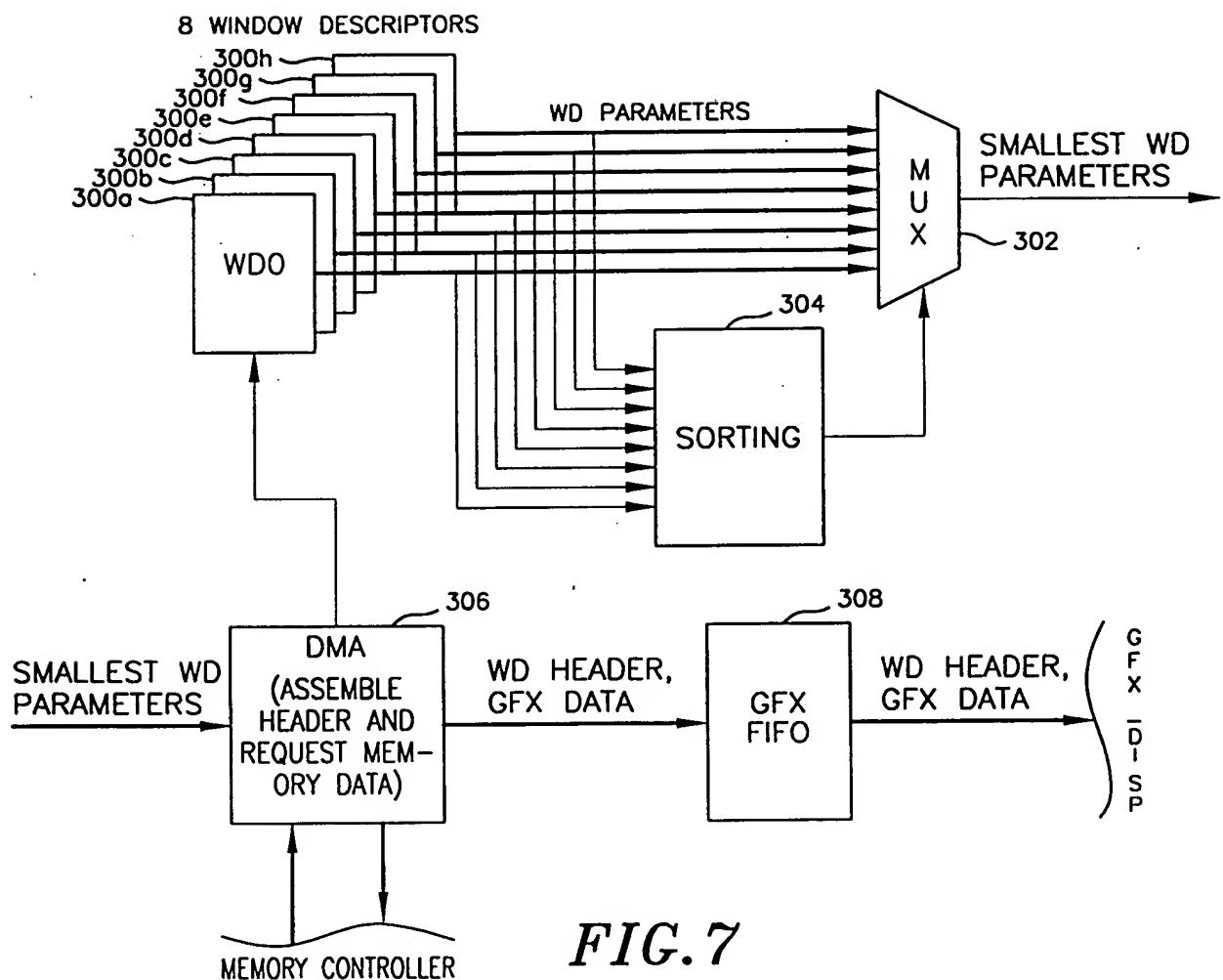


FIG. 6





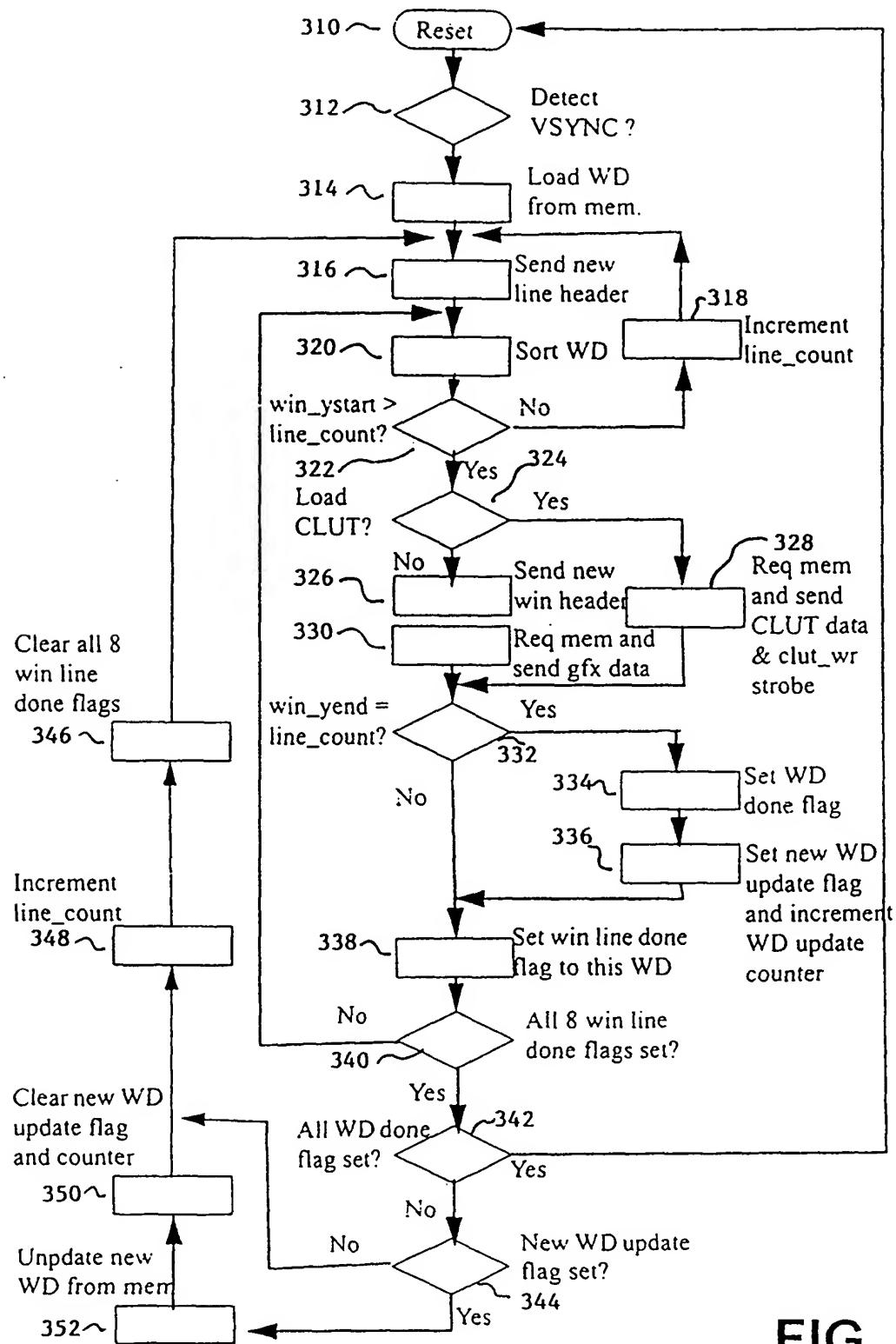


FIG. 8

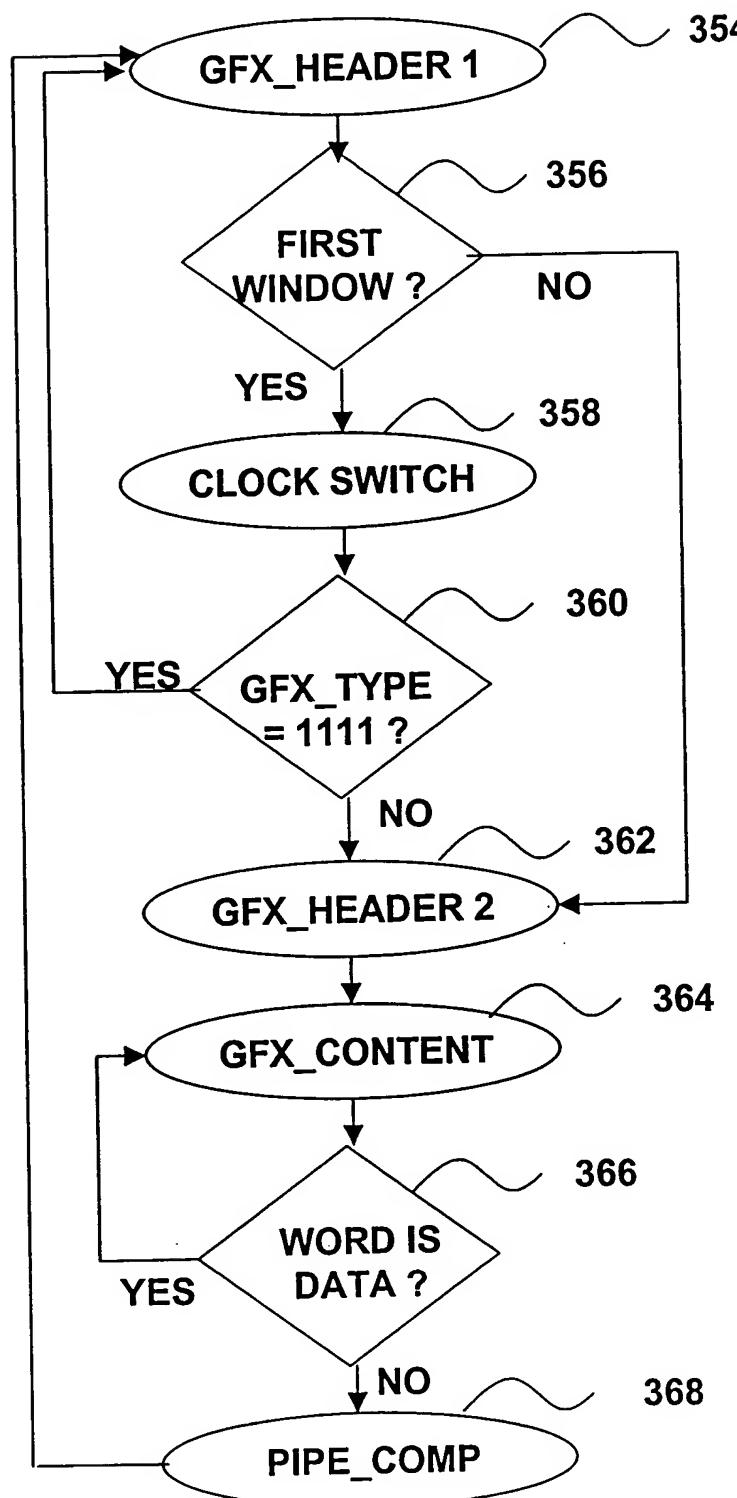


FIG. 9

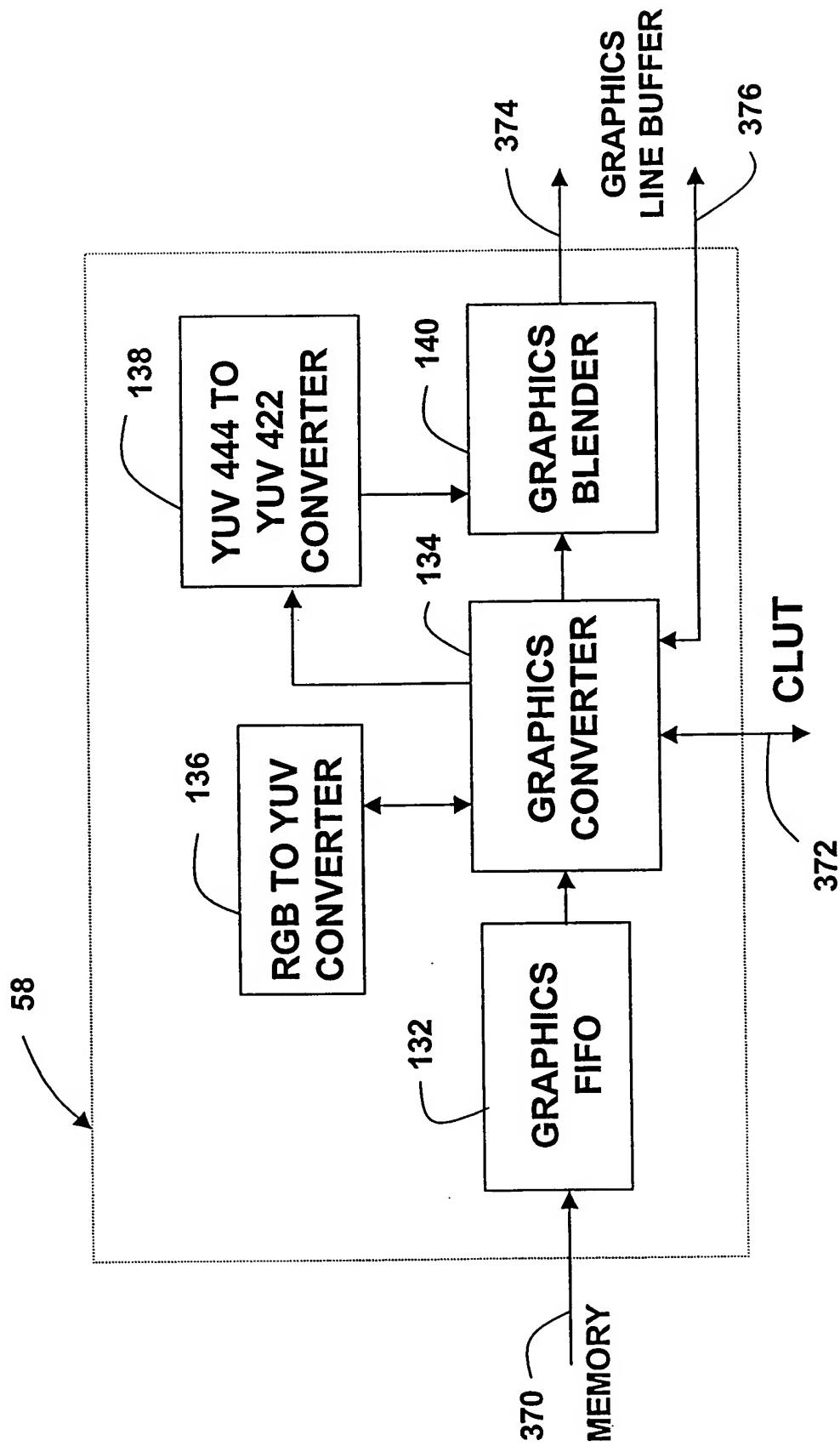


FIG. 10

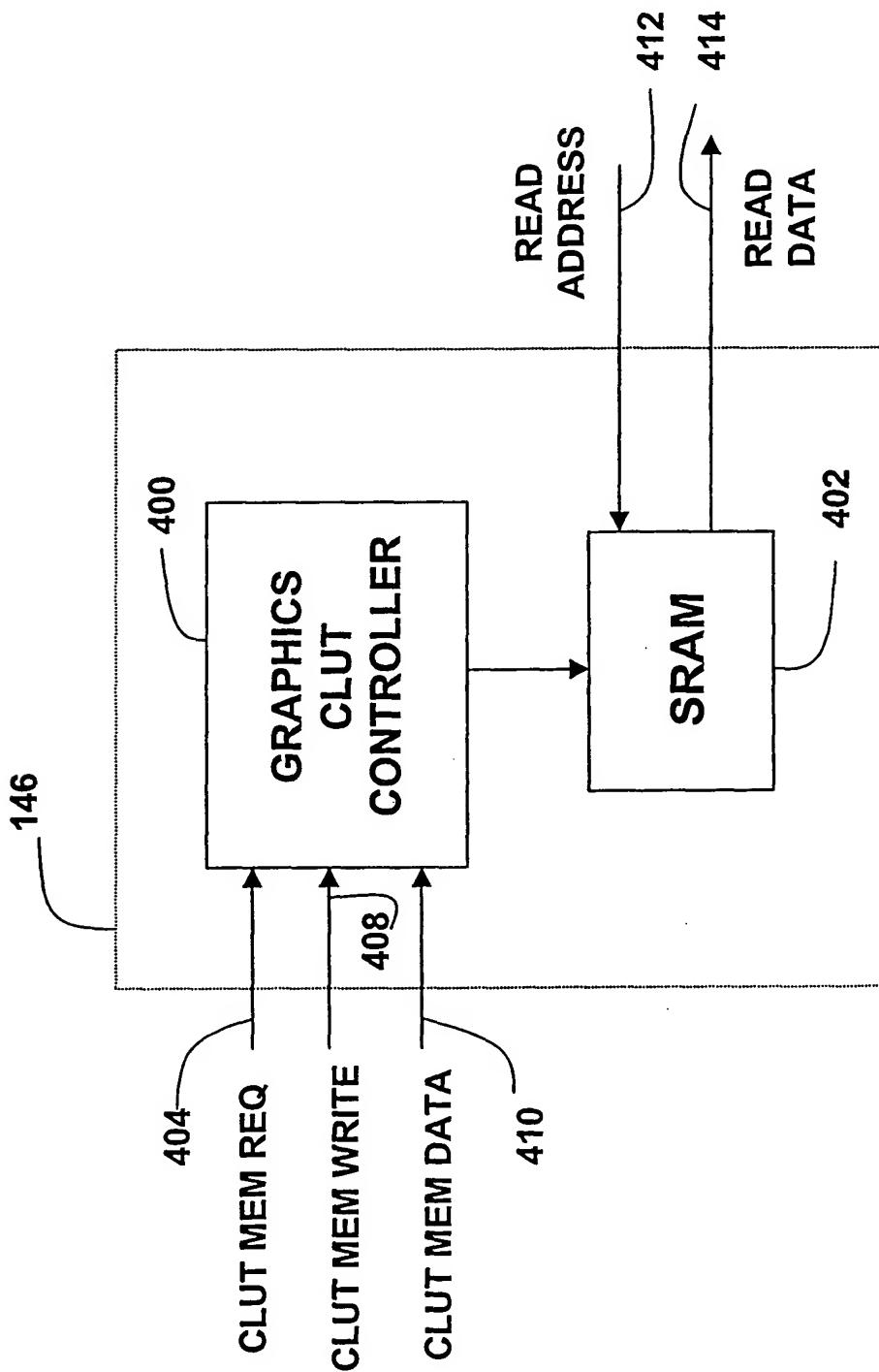


FIG. 11

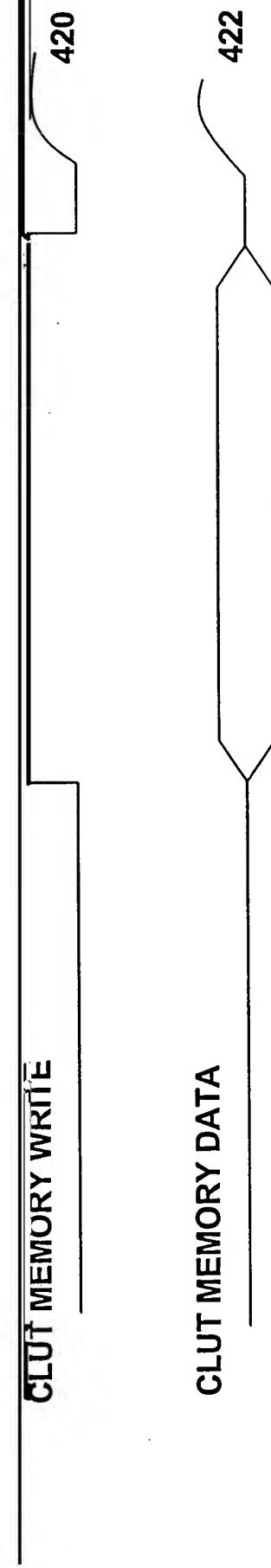


FIG. 12

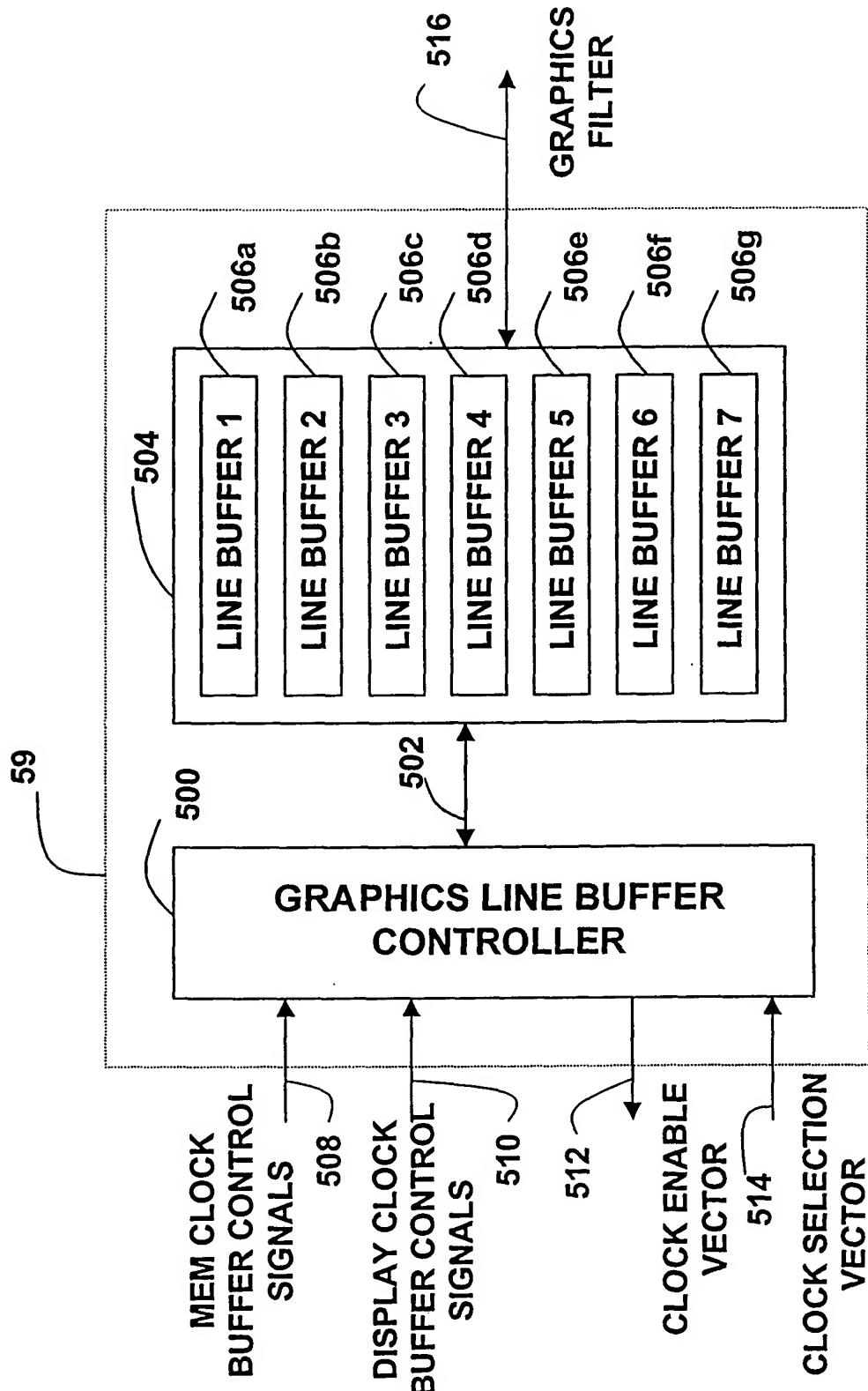


FIG. 13

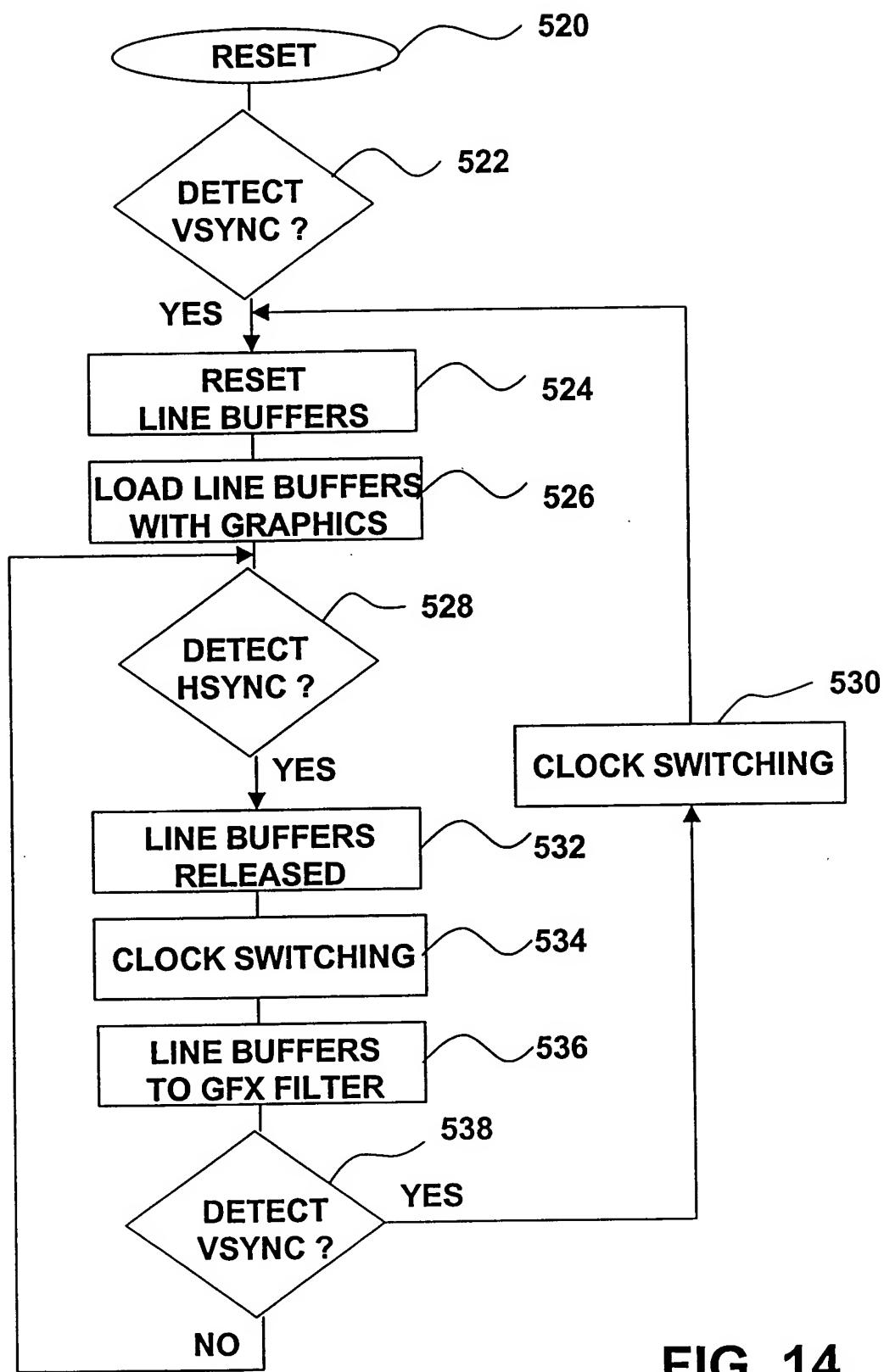


FIG. 14

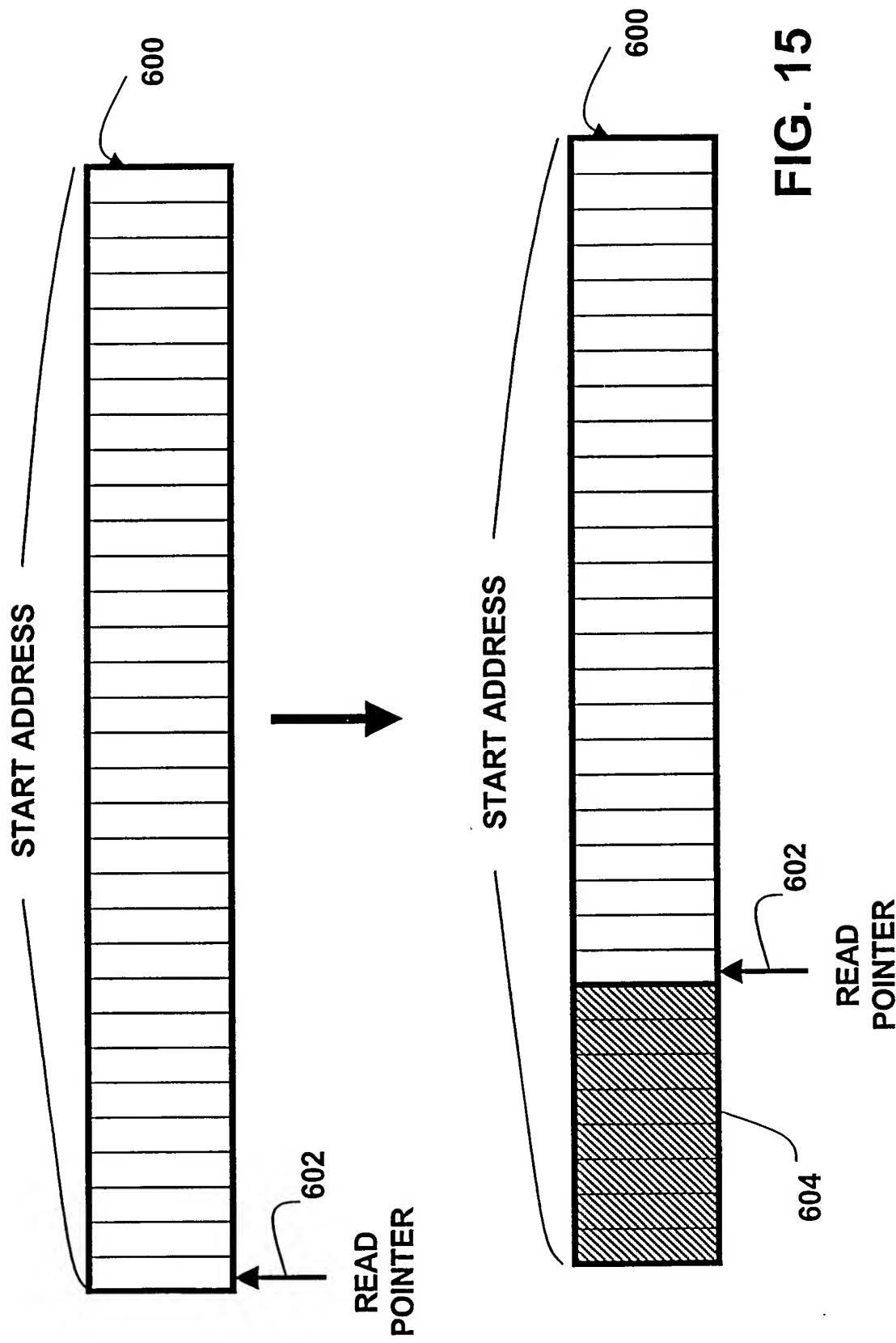


FIG. 15

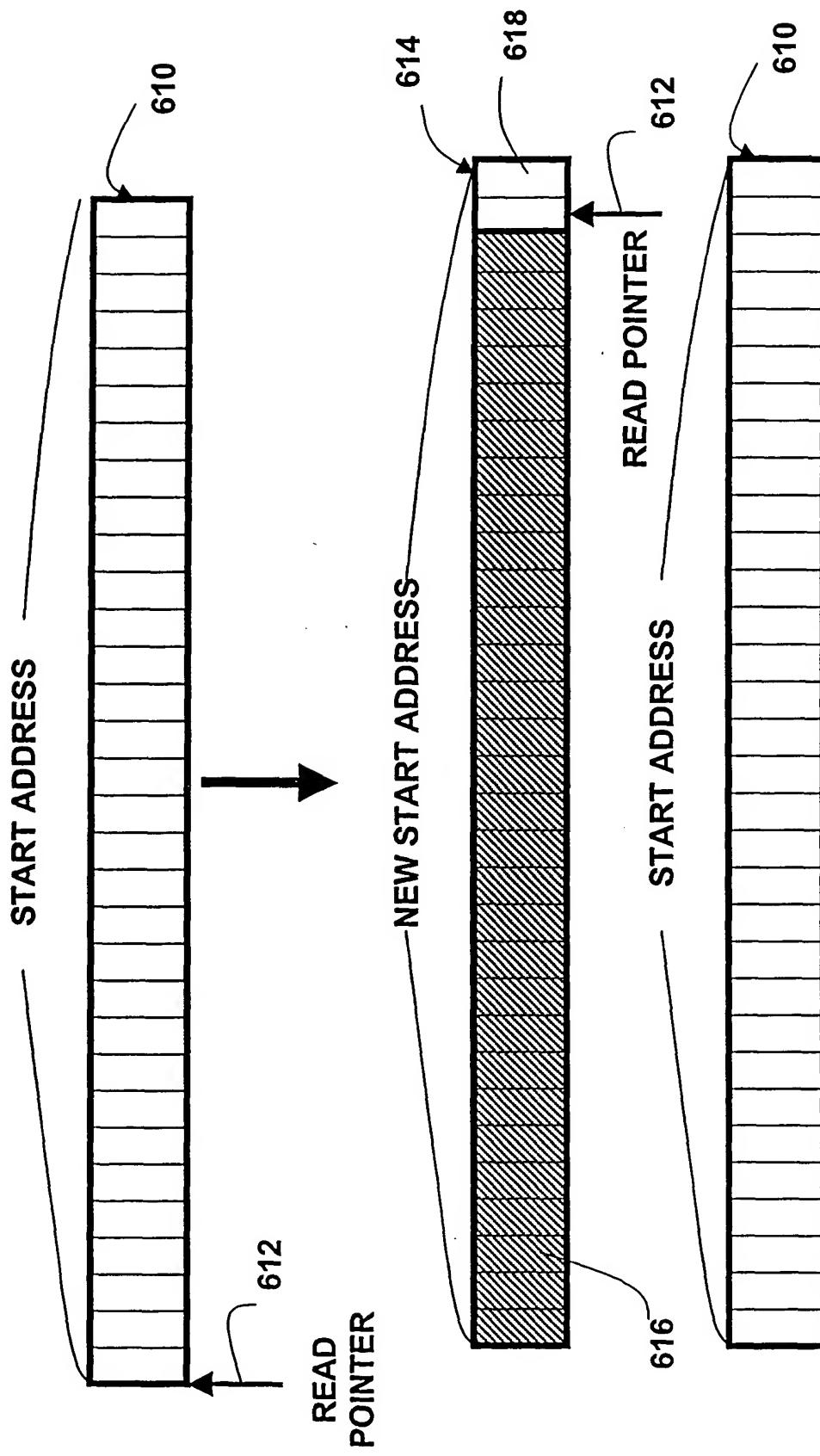


FIG. 16

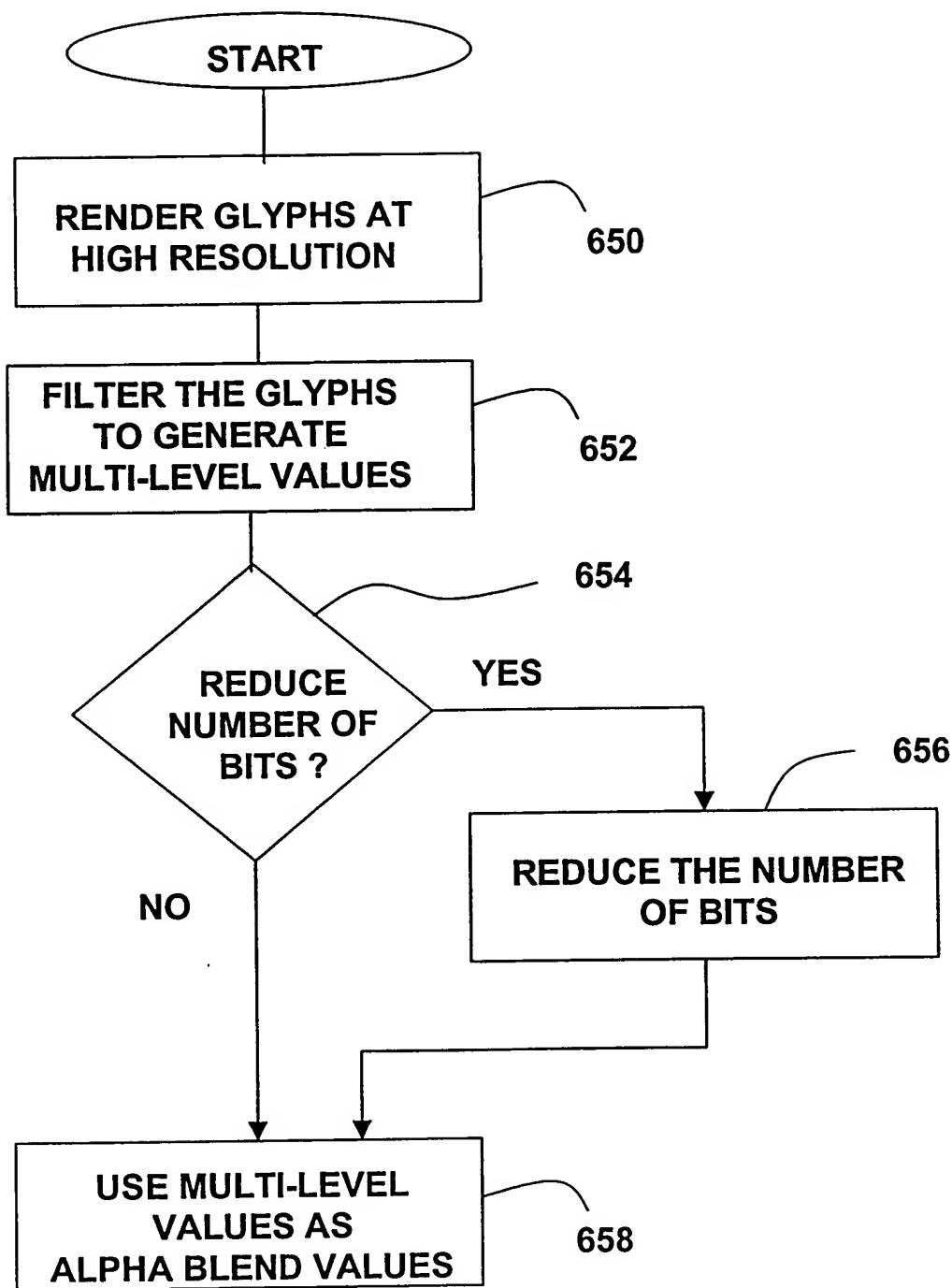


FIG. 17

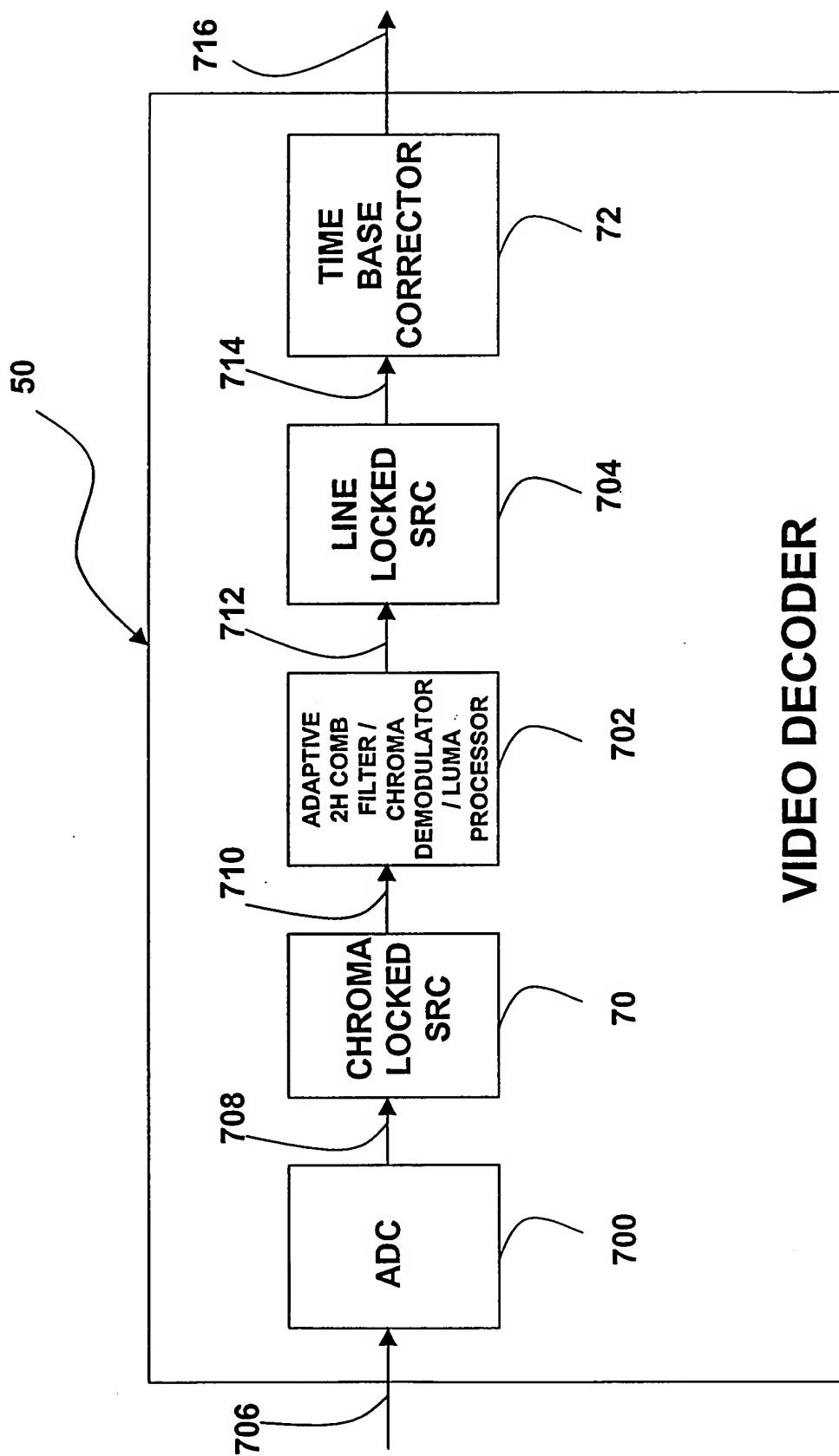


FIG. 18

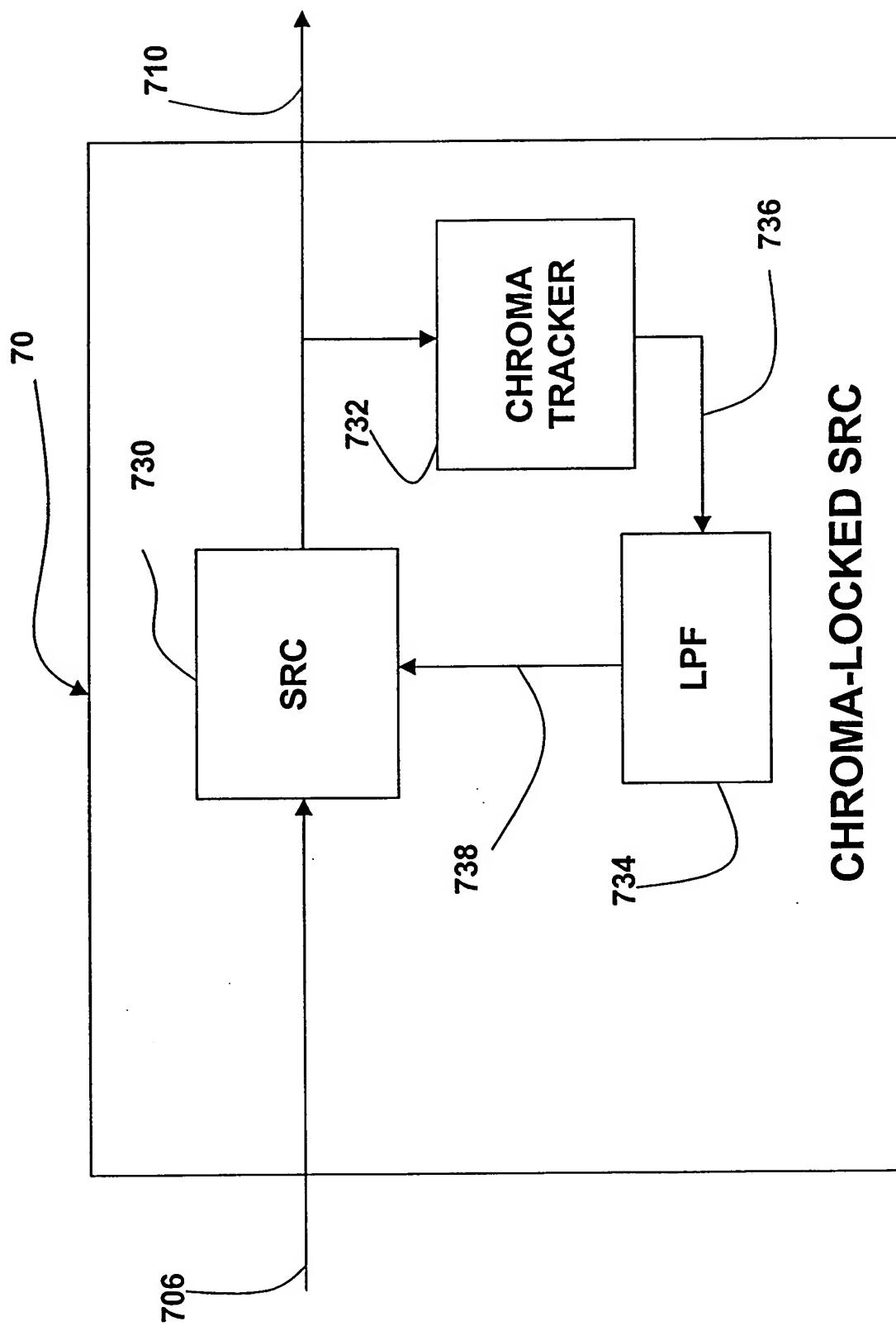


FIG. 19

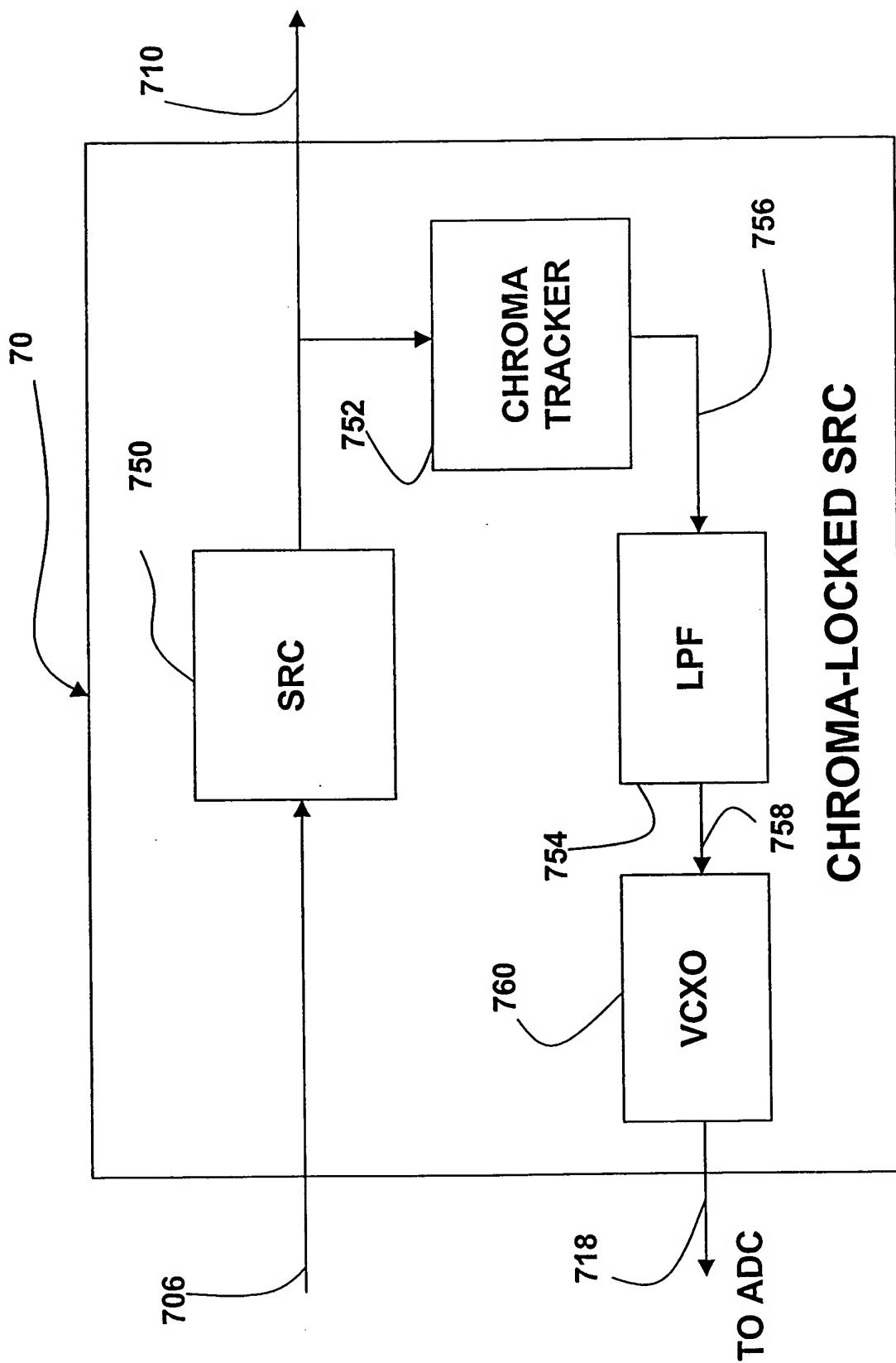


FIG. 20

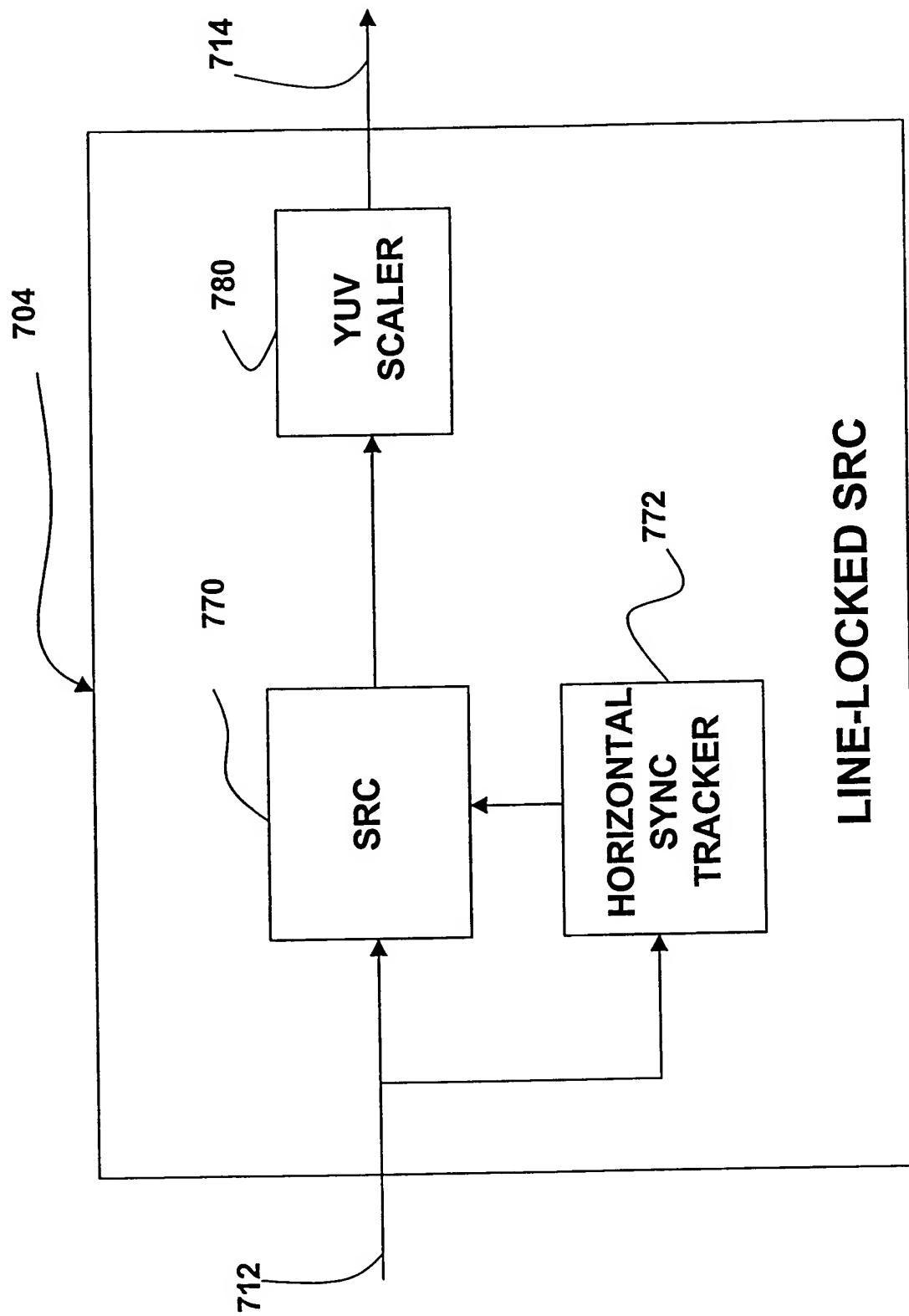


FIG. 21

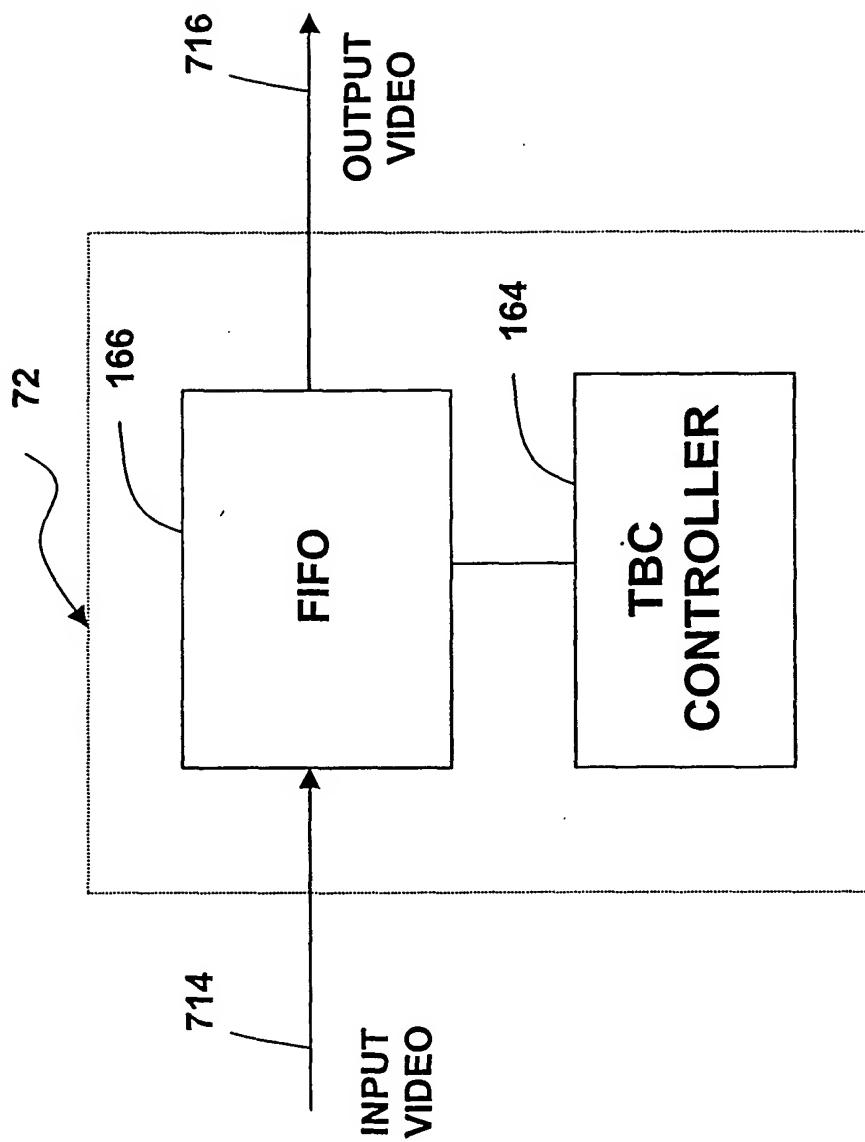


FIG. 22

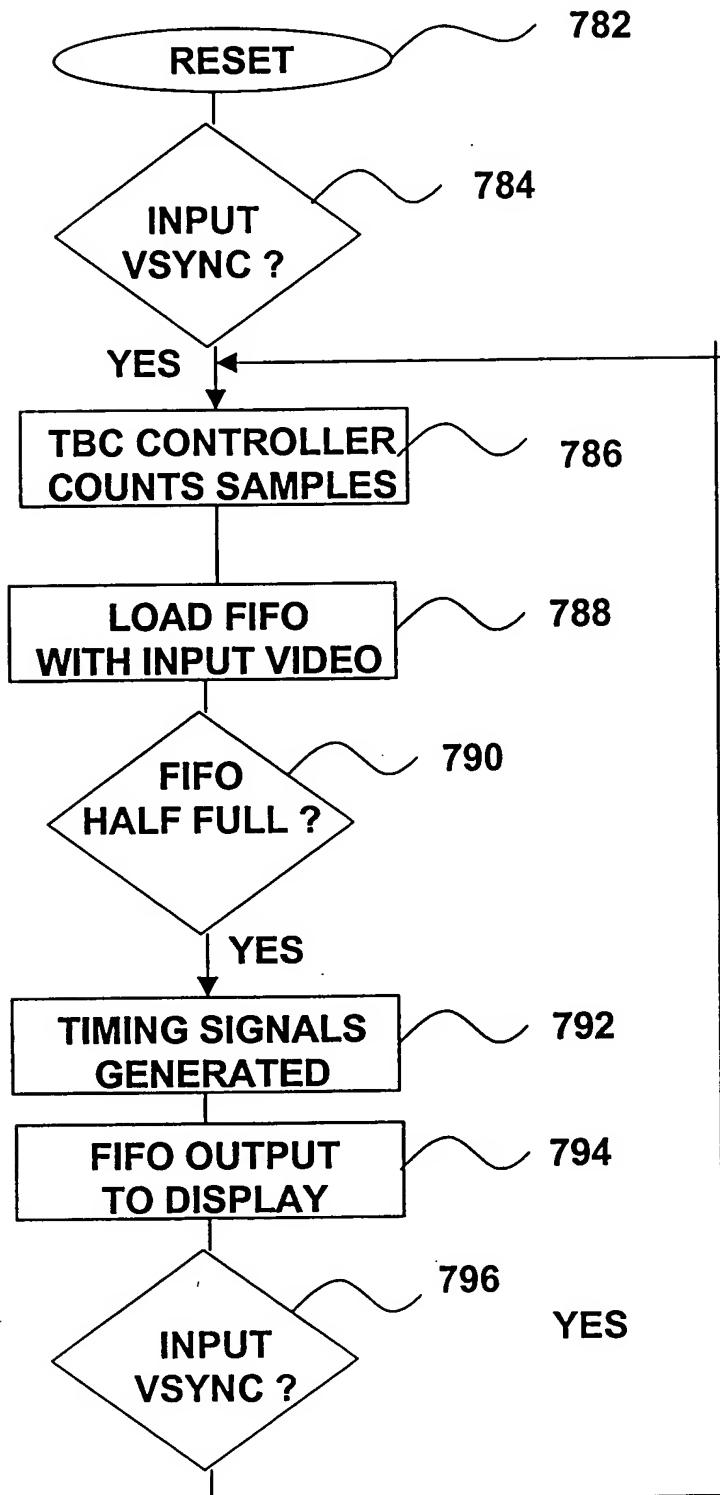


FIG. 23

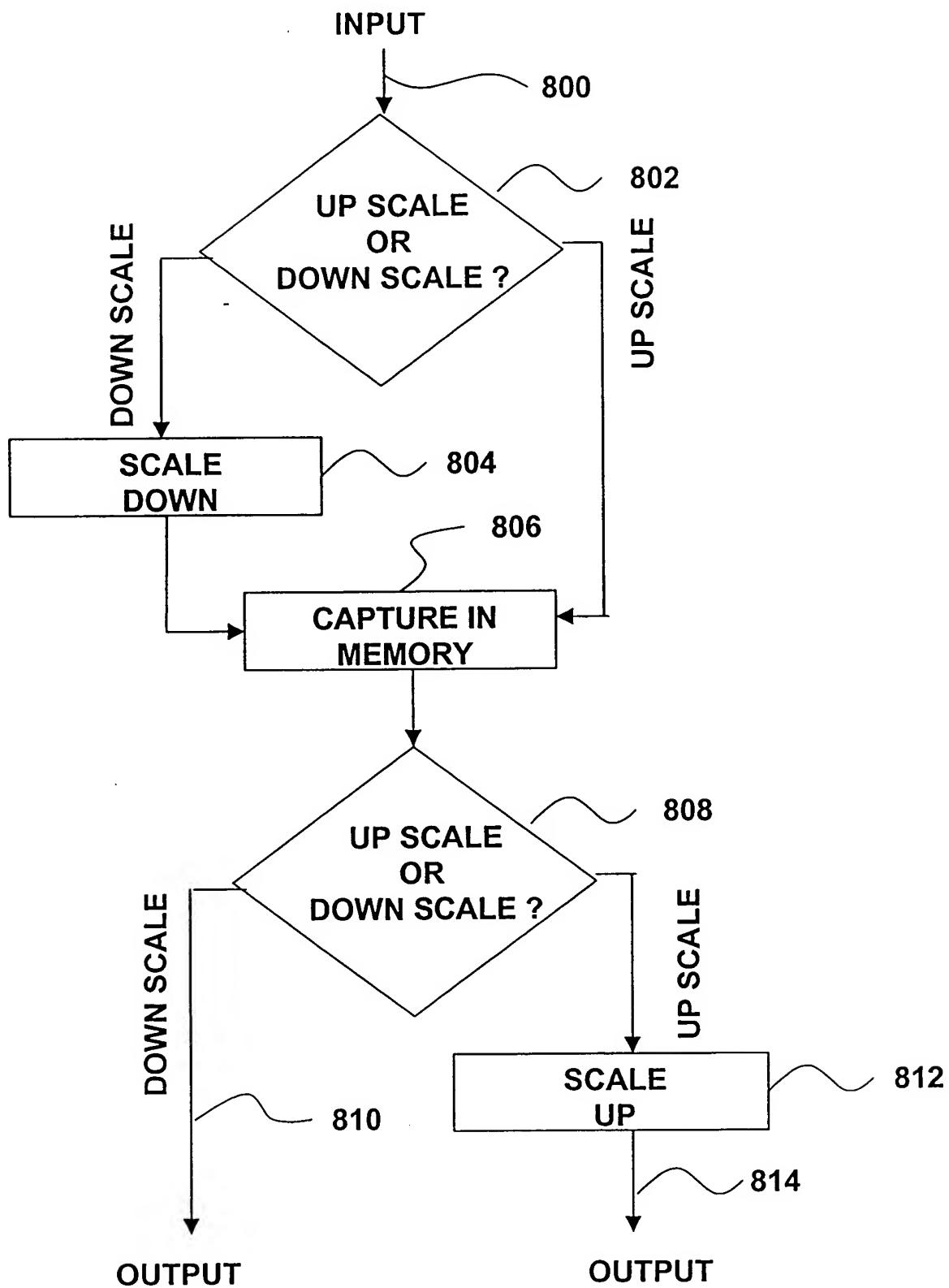


FIG. 24

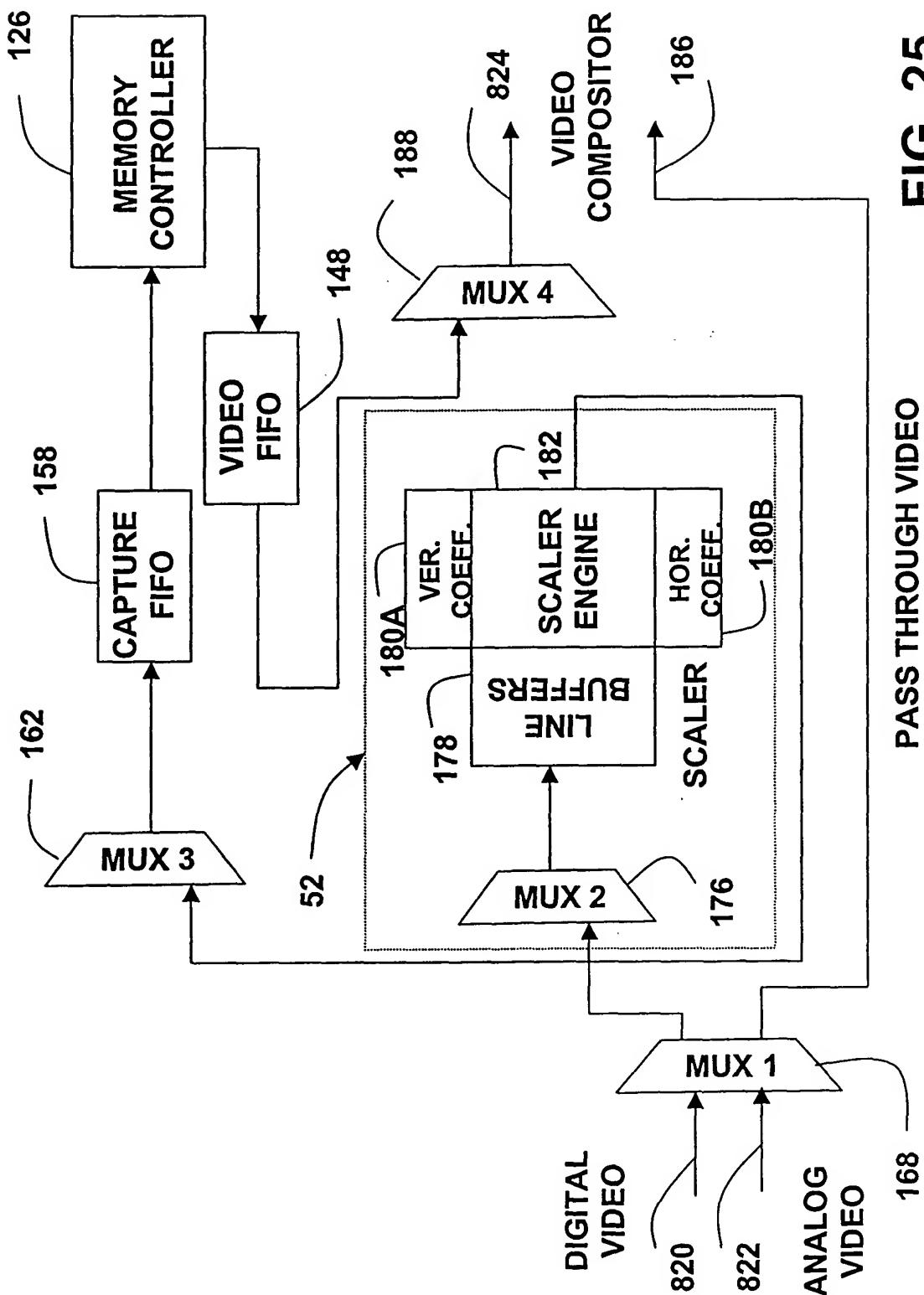


FIG. 25

PASS THROUGH VIDEO

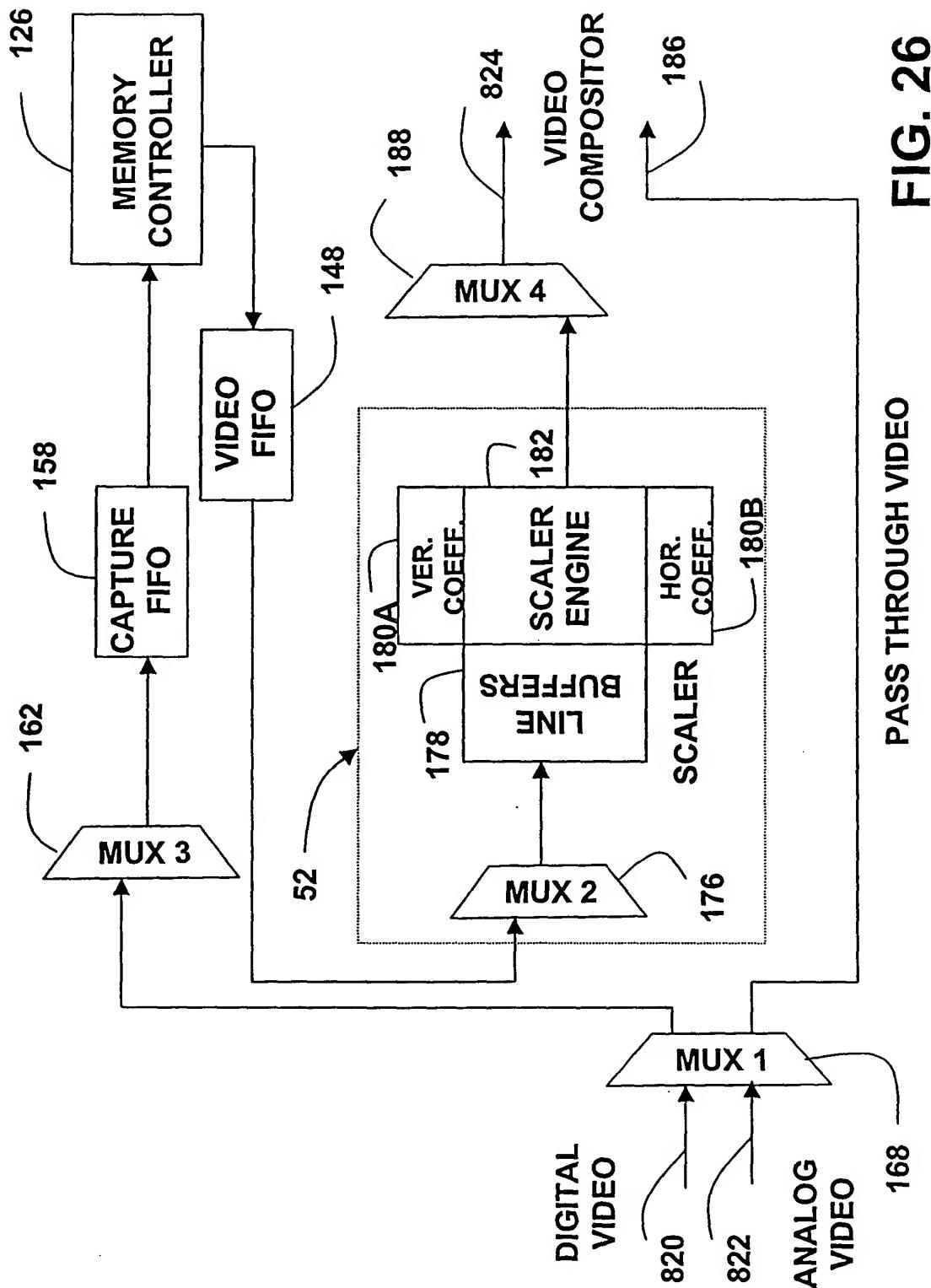


FIG. 26

PASS THROUGH VIDEO

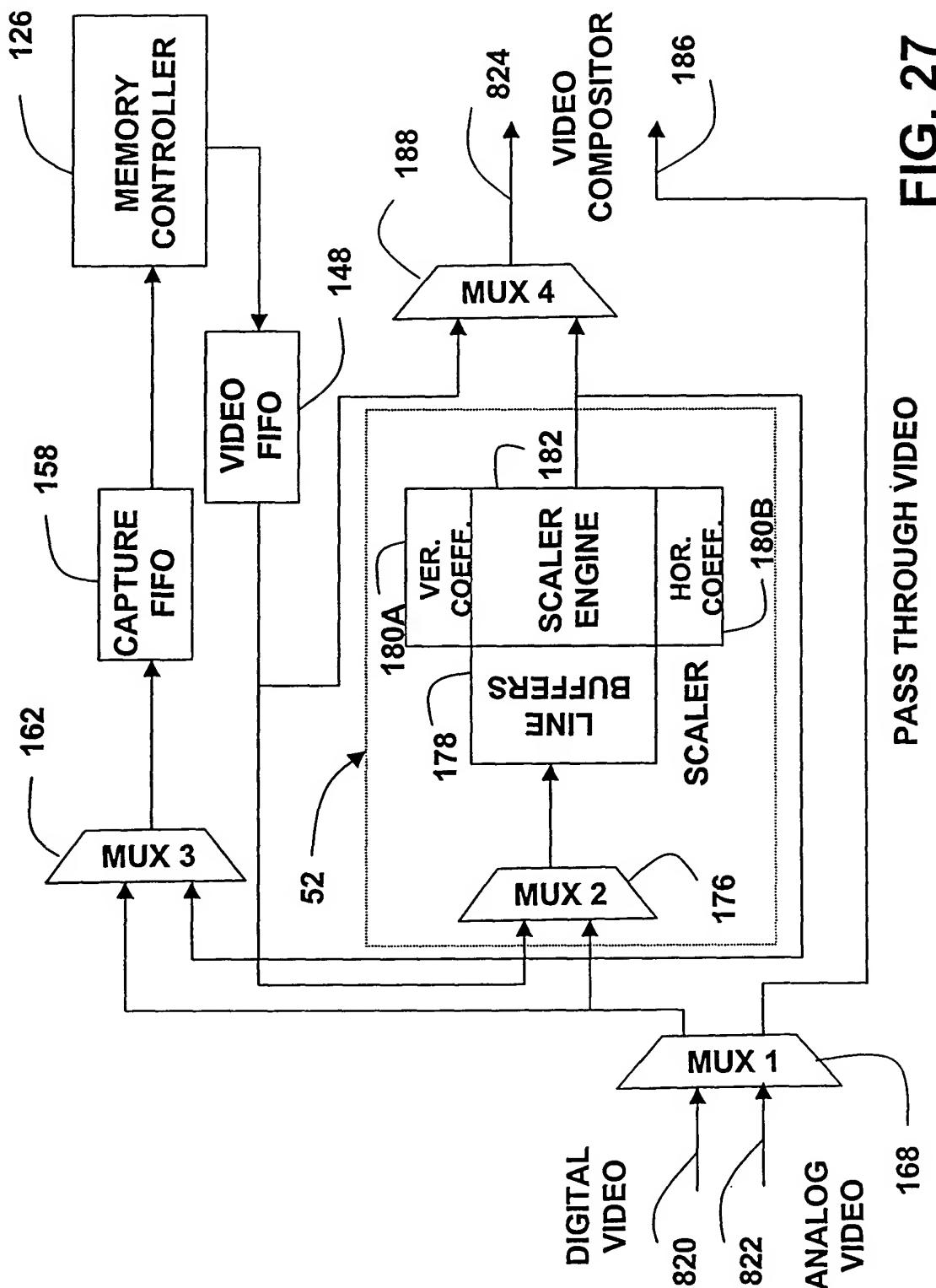


FIG. 27

PASS THROUGH VIDEO

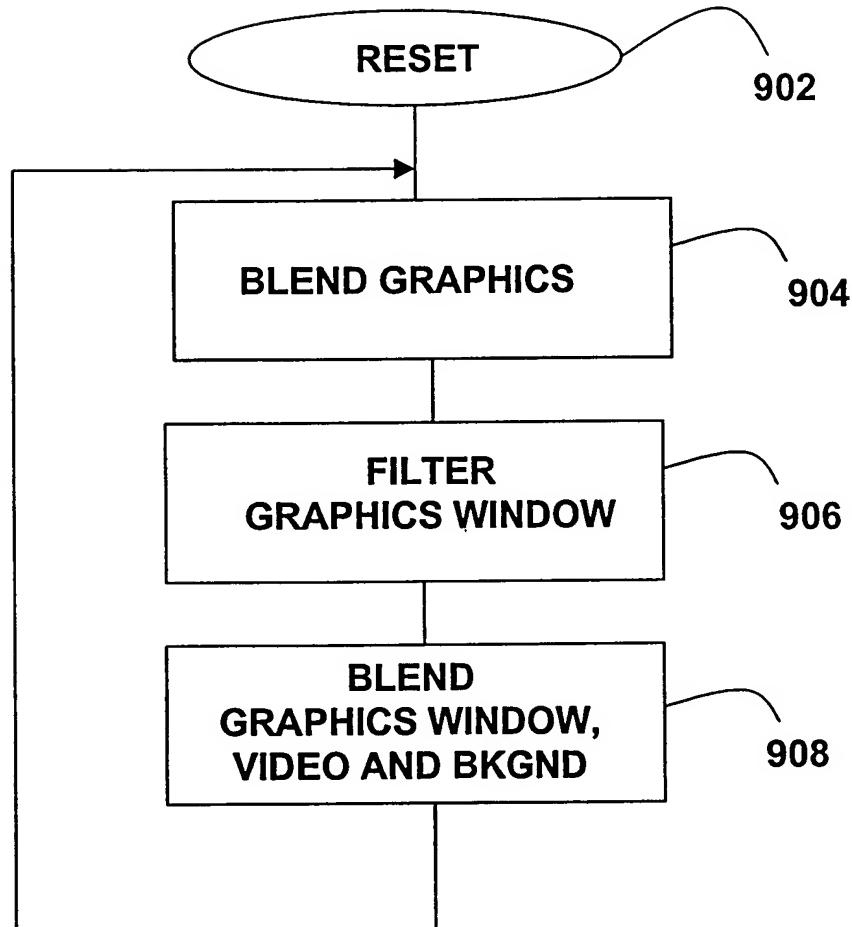


FIG. 28

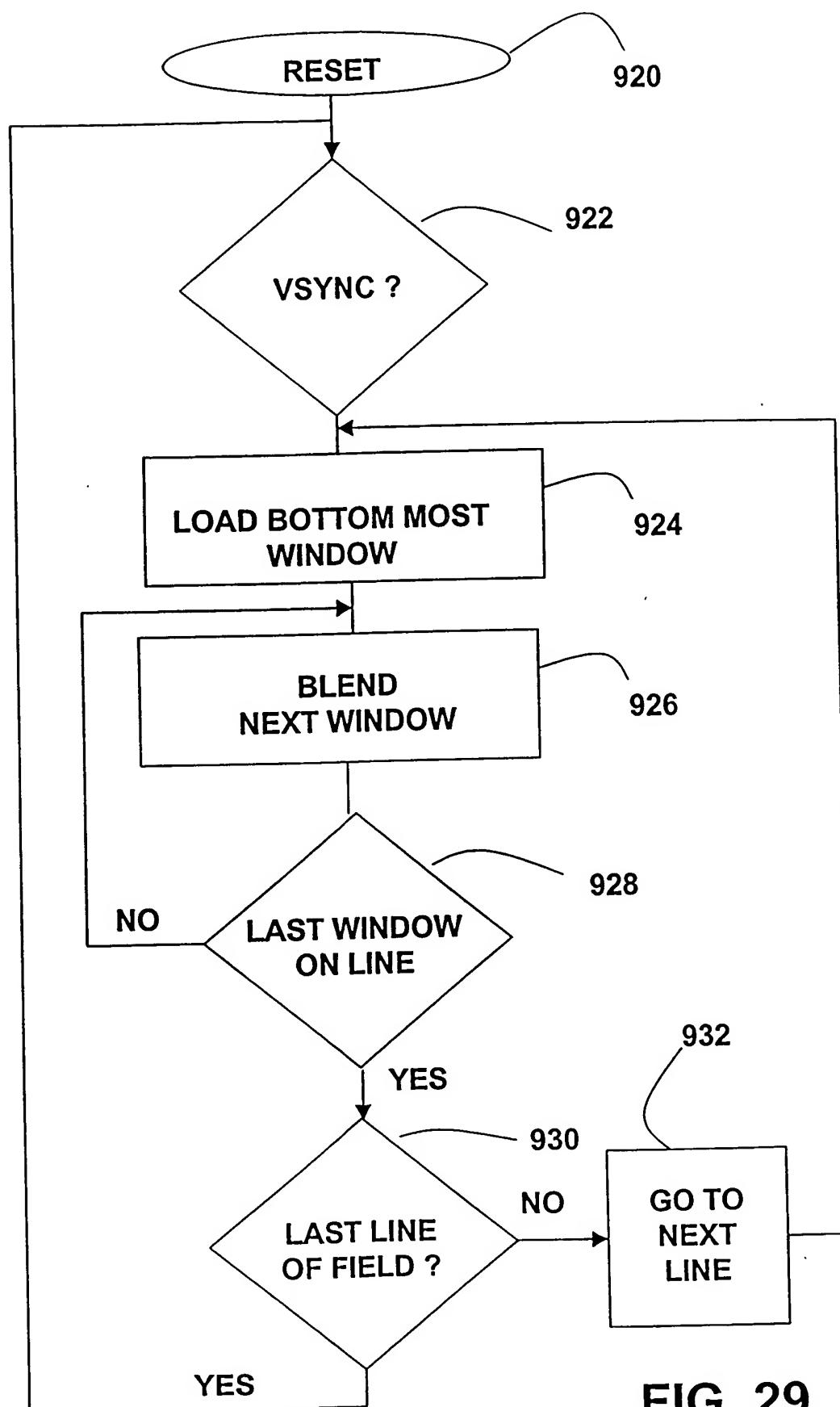


FIG. 29

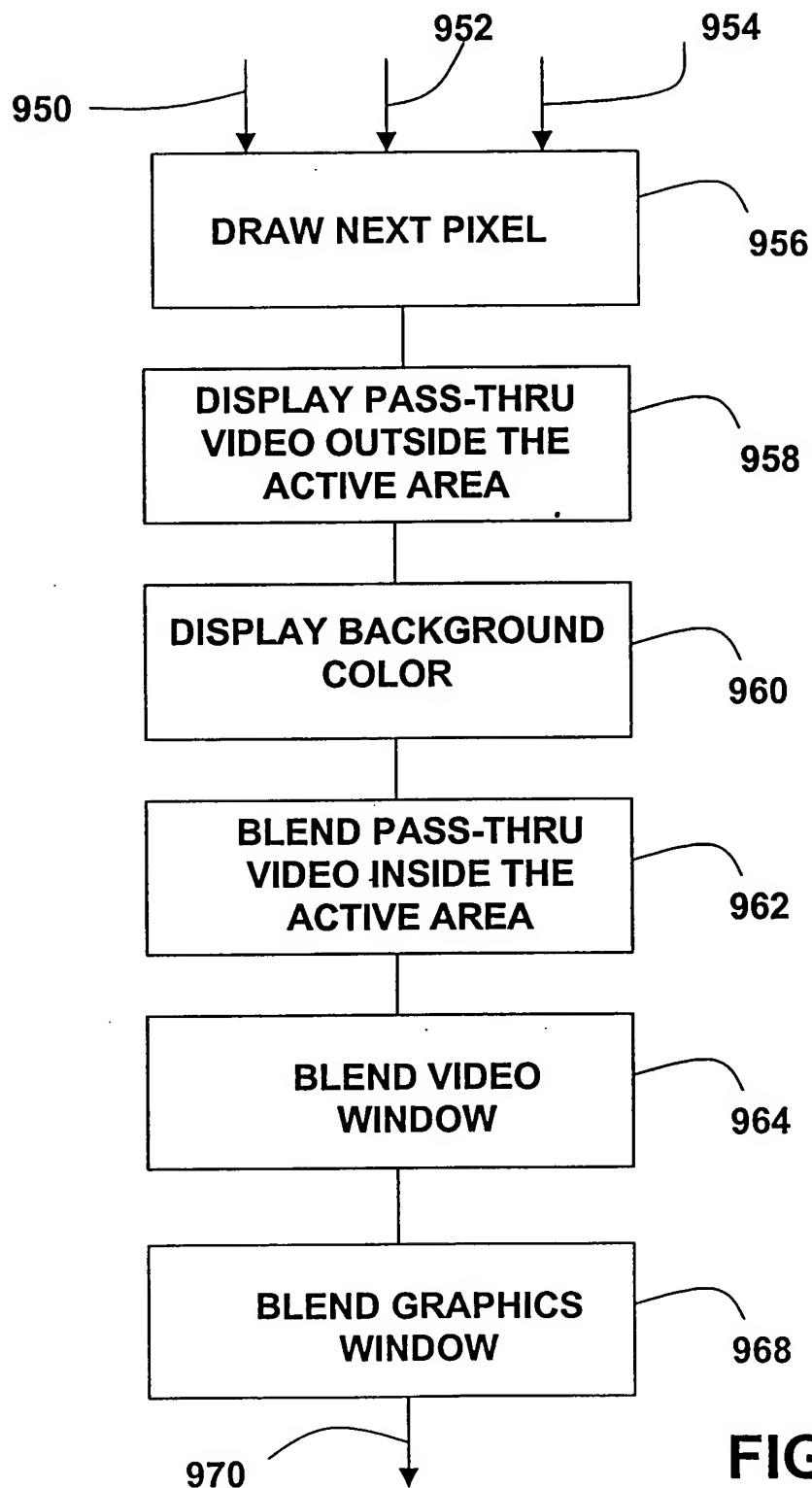


FIG. 30

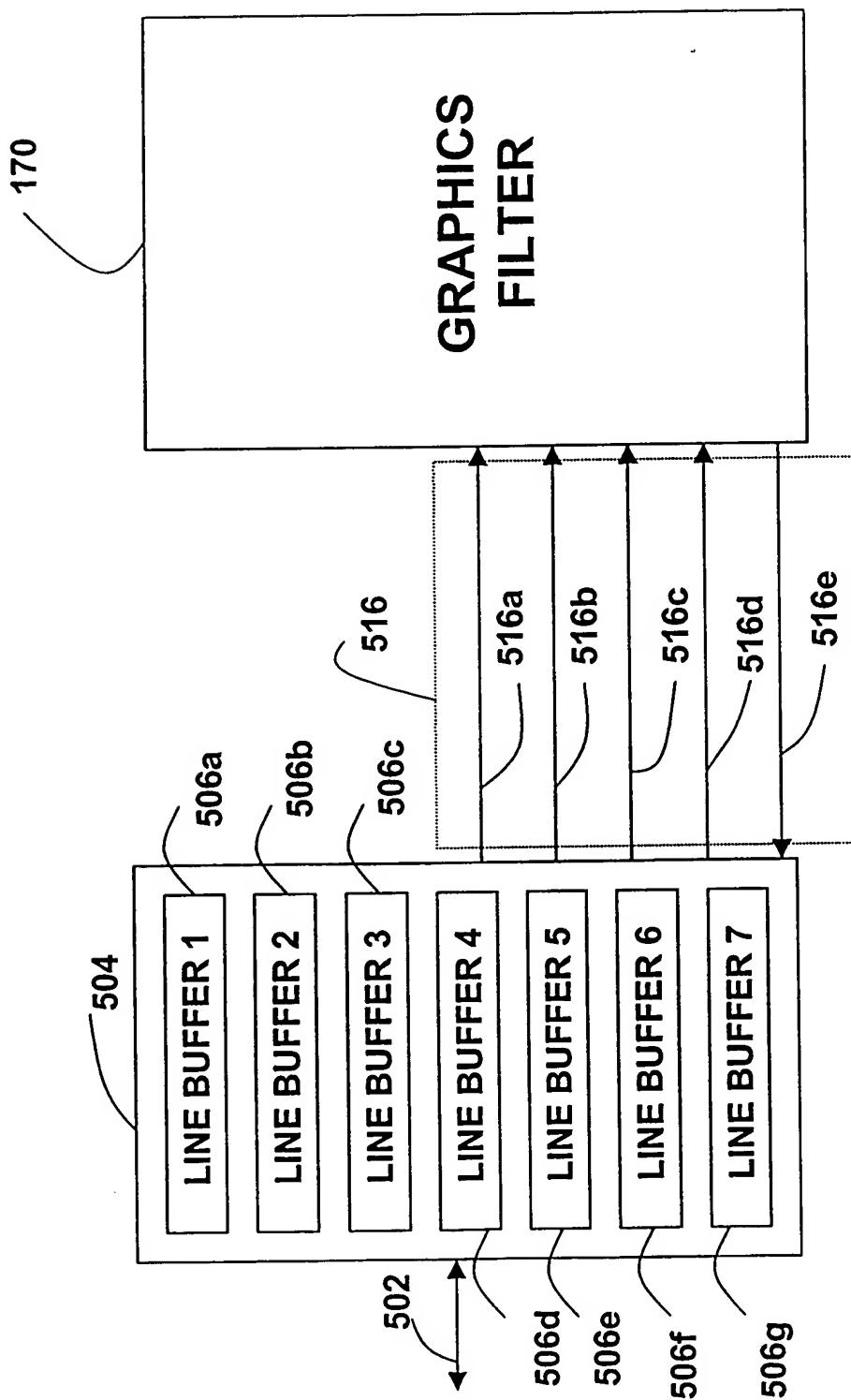


FIG. 31

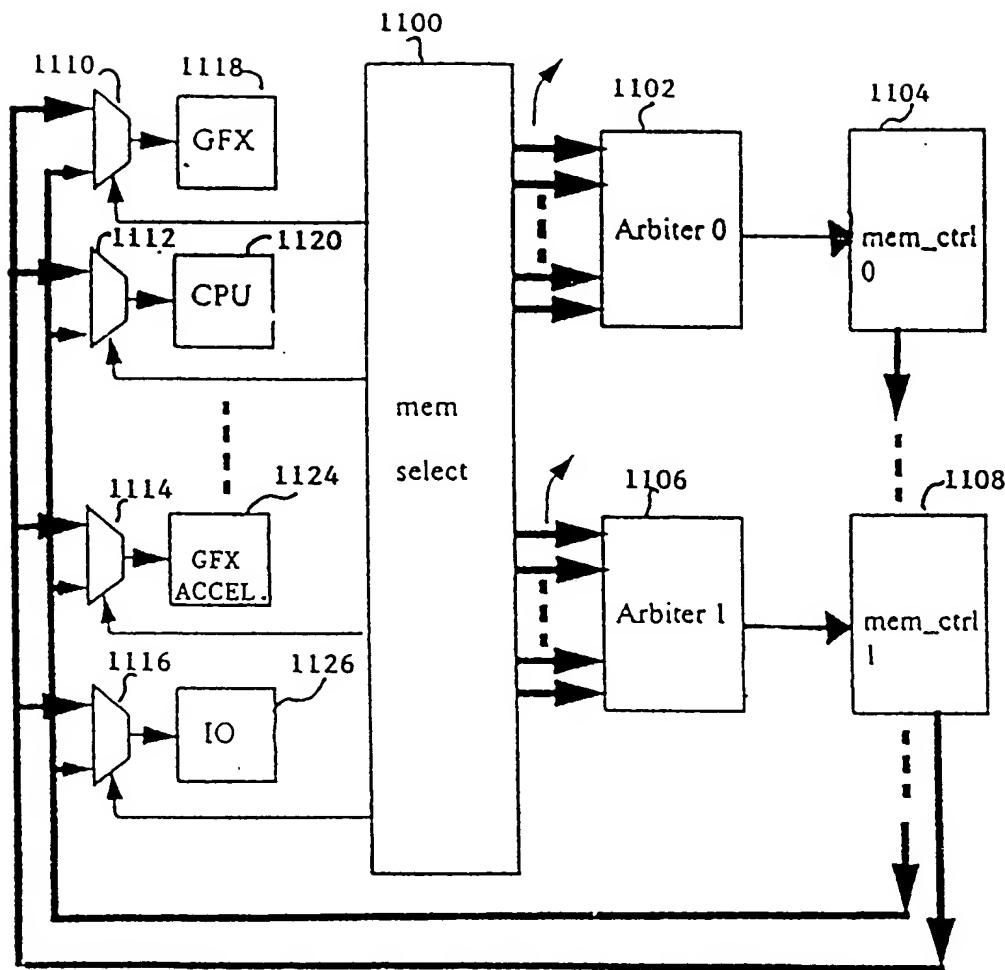
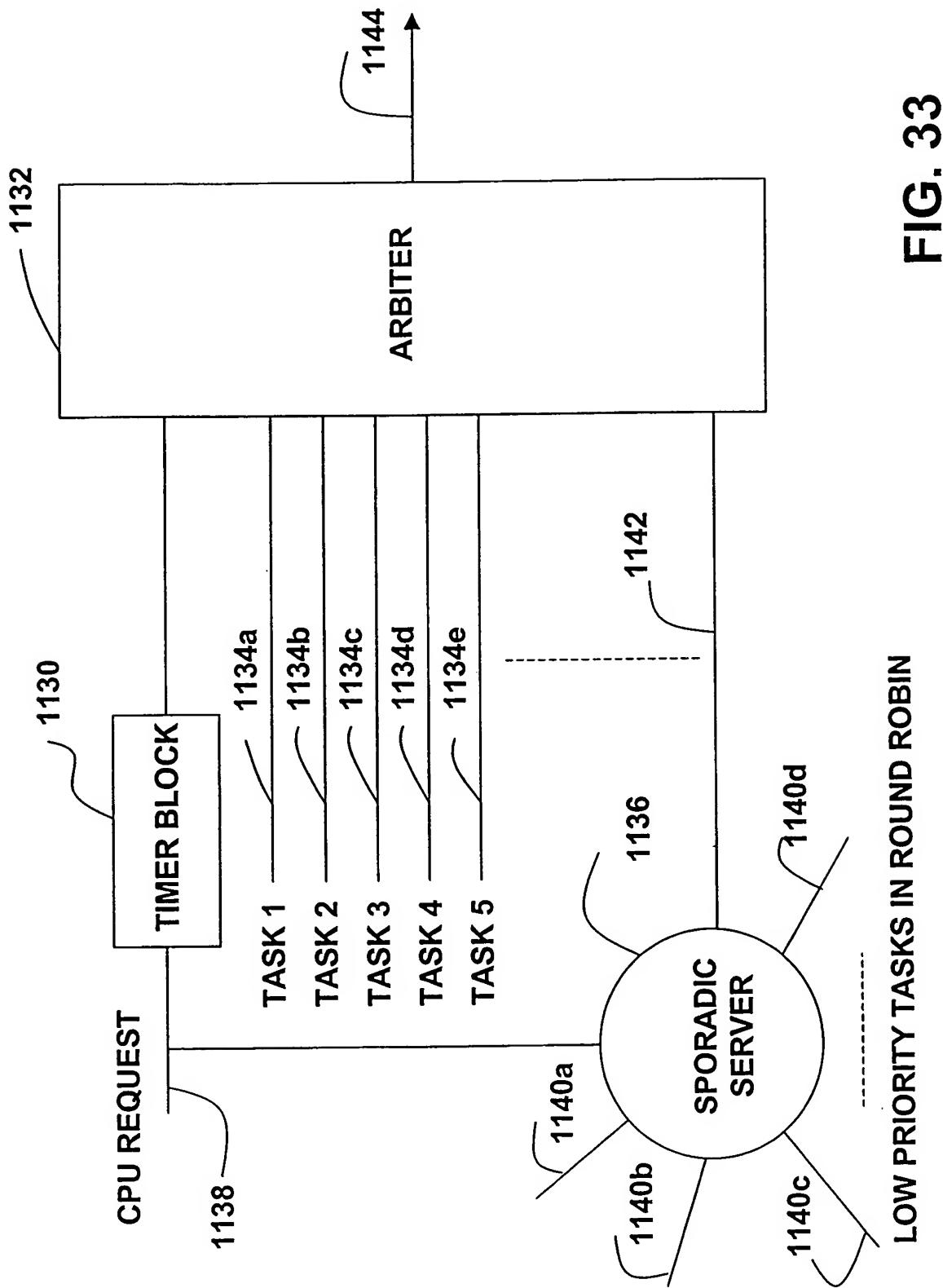


FIG. 32



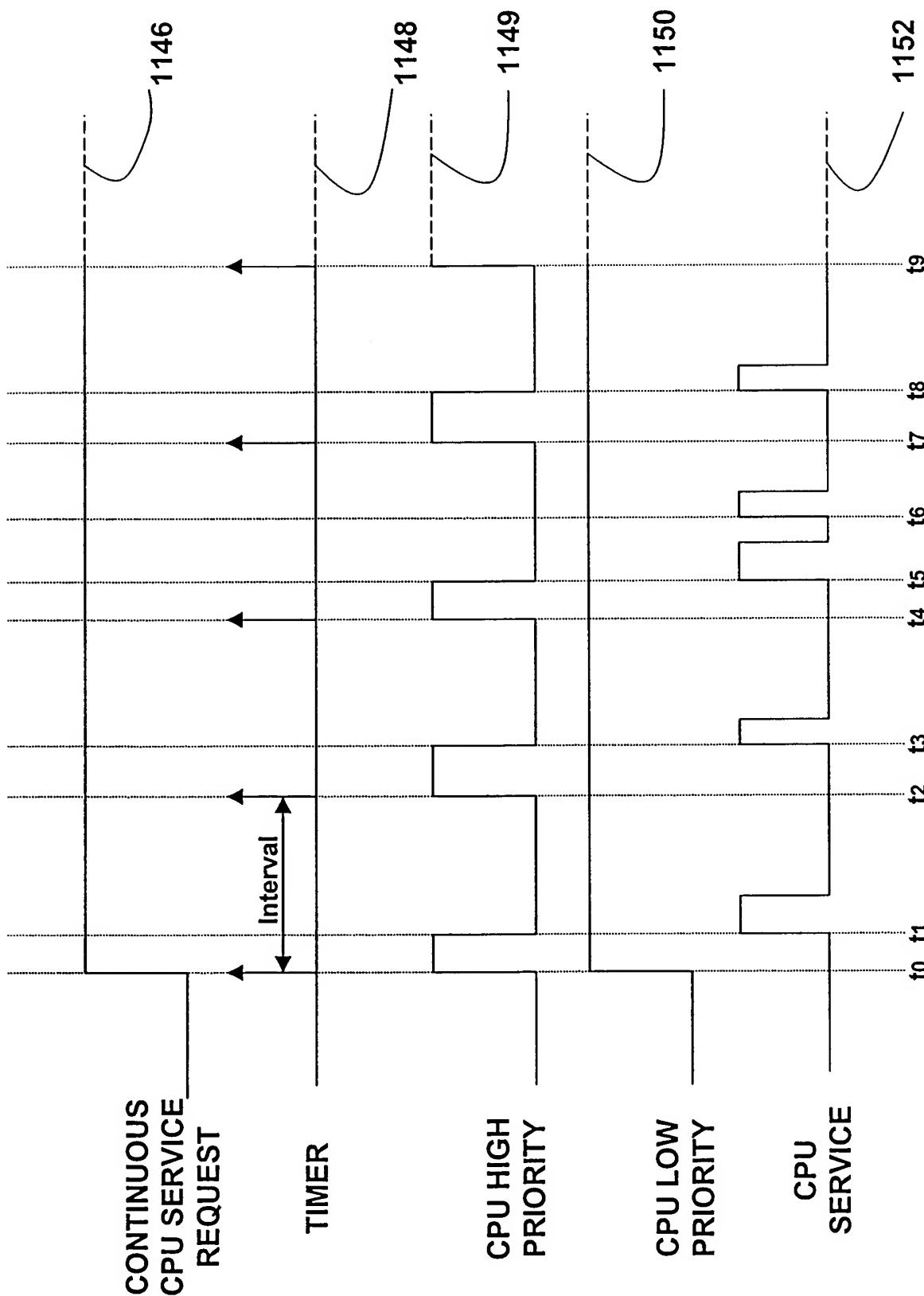


FIG. 34

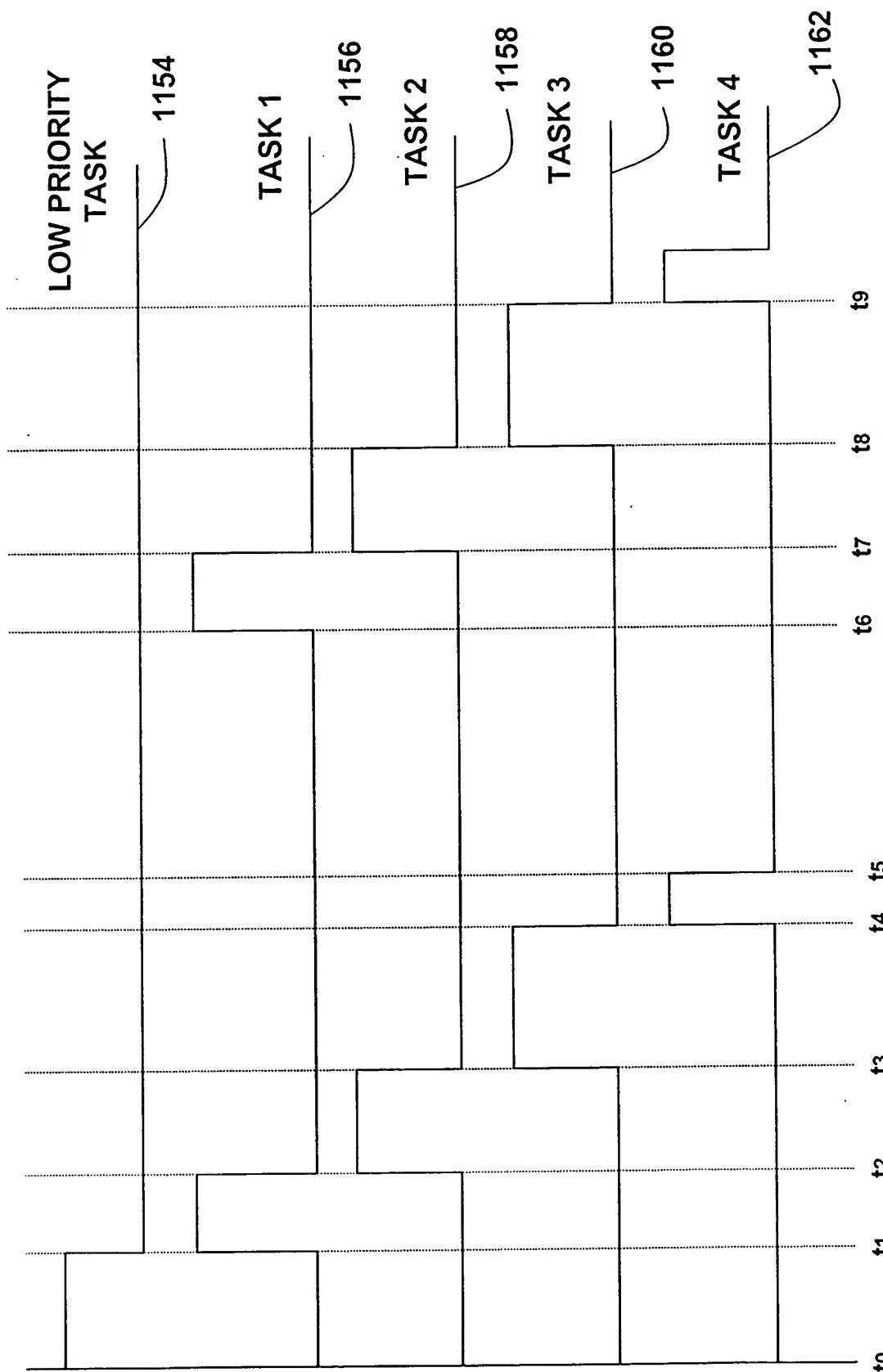
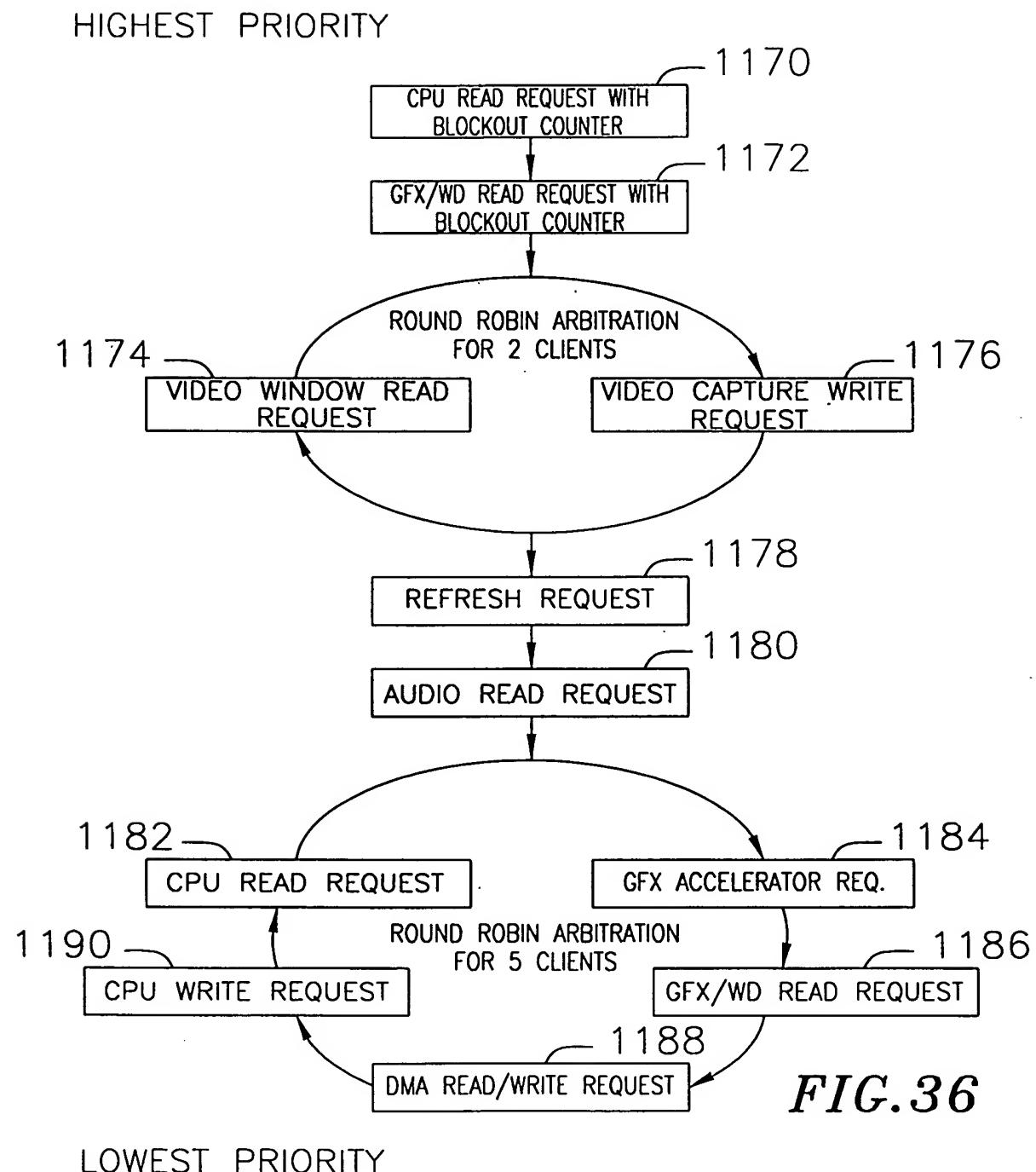


FIG. 35



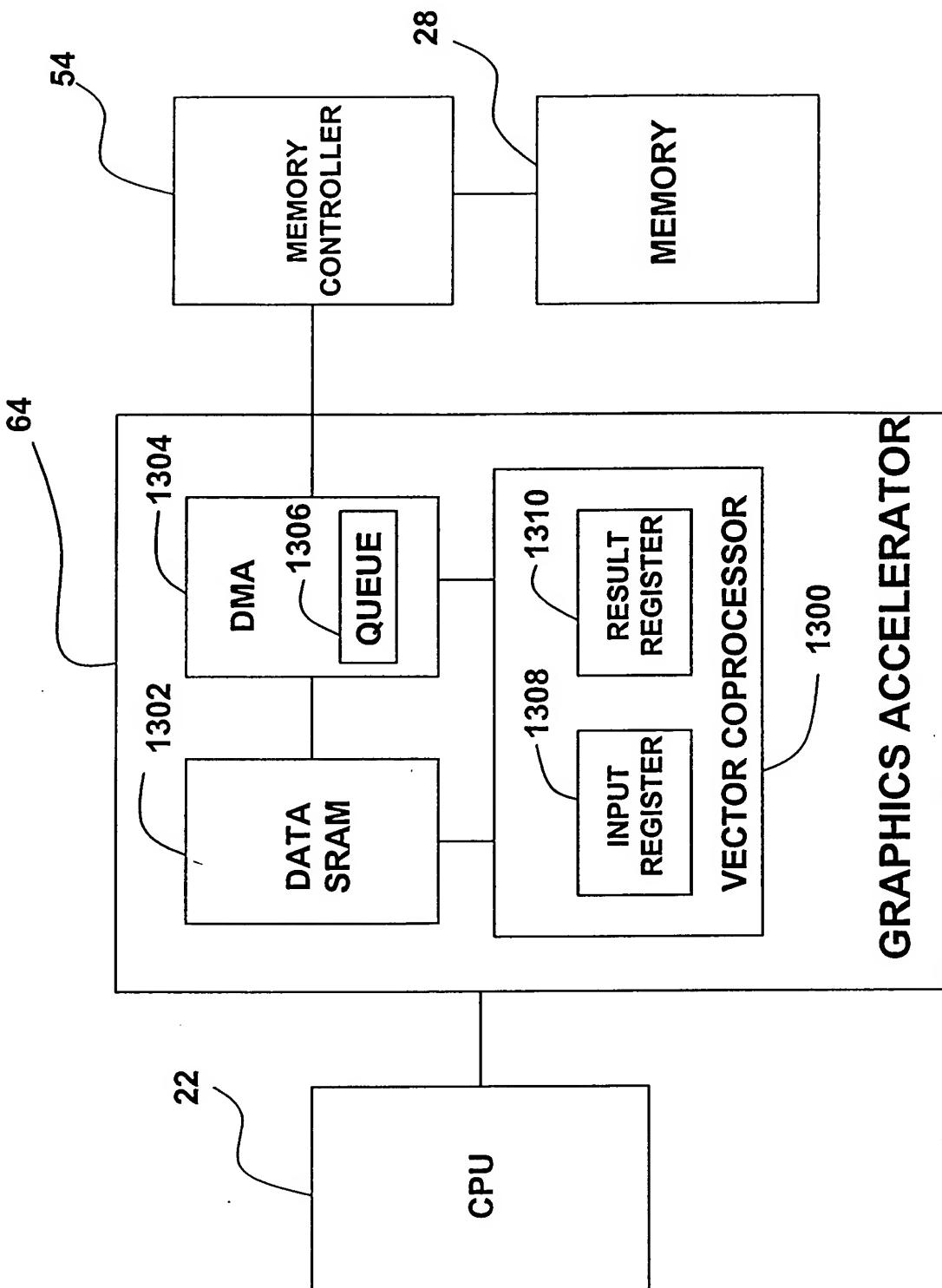


FIG. 37

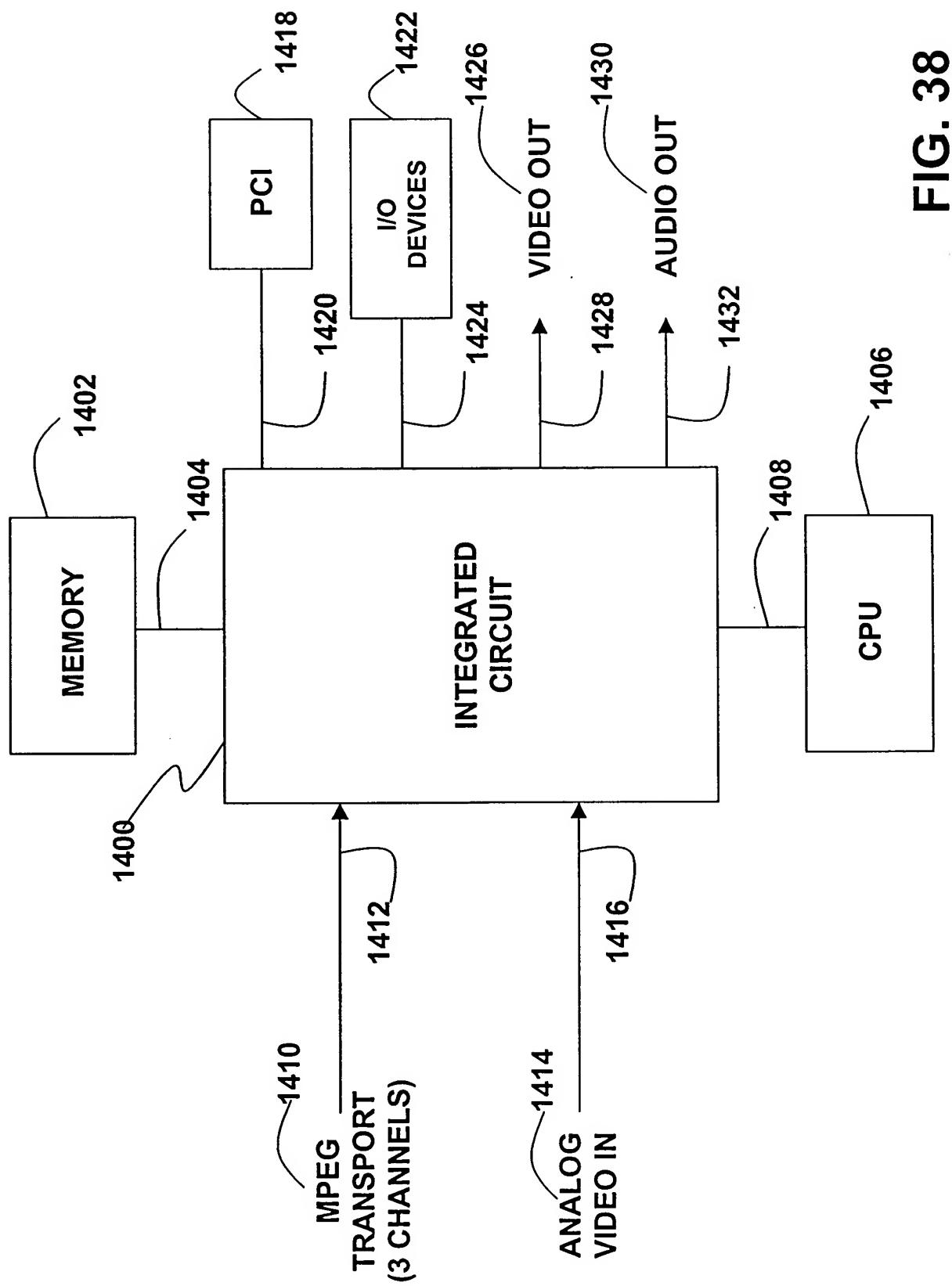


FIG. 38

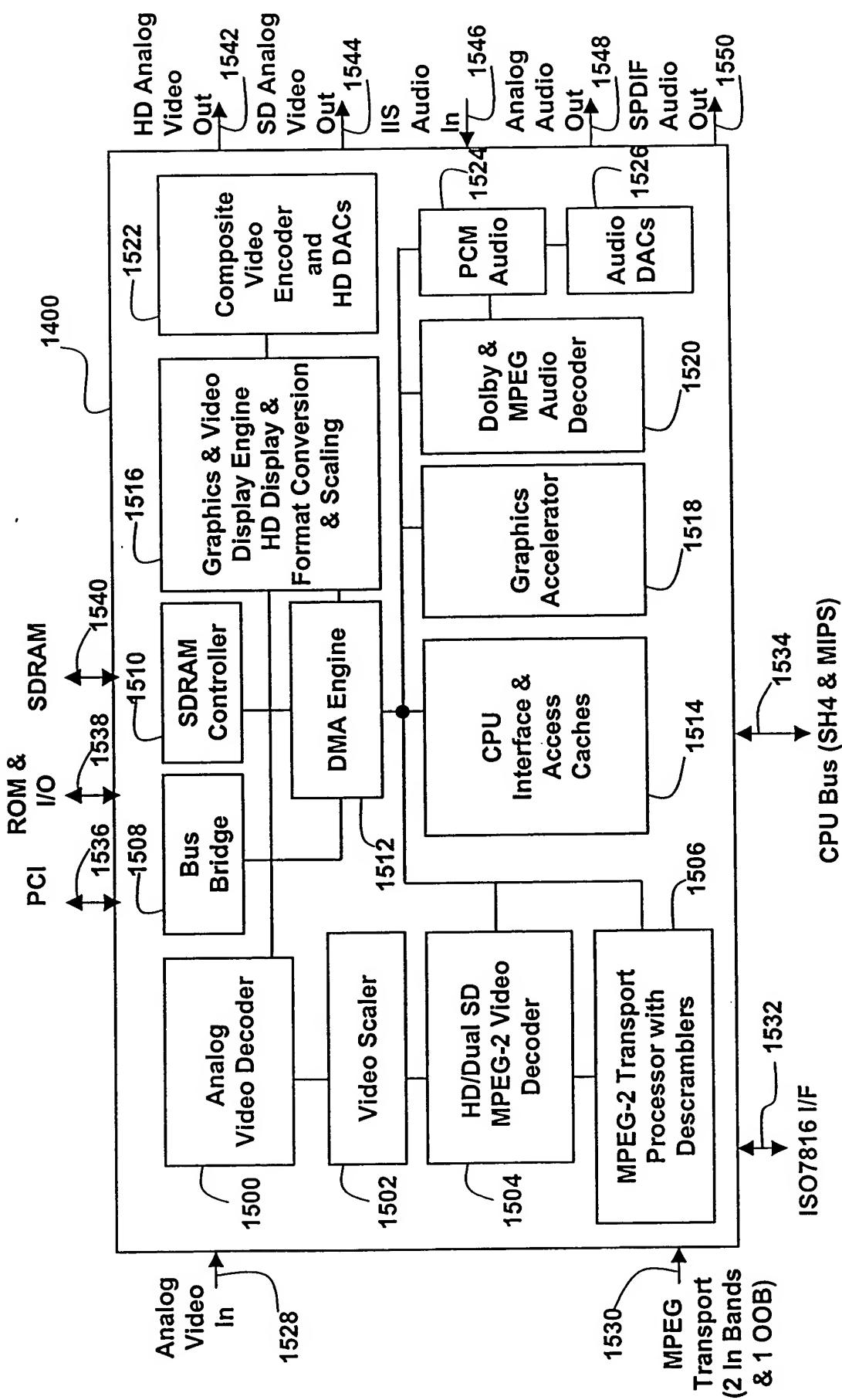


FIG. 39

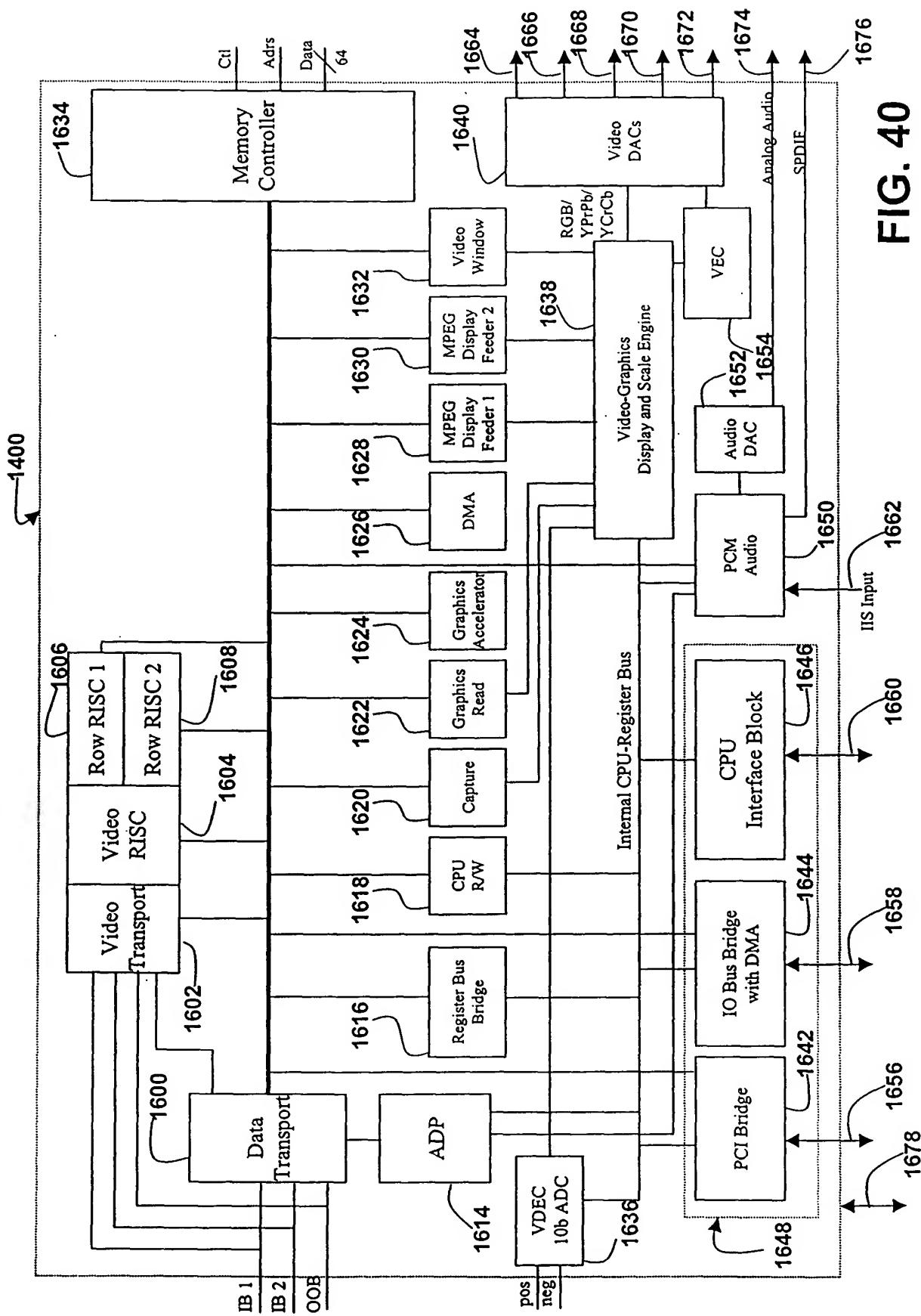
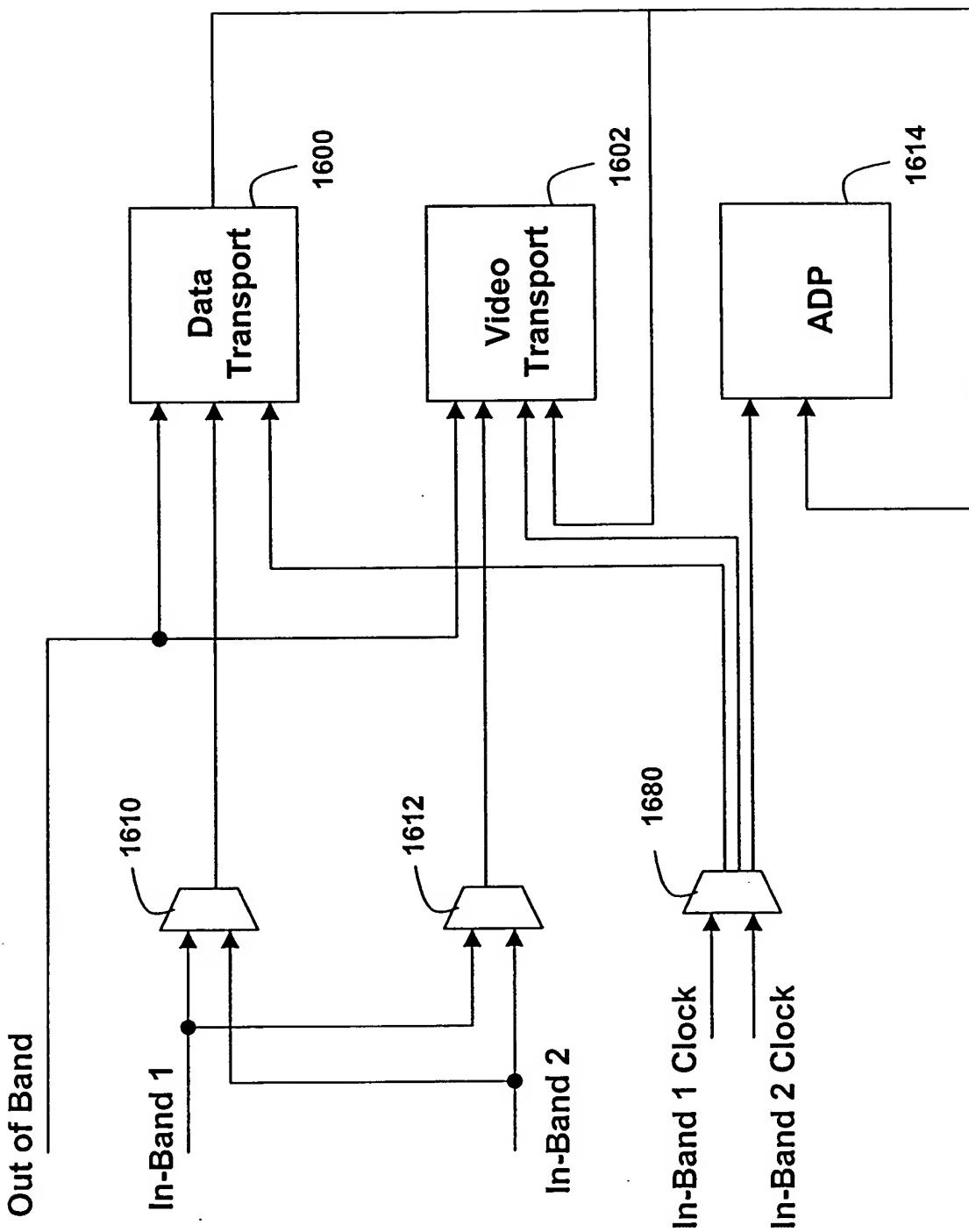


FIG. 40

FIG. 41



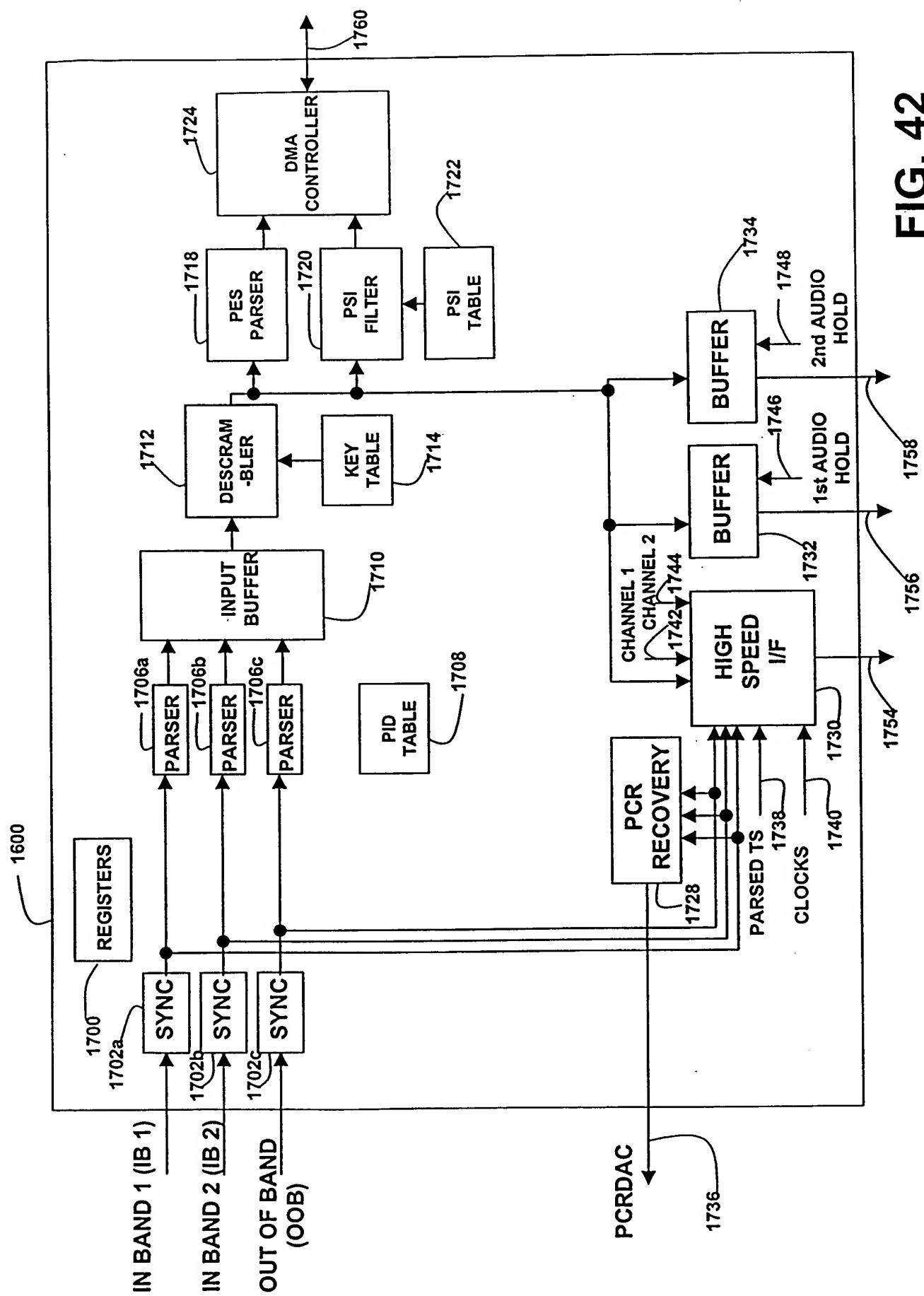


FIG. 42

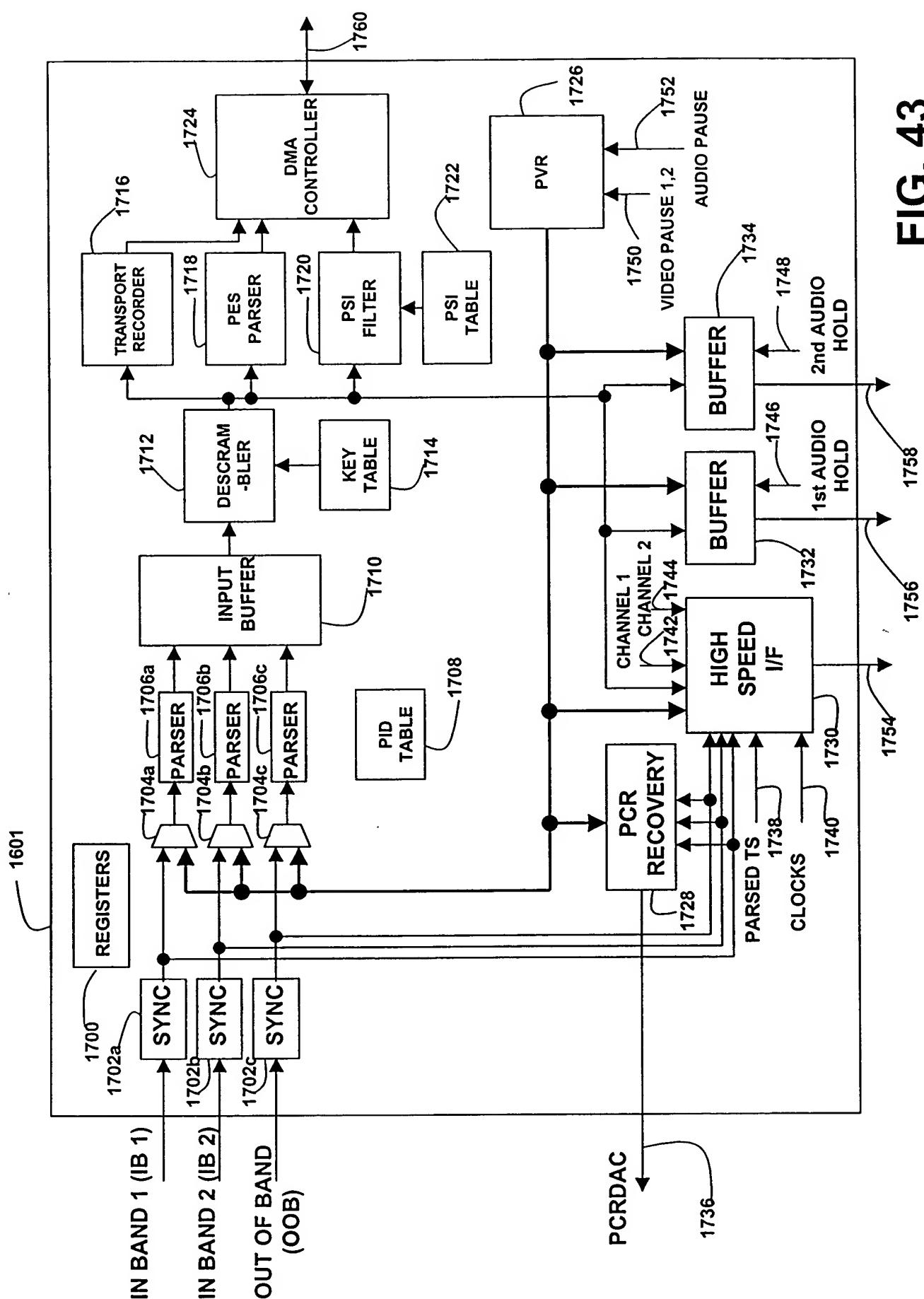


FIG. 43

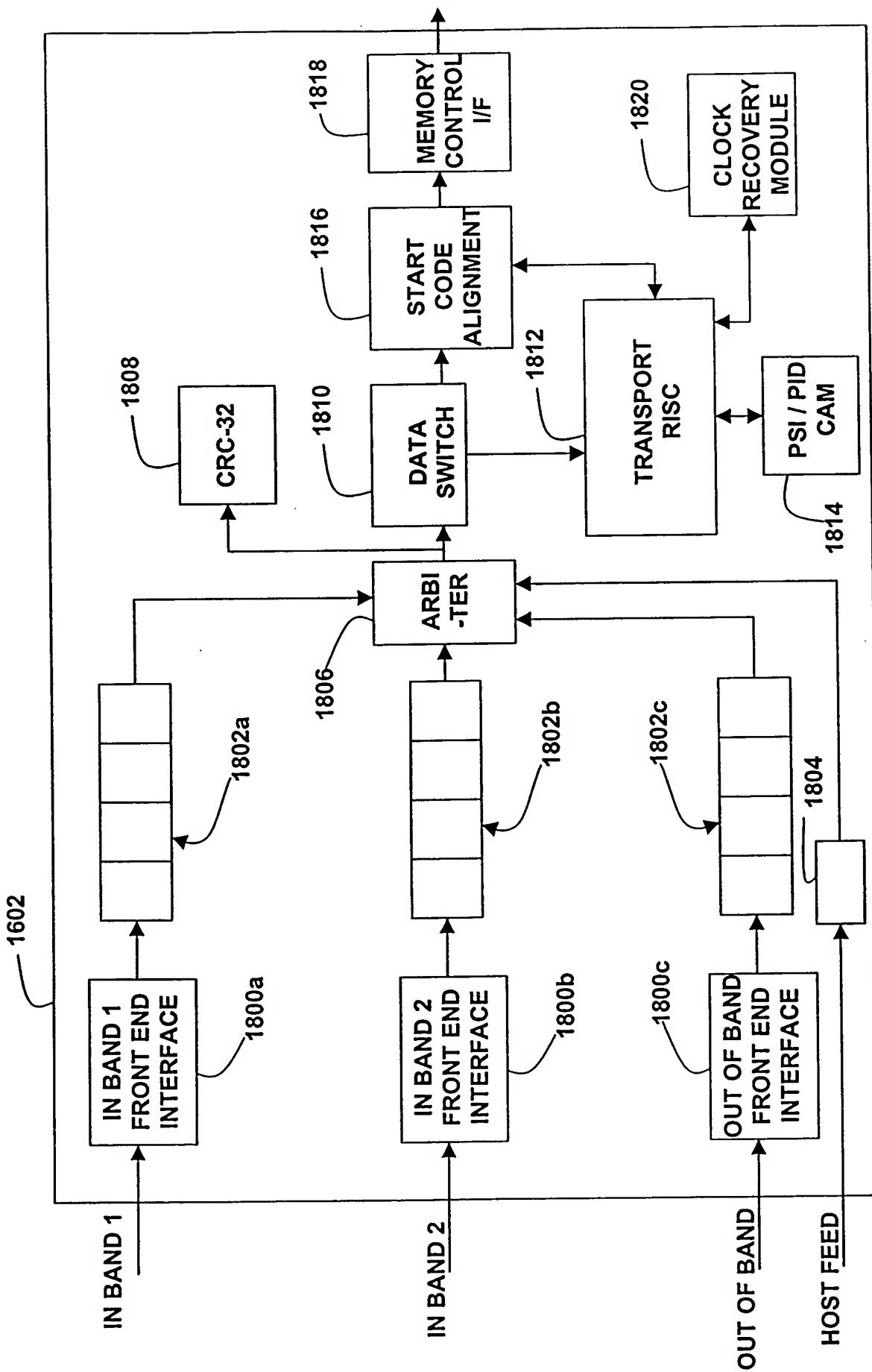


FIG. 44

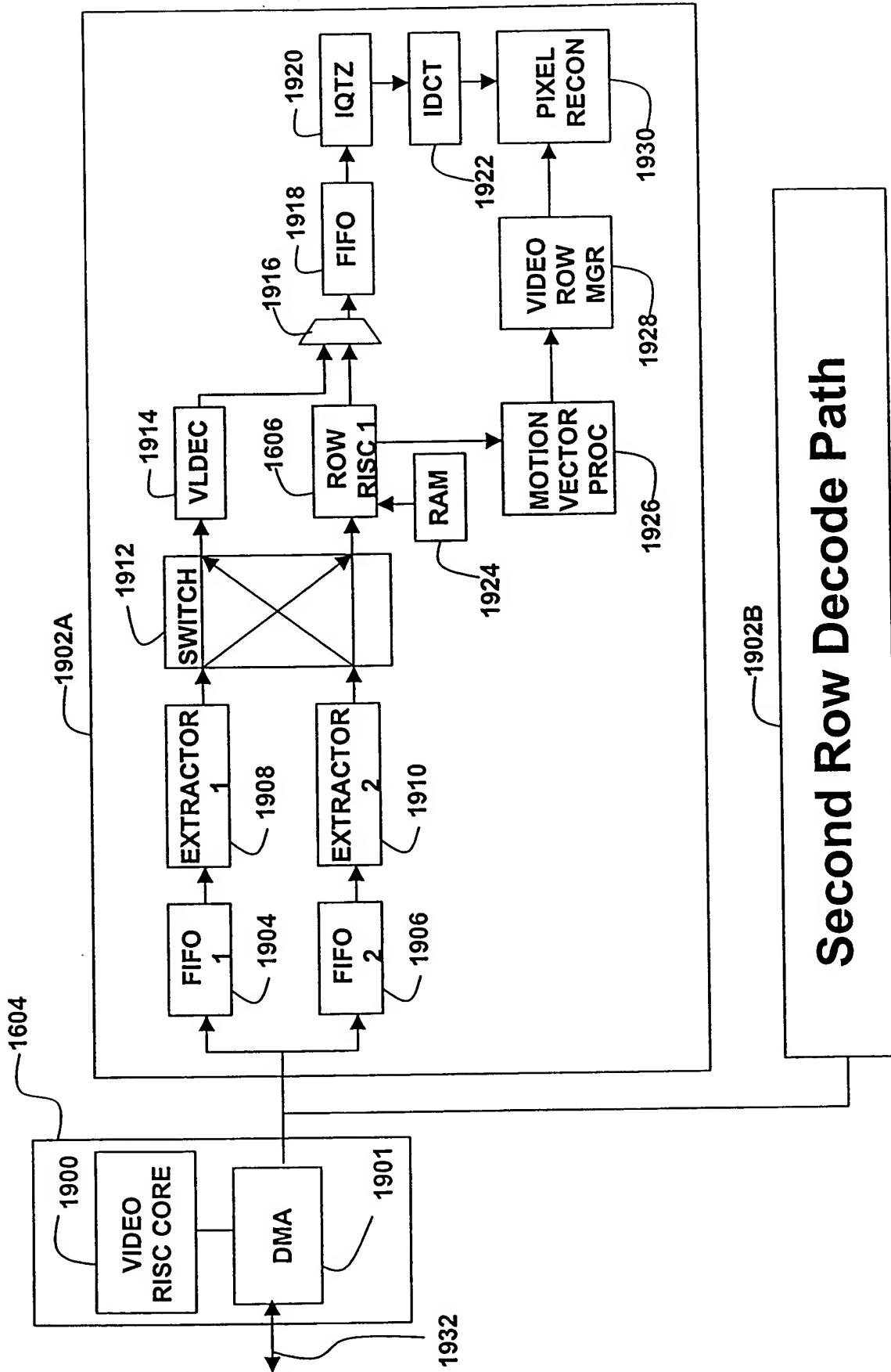


FIG. 45

Second Row Decode Path

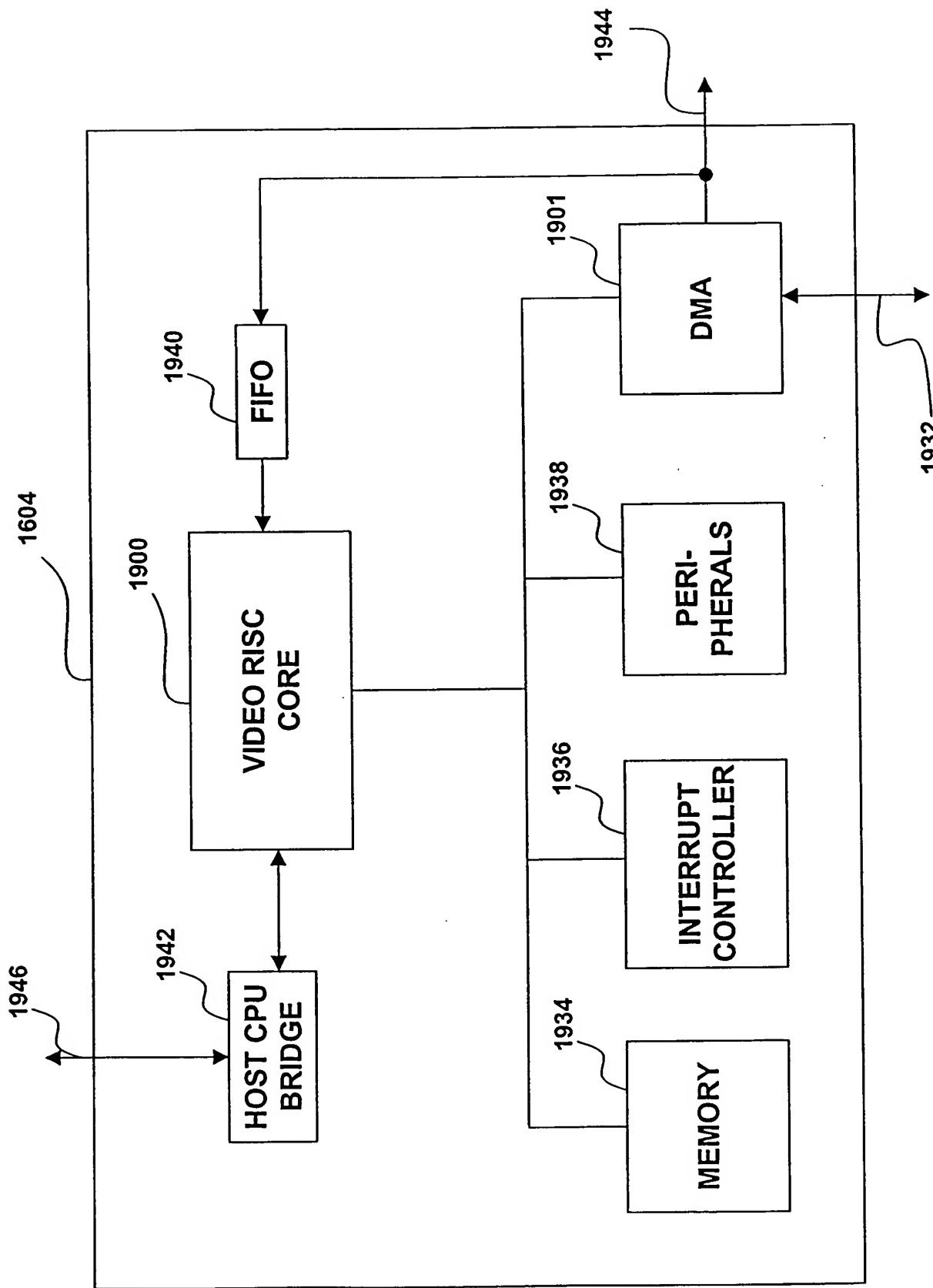
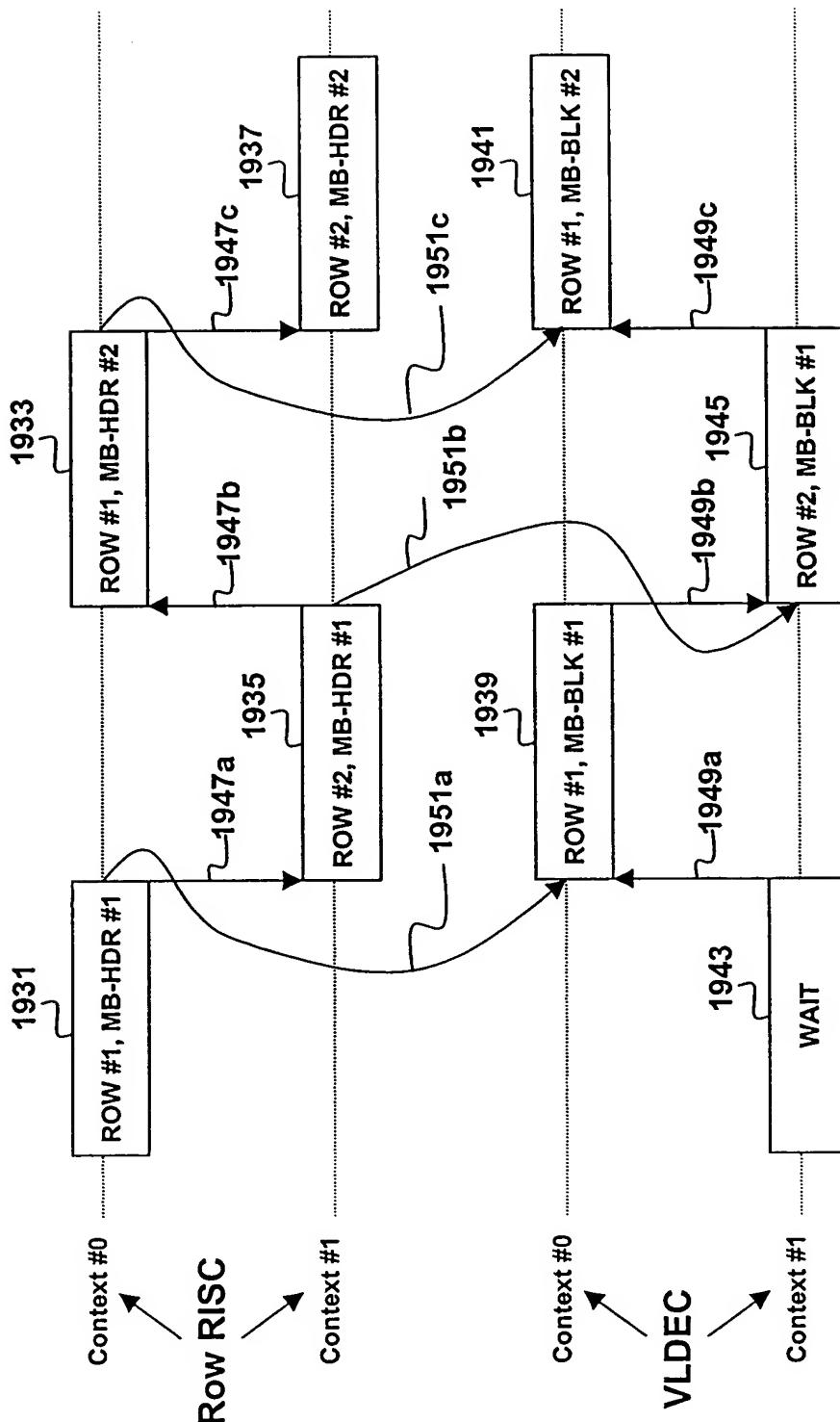


FIG. 46

FIG. 47



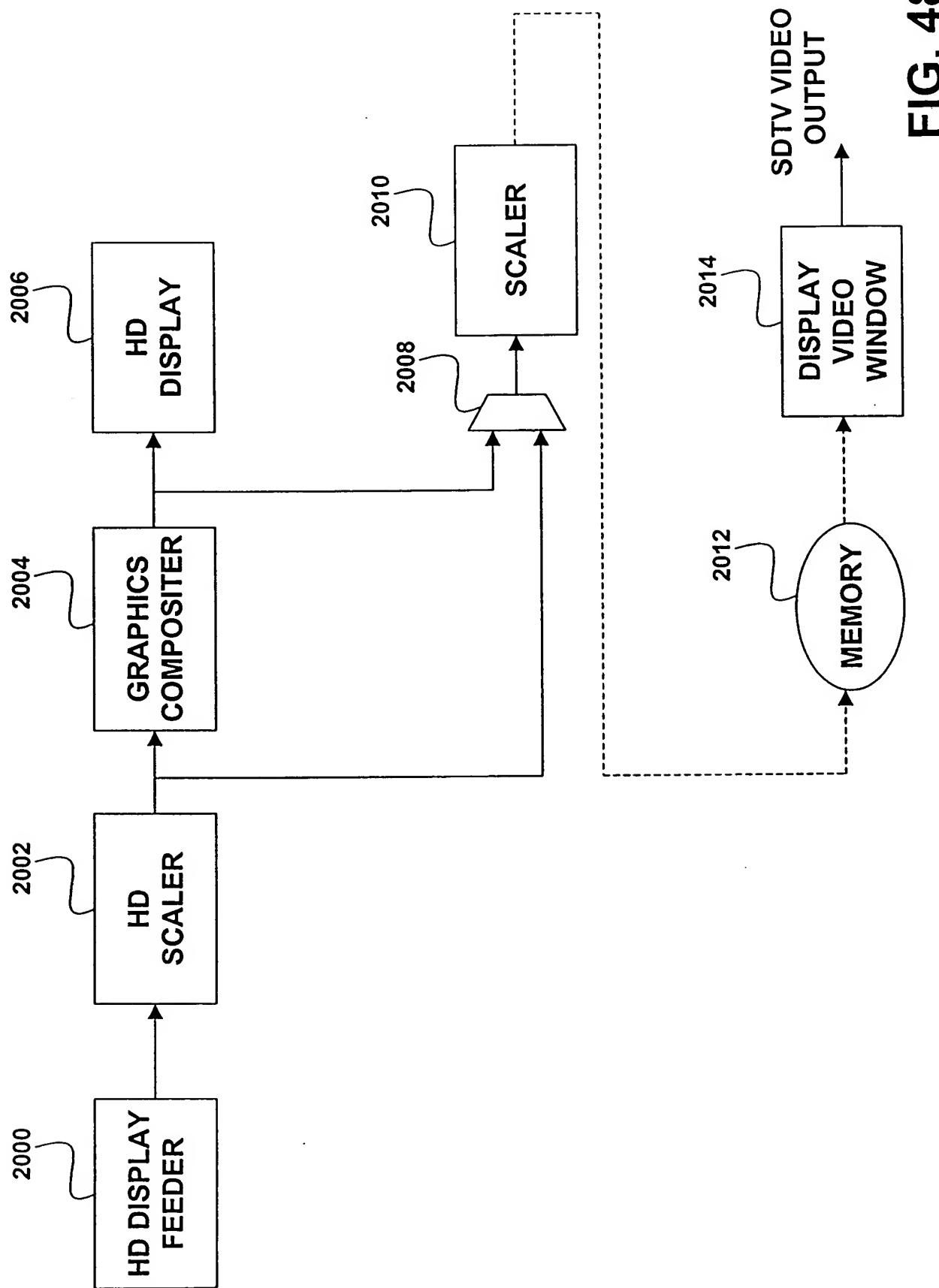


FIG. 48

MPEG VIDEO DECODING STAGES

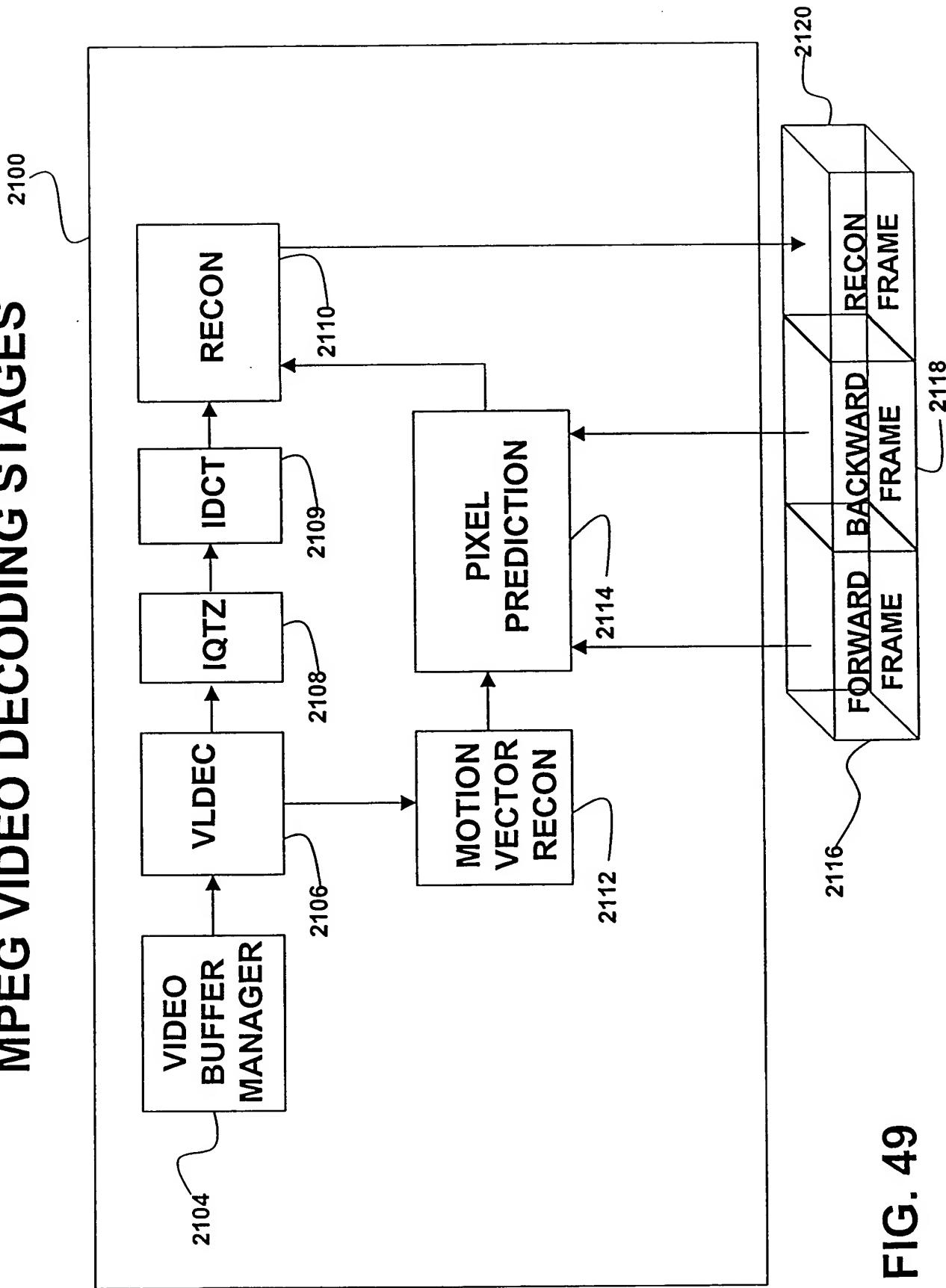


FIG. 49

MPEG VIDEO DECODING STAGES WITH VIDEO TIME DOWNSCALING

2102

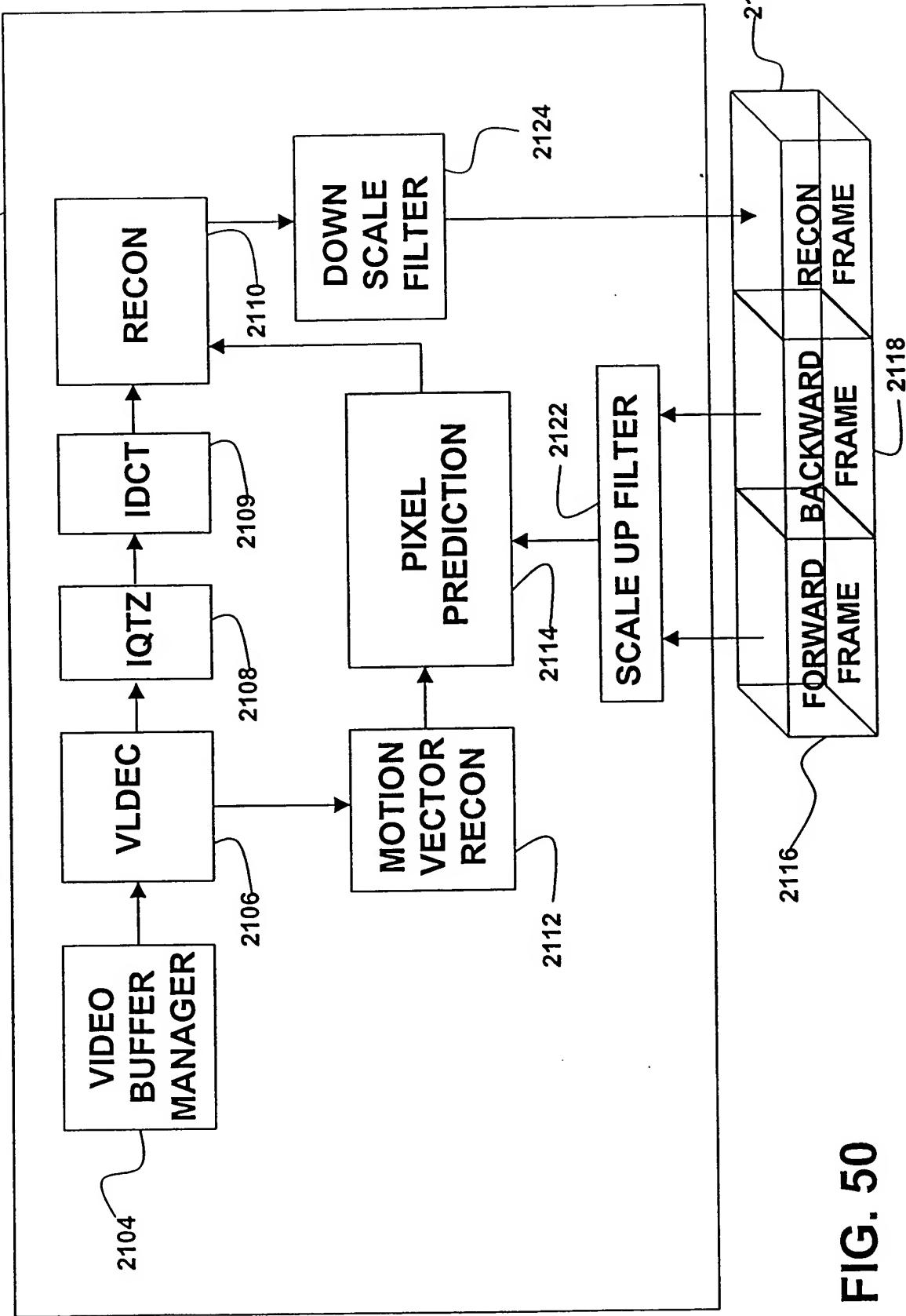


FIG. 50

FRAME-PREDICTION FOR I-PICTURES AND P-PICTURES

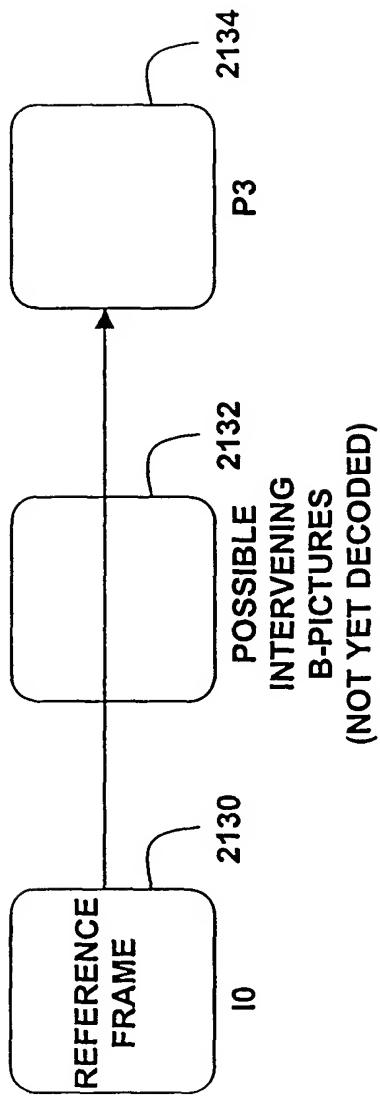


FIG. 51

FIELD PREDICTION IN A FRAME-PICTURE

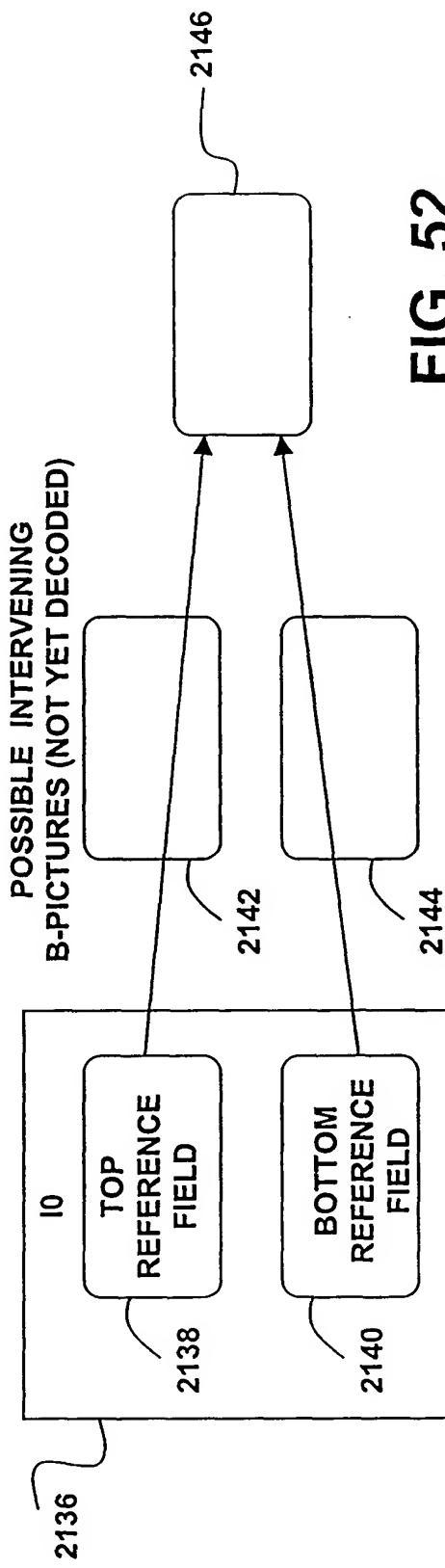


FIG. 52

PREDICTION OF THE FIRST FIELD-PICTURE

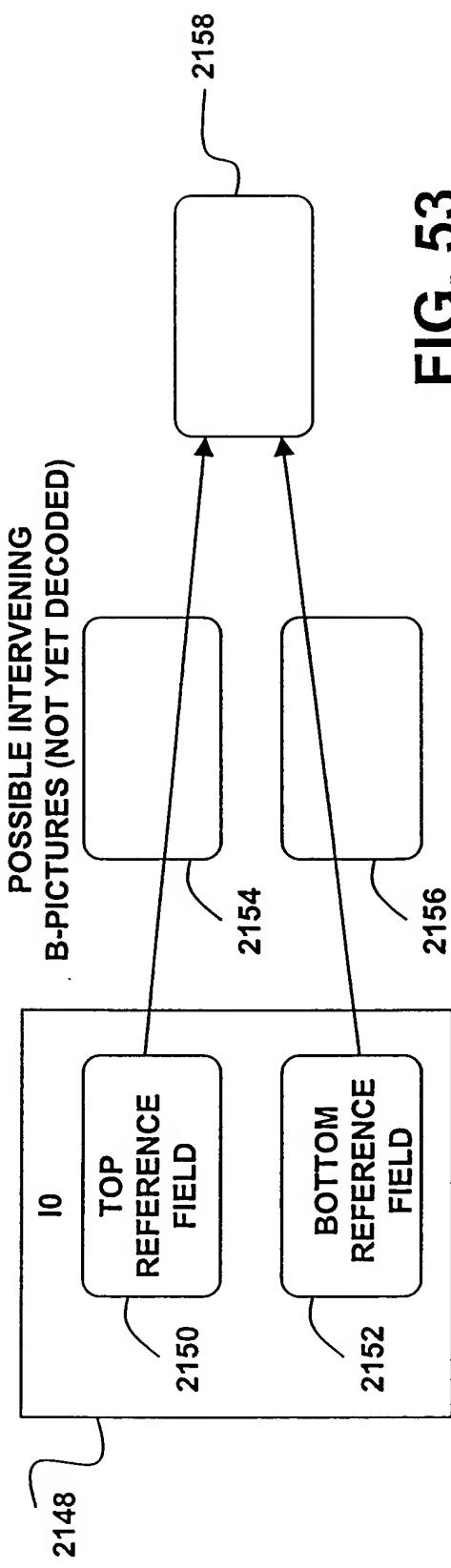


FIG. 53

PREDICTION OF THE "BOTTOM FIELD" SECOND FIELD-PICTURE

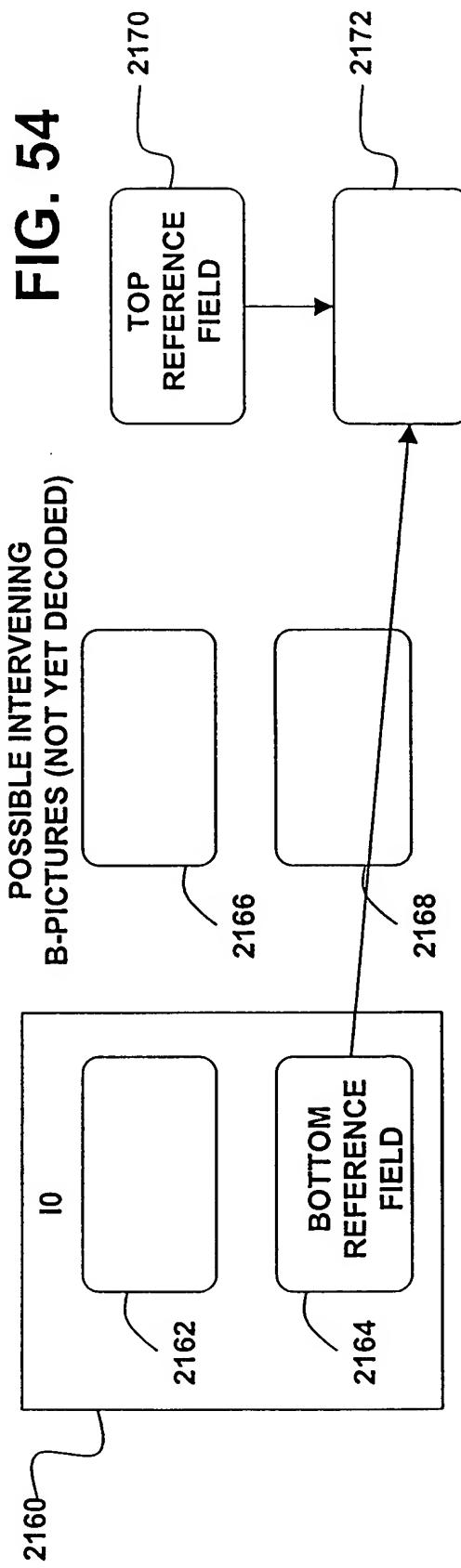


FIG. 54

PREDICTION OF THE “TOP FIELD” SECOND FIELD-PICTURE

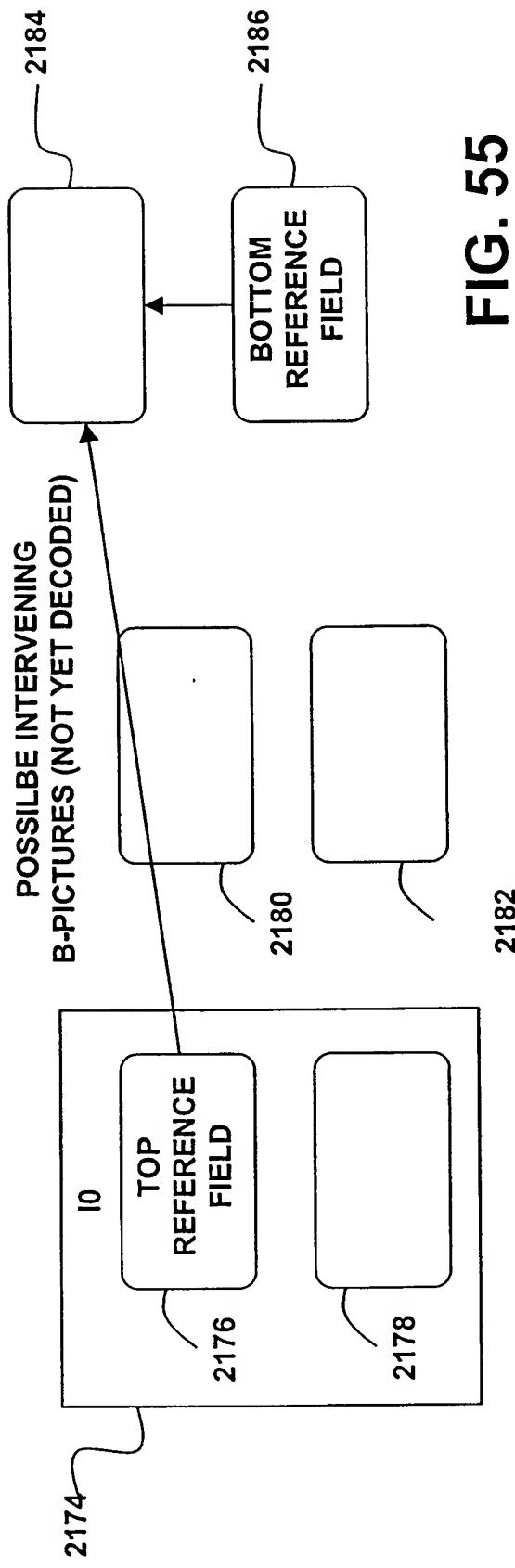


FIG. 55

FIELD-PREDICTION OF B FIELD PICTURES OR B FRAME PICTURES

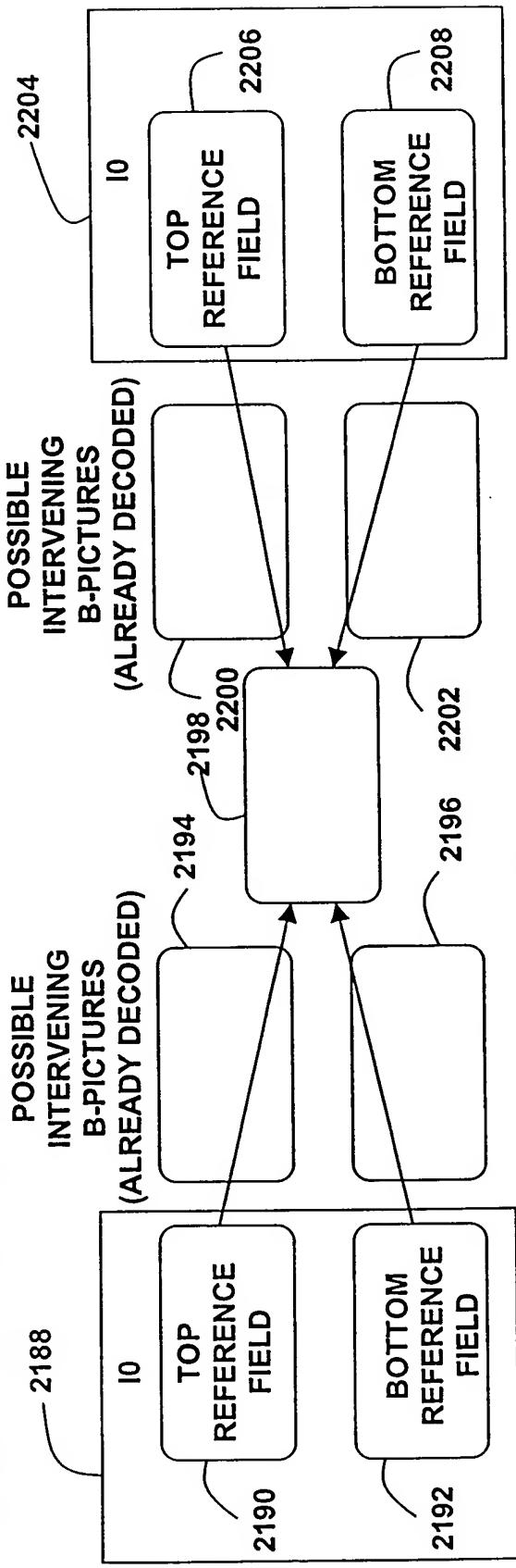


FIG. 56

FRAME-PREDICTIONS FOR B-PICTURES

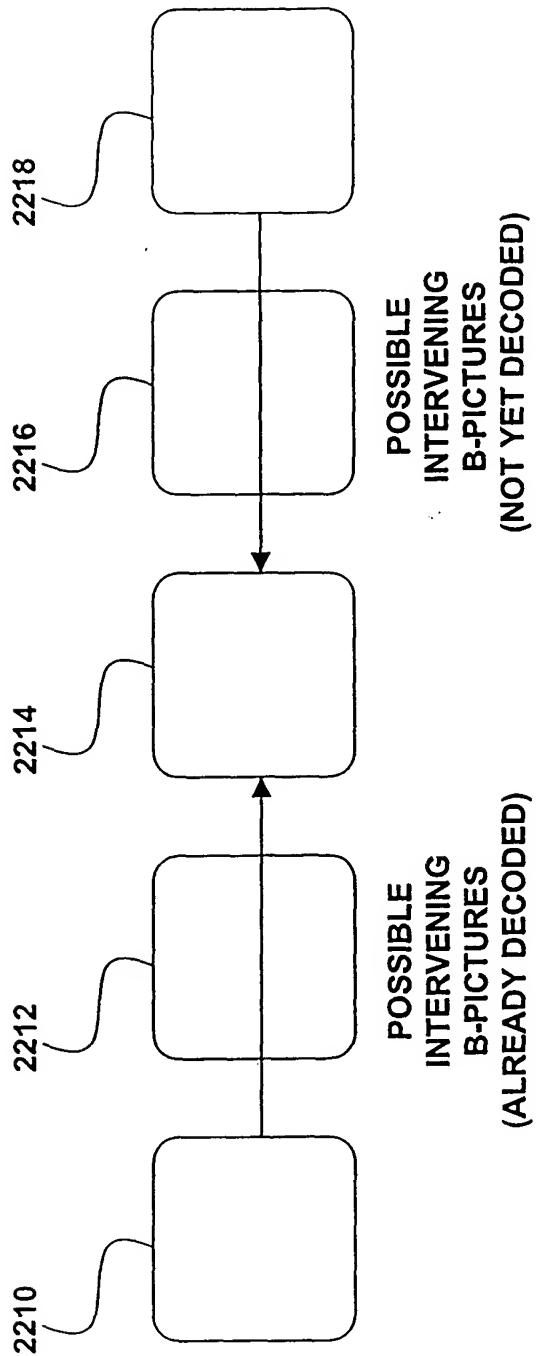


FIG. 57

Image Organization - 64 bit SDRAM

2250

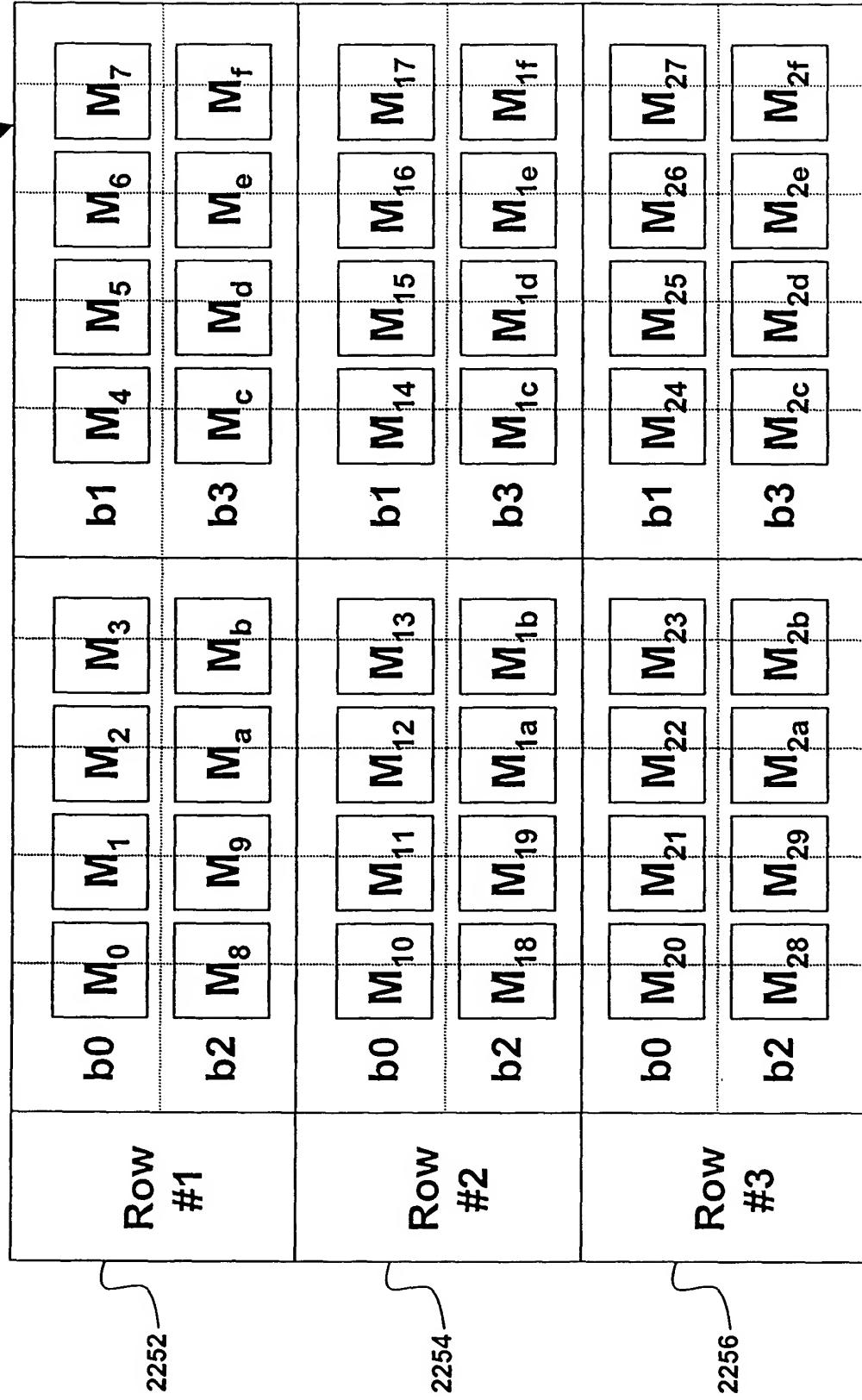


FIG. 58

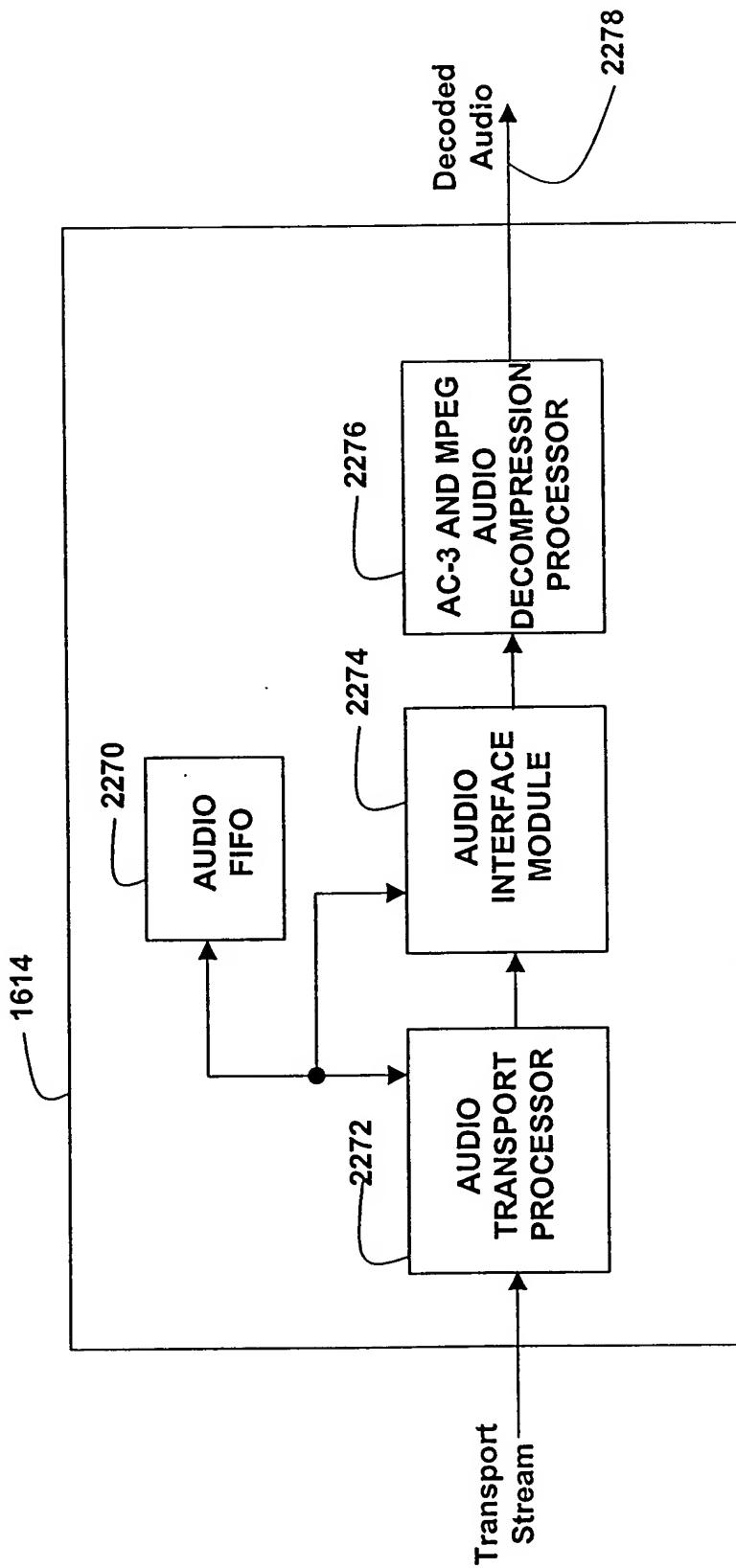


FIG. 59

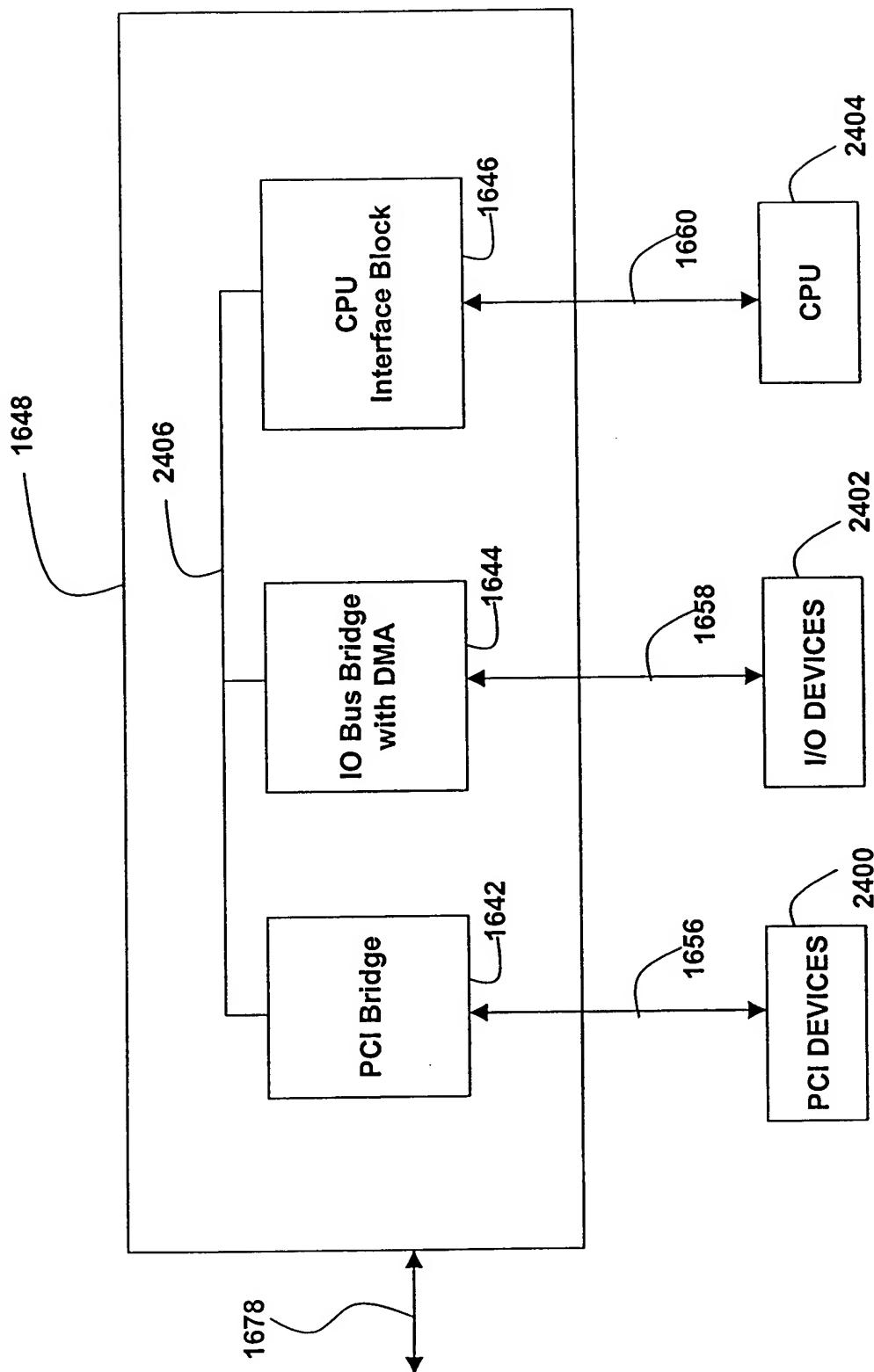


FIG. 60

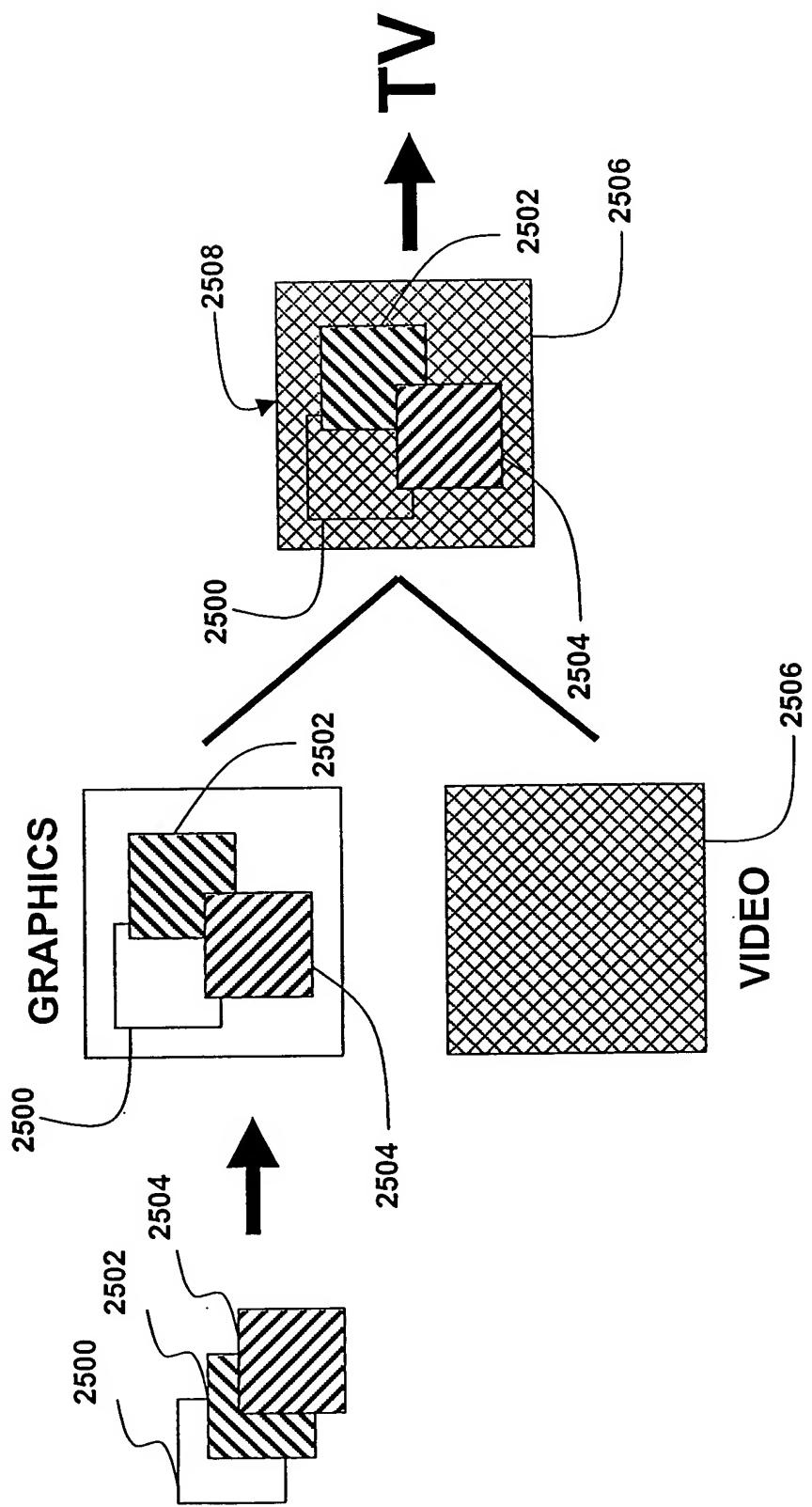
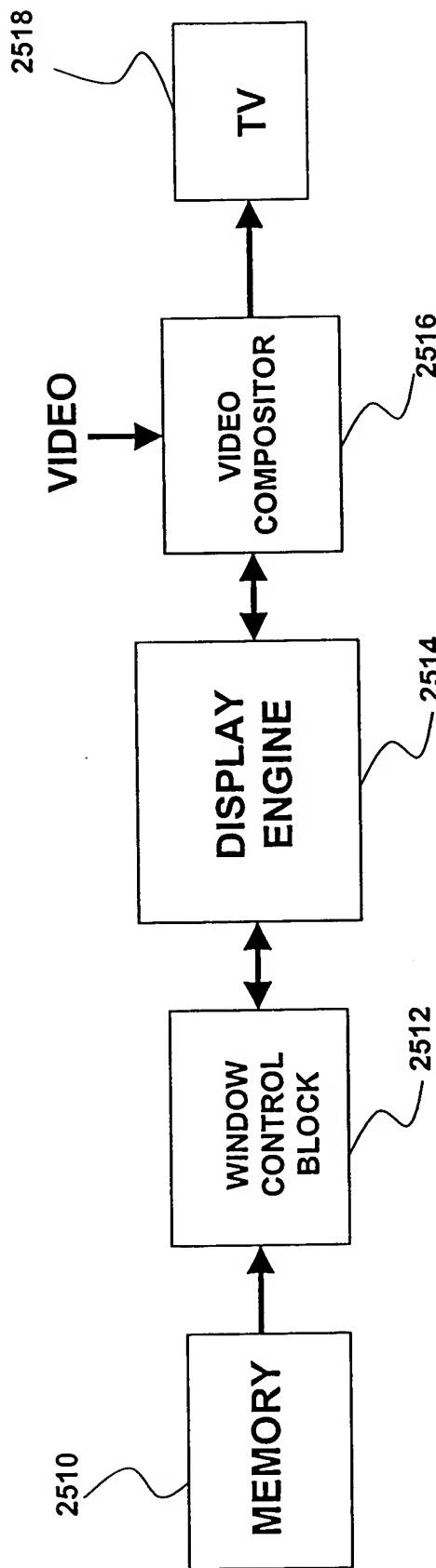


FIG. 61

FIG. 62



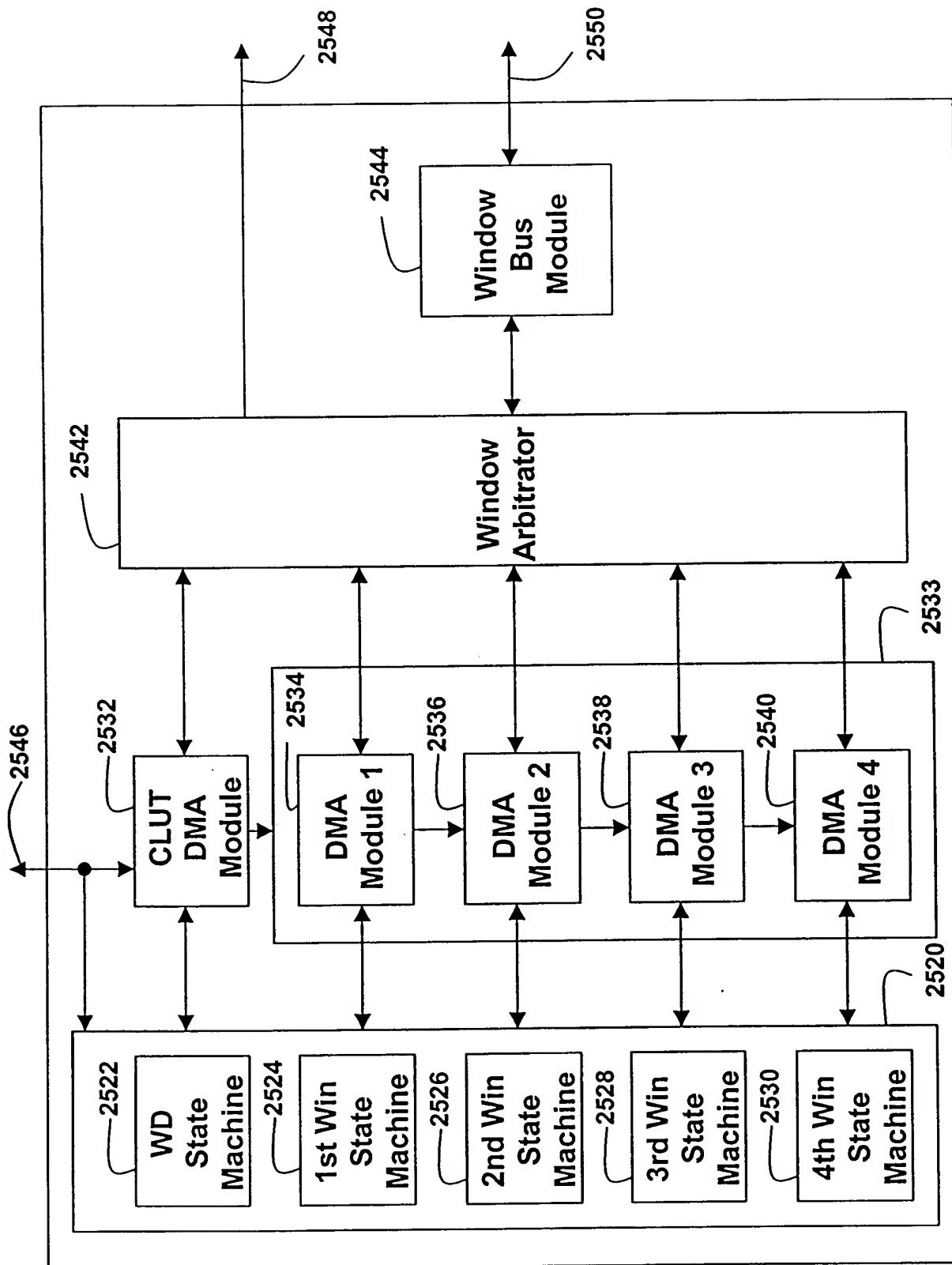


FIG. 63

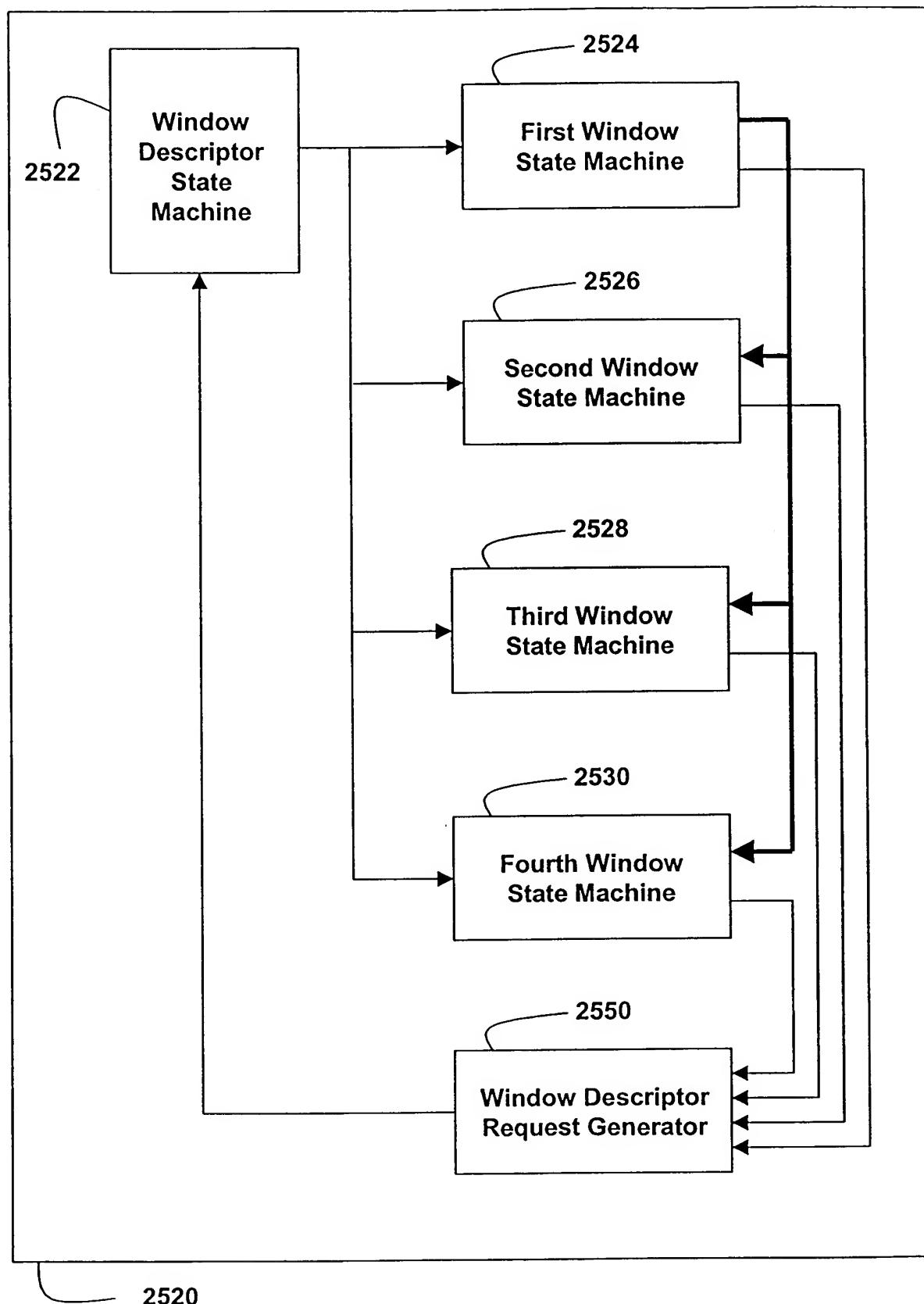


FIG. 64

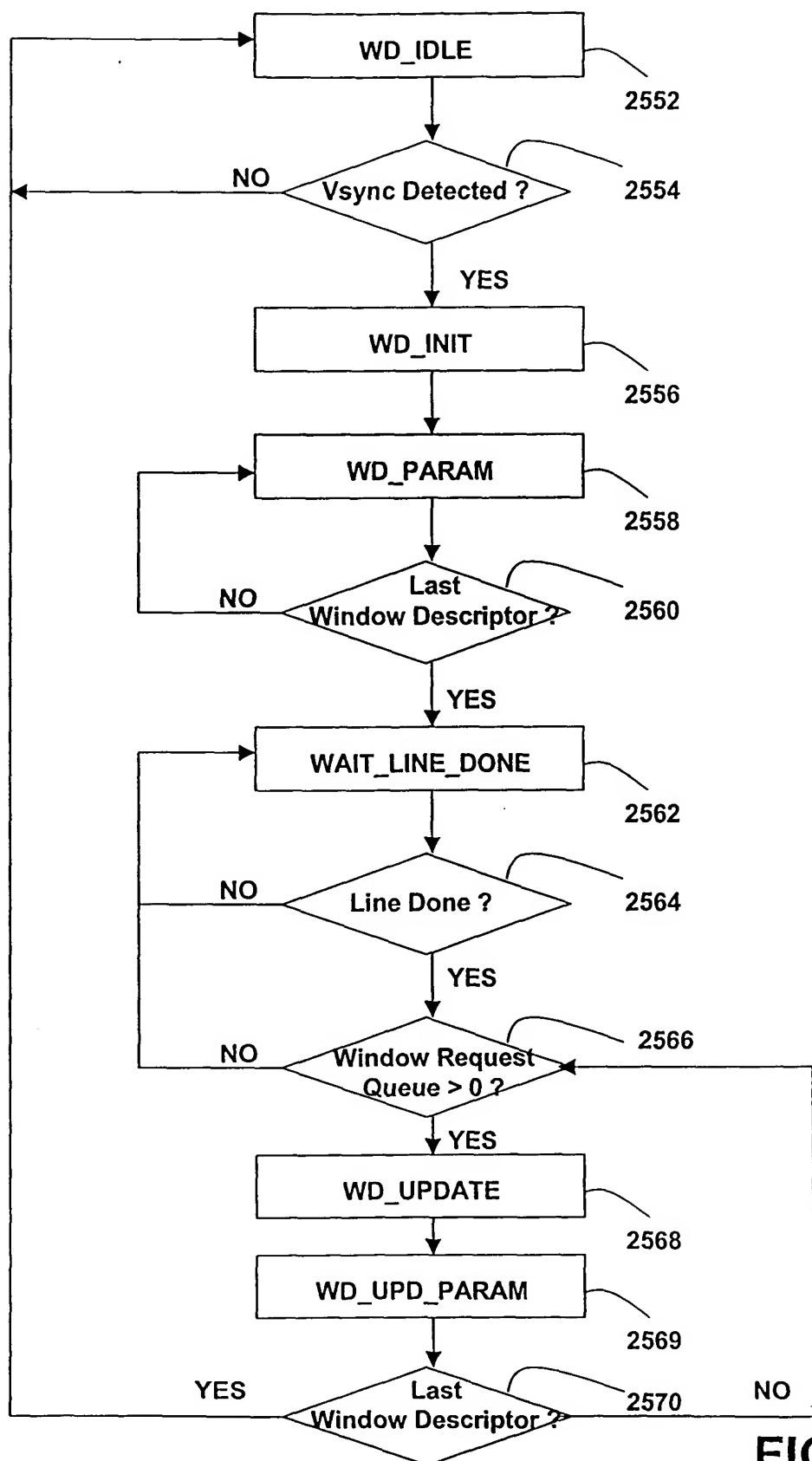


FIG. 65

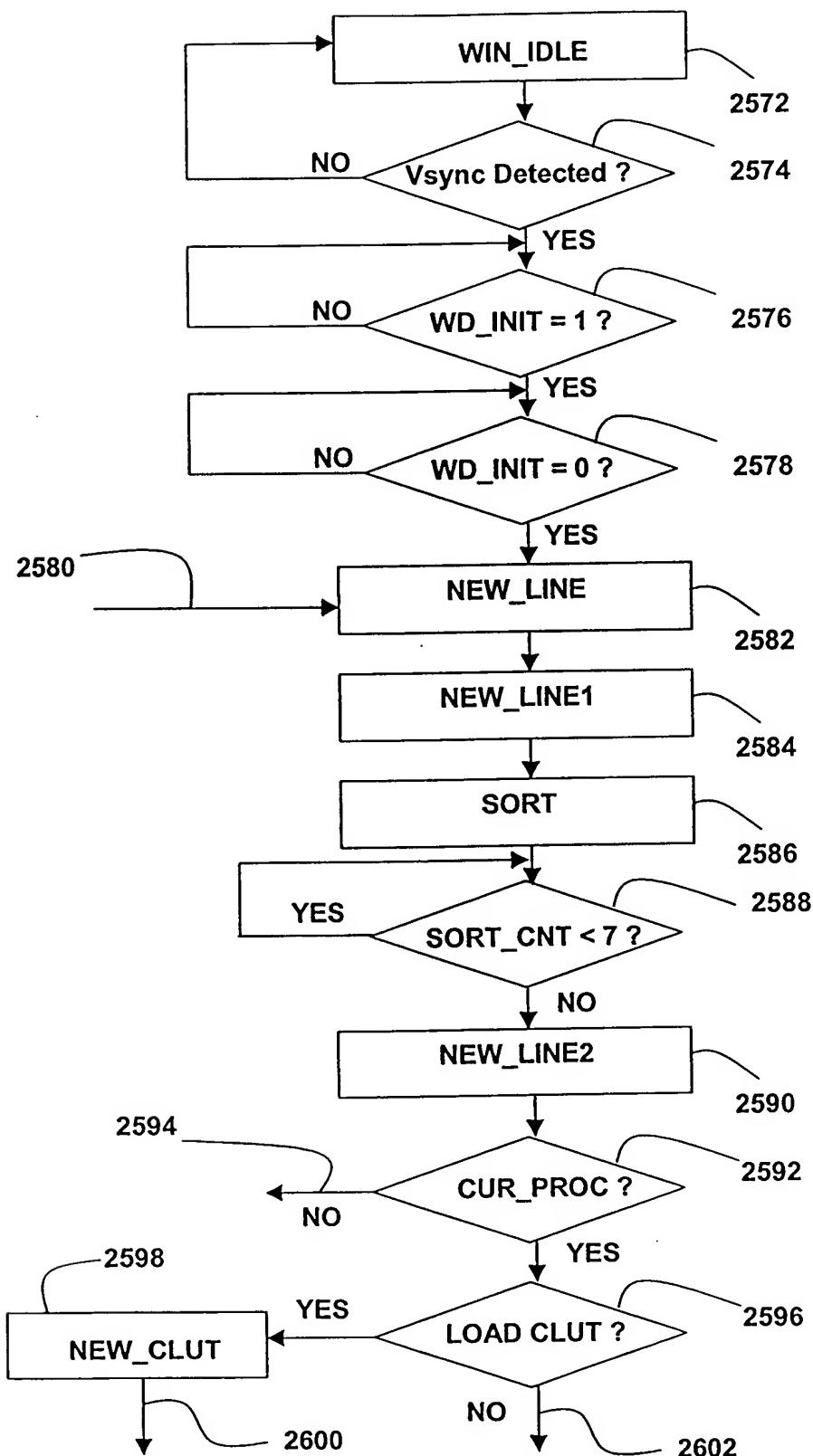


FIG. 66

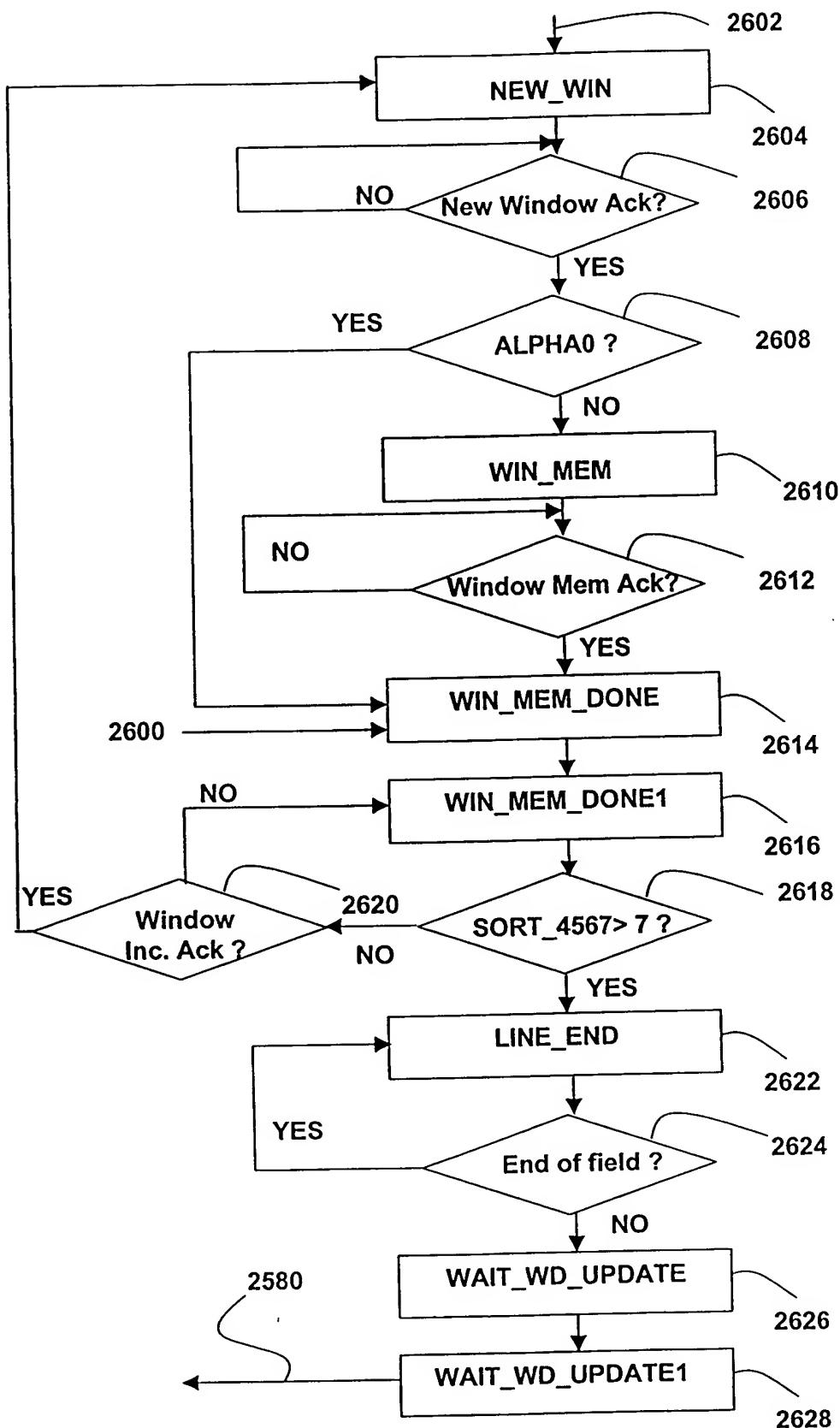


FIG. 67

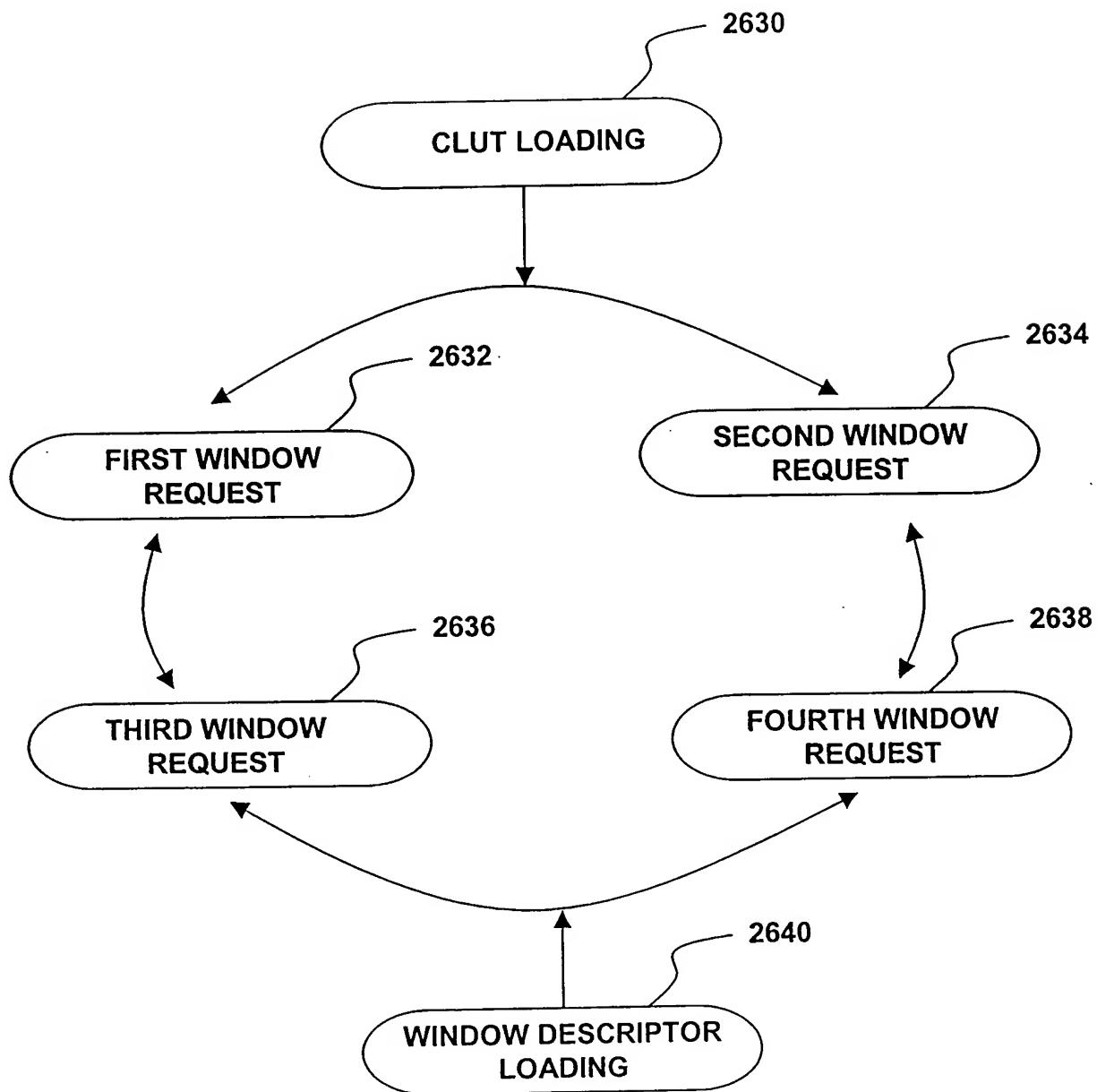


FIG. 68

DISPLAY ENGINE

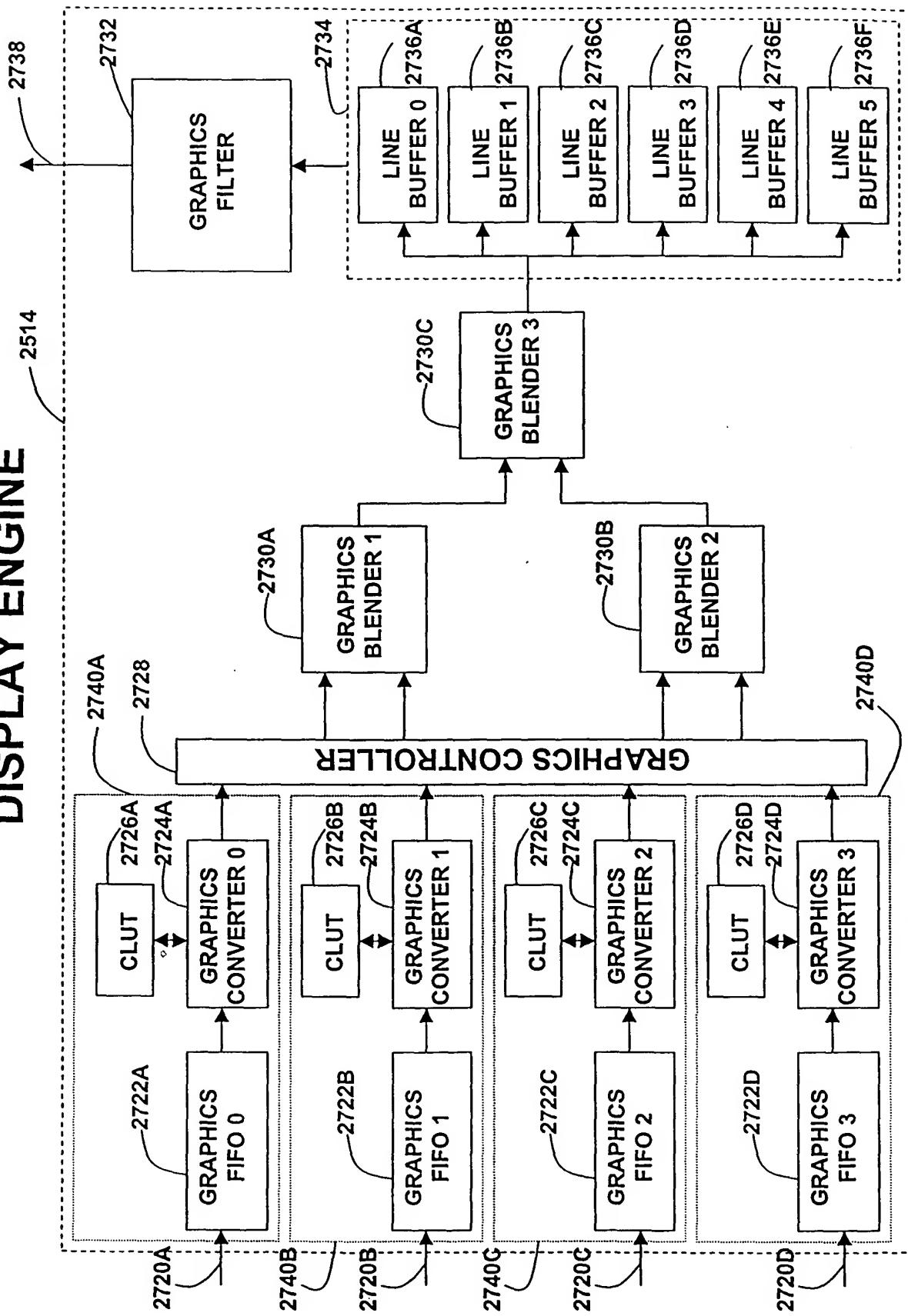


FIG. 69

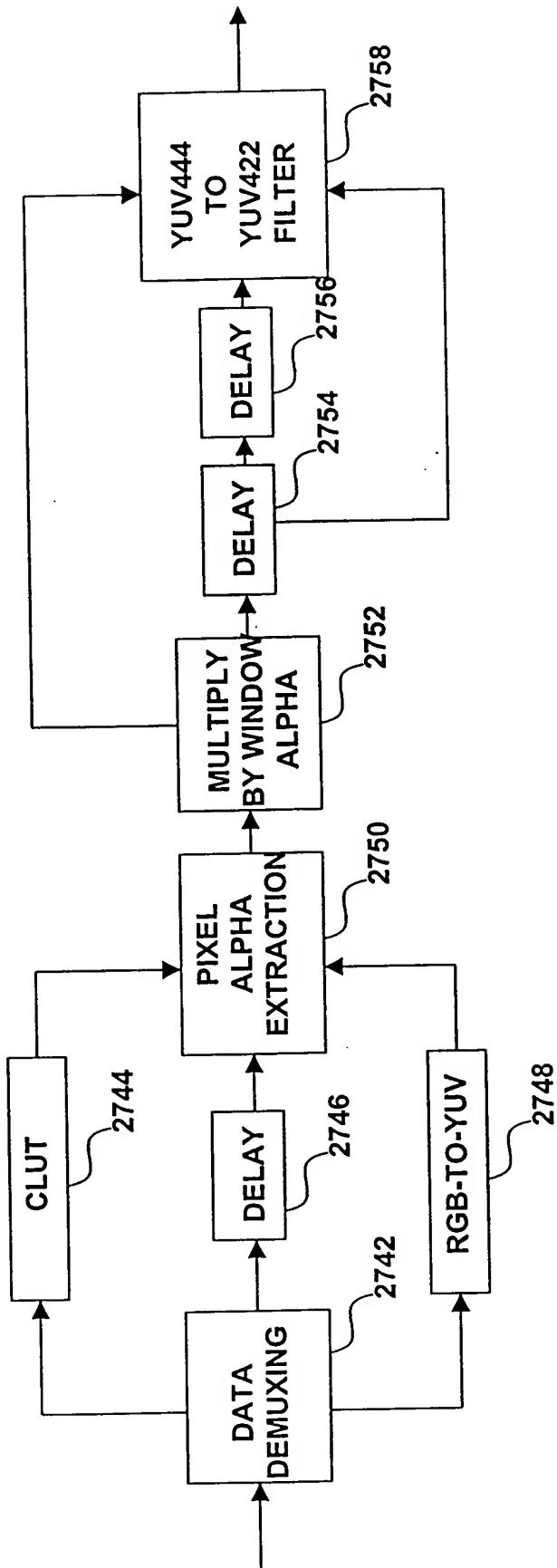


FIG. 70

FIG. 71

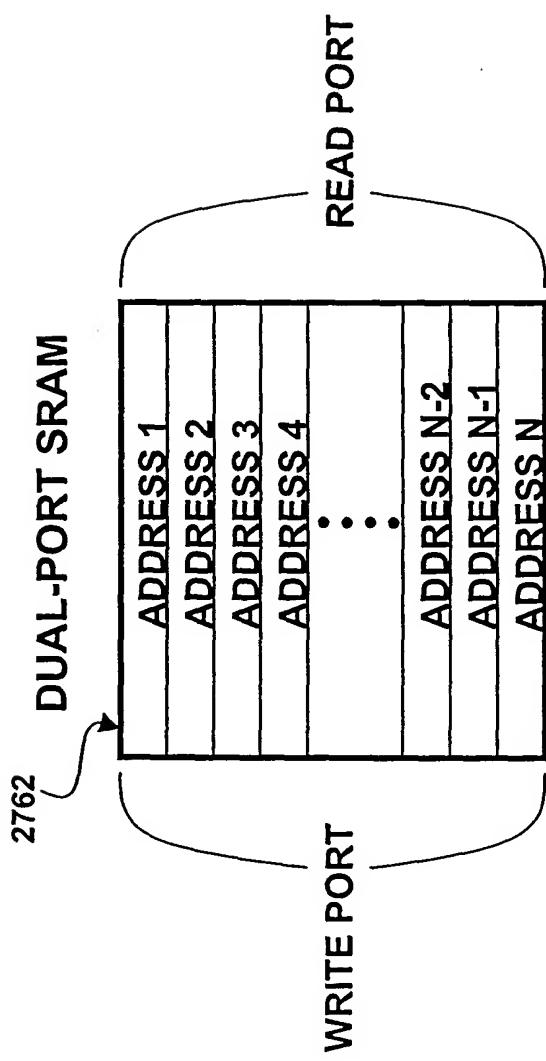
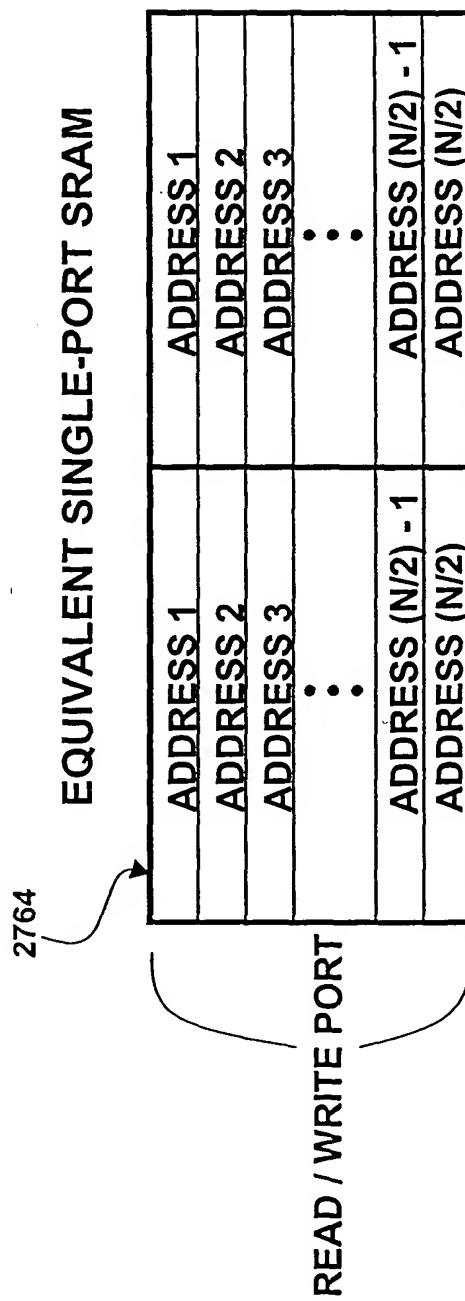


FIG. 72



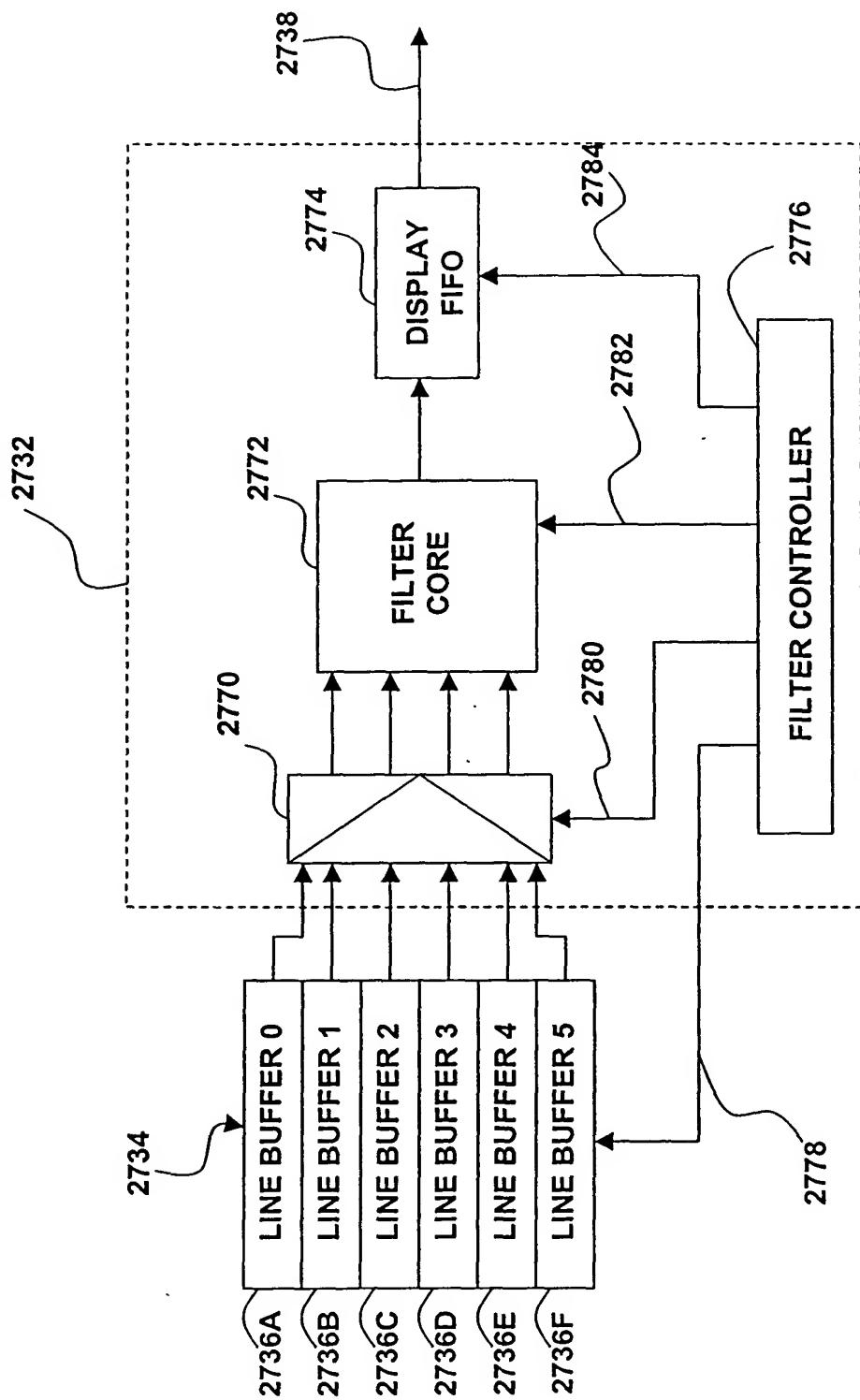


FIG. 73

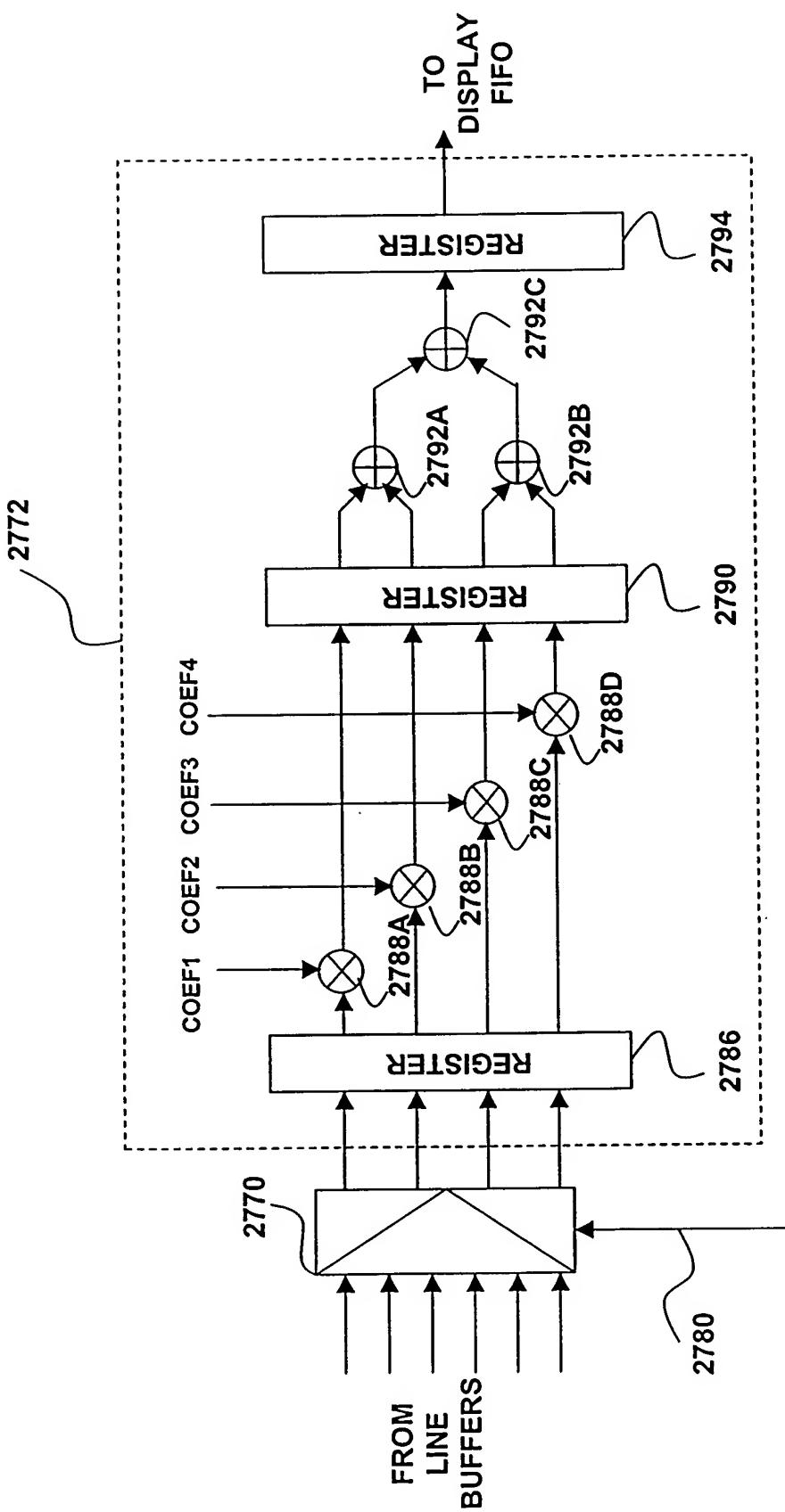


FIG. 74